#### **Errata**

# **Title & Document Type:** 6826A and 6827A Bipolar Power Supply / Amplifier Operating and Service Manual

#### Manual Part Number: 05950-1702

#### **Revision Date: January 1974**

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# OPERATING AND SERVISCES MANULAL

BIPOLAR POWER SUPPLY/AMPLIFIER MODELS 6826A AND 6827A

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### CERTIFICATION

D. H. S.S.

The Hewlett-Packard Campany certifies that this instrument wis thoroughly tested and inspected and found to meet its published specifications when it was shipped from the factory. The Hewlett-Packard Company further certifies that its calibration measurements, are traceable to the U.S. National Bureau of Standards to the extent allowed by the Bureau's calibration facility.

## WARRANTY AND ASSISTANCE

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For any assistance contact your, nearest Hewlett Packard Sales and Service Office. Addresses are provided at the back of this manual.

# BIPOLAR POWER SUPPLY/AMPLIFIER MODELS 6826A AND 6827A

OPERATING AND SERVICE MANUAL FOR SERIALS 1317A-00101 AND ABOVE

> \*FopSerials Above 1317A-00101 a change page may be included.

> > HEWLETT-PACKARD

HP Part No. 5950-1702,

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Printed: January; 1974

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VII.

#### ECTION GENERAL INFORMATION

#### DESCRIPTION

This instruction manual contains operating and Aurvice Instructions for tworBipgla Rower Supply/Amplifiers Models (1926A and 6827A) The Bipolar Power Supply Amplifiel (BPS/A) is a ganeral purpose instrument useful in wany laboratory engaged in the research and development of electionic systems, circuitly or companents. The BPS/A ing be operated as a power supply or as an amplifier. Ter of minals on the rear terminal stripthermy access to various in-ternal control points to turther expand the operational cop-abilities of the unit. The resulting flexibility lends the BPS/A to an almost unlimited number of applications, Some of these applications are outlined in Section []] of this manual. The following paragraphs describe some of the features of the BPS/A'as a sower supply and as an amplifier, a

#### POWER SUPPLY FEATURES 1-3

The unit can be made to function as a regulated dc 1.4 power supply by setting the front panel MODE switch to the POWER SUPPLY position, The supply can furnish either a Constant Voltage output or Constant Current output. The dc output is bi-polar and is continuously adjustable from its maximum rated positive value to an equal negative continuously through zero. A crossover feature automatically changes the supply from constant voltage to constant current operation at a preset or programmed voltage/current, point. The front panel CURRENT MODE indicator lights for constant current operation. Both the supply and the load are protected against overvoltage and overcurrent conditions by internal circuits. Dual output voltage ranges are provided for better resolution. The front panel RANGE switch allows selection of the high (X10) or low (X1) output range

The output voltage can be programmed locally 1.5 using the front panel VOLTAGE control, or remotely, by means of a resistance connected to the appropriate real terminals. The output correct can be programmed locally using the front panel CURRENT control, or remotely, by means of a resistance or voltage source connected to the appropriate rear terminals. The BPS/A can be programmed (controlled) at a very high rate of speed (less than 50 used for output voltage change over the entire voltage span). Local and remote programming connections are described in Section III. The output voltage and current ranges are as follows:

Model 6826A - 54 to 354 at 0 to 1.0A (low range) +50V to +50V at 0 to 1.0A (high rande Model 6827A: (-10V to +10V at 0,18 0.5A (low range) -100V to HOOV at 0 to 0.5A Intoh range &

The BPS/A con sink as well as source current per mitting it to serve as a variable load device. The BPS/A can sink-up to 50% of the rated output cur

### AMPLIFIER FEATURES

He unit can be tride to function as a valiable gain of a fixed gain amplifier by setung the MODE switch to the WAR GAIN AMP or FXO GAIN AMP position. When open ating as an amplifier the BPS/A can amplify externally applieduc or de signals. Variable gain can be controlled locally tyOLTAGE control ar remately and is accorded to 2. The variable or fixed gan provided is as follows: ( Model 6826A: Variable Galak, 0.2 flow range) [02]

(bigh range) (low range) 10X Fixed Gain mitange) 0-4 (low range), 0-40 Variable Gain (high: range) 2X (low range), 20X

Fixed Gain, (high, range)

The veriable gain amplifier is non inverting and has a frequency response from dc to 15kHz. The fixed gain amplifier is inverting and has a frequency response from dc to 40kHz (6826A) or from dc to 30kHz (6827A) . Total

#### METERS 1-10

Model.6827A

1.0

A voltmeter and an ammeter on the front-panel 1-11 monitor the ac of dc output voltage and current respective ly. Associated front partel VOLTAGE METER and CUR-RENT METER switches, allow the meters to monitor either an ac or de output and also provide dual range monitoring capability for better resolution. The dc meter accuracy, is ±3% of full scale and the ac meter accuracy is ±5% of full scale.

#### SPECIFICATIONS 1-12

harmonic distortion is 0.1% (maximum).

Detailed specifications for the two models are 1-13 given in Table 1-1.

# Table 1-1: Specifications Models 68264 and 6837A

#### \*NOTE N V to all models unless otherwise specified ecífications a GENERAL SPECIFICATIONS DC Output (Conlinued): Model 6827.A: ₩1 Ringe: \$10V to +48V, 0 to 05A 7×10 Ringe: -100V to +100V, 0 to 05A Input Power Mouell.6826AV Adden 24/208-254Vac (switchable) 48.63H2: 1.DA, 130W 104,127/208-254Vac (switchable). Load Affect (Load Regulation): 4 48-63Hz, 1.2A. 150WA 10 1 Voltage load offect is given for a load ourrent change equal to the current tating of the supply. Barkent load Meters: effect, & given for a load voltage change equal to the odividual voltage and current meters . DO activacy is voltage rating of the supply? Model 6826A: 1 of full scale. AC accuracy is 5% full scale with sinfi I. 100Hz input Voltage (X1, Range): 0.01%+.5inV Voltring (X10 Rangel: 0.01% + 1mV Current: 01% + 250(A VI) Meter Batters (DC) Moch 6820A 46V. ±60V. . ±0.12A, ±1.2A ±12V, ±120V Load Effect (Load Regulation) Continued Mindel 68 ±0.06A, ±0.6A 一般の Voltage (Xi Range): 01% +13mV Meter Ranges (AC) Voltage (X10 Range)-01% + 1mV Current: 01% + 250µA Model 6826A: 4V (unçāl)', 40V i 0.08A rais, 0.8A rms Mudel 6827A: 8V (uncal), 80V mis 0.04A rms, 0.4A ri Temperature Ratings: 208 and 254 Vacat any output voltage and current Operating: ,0 to 55°C. Storage: -40 to +75°C. within rating and some Model 6826A: Voltage (X1 Range); OT + .5mV Cooling: Voltage (X10 Range): 01% + 5mV Convection cooling is employed Withe supplies have no Current: 01% + 250µA Model 6827A moving parts Voltage (XI Range): 01% + 1mV Dimensions: " Voltage (X 10,Range) 2, 01% + 10mV Corrent; , 01% #250uA See outline diagram : Figure 2.1. Weight: 18 lbs. (8:2 kg.) net, 21 lbs. (9.5 kg.) shipping); PARD (Ripple and Noise): Rms/p-p (20Hz to 20MHz), at any line voltage and under any load condition within rating. ... Model 6826A: POWER SUPPLY SPECIFICATIONS đ. Voltage (X: Range); 2mV tins/10mV p-jz DC Output Voltage (X10 Bange): 6mV rms/35mV p-p--Voltage and current spans indicate range over which Current: SmA rins/5mA p p Model 682 M output may be varied. 10 Voltage (X1 Range): 2.5mV /ms/15mV/p/p Model 6826A:

X1 Range: /-5V to +5V, O to 1.0A & X10 Range: -50V to +50V, 0 to 1.0A

Voltage (X10, Range): 10mV rms/60mV p-p Current: ...4ittA rms/5mA p.p.

# Table 1-1: Specifications, Models 6826A and 6827A (Continued)

HPOWER SUPPLY SPECIFICATIONS (Continued)	Model 6826A:	
	C Voltage (X1 Range): 10mV	A
	Voltage (X10 Range): 100mV	13.25
Temperature Coefficient:	Current, 3mA	
Quiput change per degree Centigfade change in am-	Model 6827A:	
with bind following 30 minutes warm-up.	, Voltage (X1 Range): 20mV	
Model 6826A:	Voltage (X10 Range): 200mV	
Min Voltage (X1 Range): .01%;+ .35mV	Current: 1.5mA	
•/// :/Voltage (X10 Range); .01% + 3mV		
////Current: .02% + 50µA	Output Impedance (Typical to 50kHz);	
Model 6827A:"	Approximated by a resistance in series with an induc-	
Voltage (X1 Range): .01%+:7mVi	tance (constant voltage operation).	
Voltage (X10 Range): .01% + 6mV.	<u>Mpdel 6826A:</u> 1mΩ & 1:5μH	
• Current:: .02% + 50µÅ	<u>Model G827A:</u> \ 2mΩ & 4μH	
	DC Output Isolation:	1.415
Drift (Stability):	Supply may be floated at up to 300V above ground.	5 Sec.
Change in output (dc to 20Hz) over-8 hour interval		
under constant line, load, and ambient following 30,	Remote Resistance Programming:	
* minutes warm-up:	Model 6826A (Resistance Coefficient)	a an an an Na Chuirtean
Model 6826A:	Voltage (X1 Range): 2000Ω/V ± .1%	
Voltage (X1 Range): 03% + DnV (Pot wiper jump)	Voltage (X10 Range): $200\Omega/V \pm .1\%$	
effect may add 5mV)	Current: 10Ω/mA ±.1%	
Voltage (X10 Range): .03%,+:10mV (Pot Wiper	Model: 6827A (Resistance Coefficient):	
jump effect may add 50mV)	- Voltage (X1 Range); 1000Ω/V± 1%	
Current: 1% + 200µA (Por wiper jump effect may	Voltage (X10 Range): $100\Omega/V \pm 1\%$	
add(15mA)	Current: $10\Omega/mA \pm .1\%$	
Model 6827A:		
Voltage (X1 Range);;;,03% # 2mV (Pot wiper jump-	Remote Programming Speed:	
w attect may add 5mV)	50usec are required to change between 1% and 99% of	nin ing
Voltage (X10, Range) 00% + 20mV (Pot wiper	/ the maximum + and - voltage limits.	
sijump effect may add 100mV1		
Current: 1%+1200LA:Int.wiper jump effect may	Remote Programming Temperature Coefficient:	1.0
(add 1mA)	Output change per degree Centigrade change in am-	
	bient using an external control resistor (RF) at output	
Load Effect Transient Recovery (Load Translent	voltage (VO) or current (IO). % T.C. RF is the tem-	
Recovery	perature coefficient of the control resistance RF.	
Time required for ourput voltage sucovery to within	Model 6826A: S Voltage (X1, Range): .25mV + .007% (VO) +	
the specified level of the nominal output voltage	% T/C, RF (VO + 5)	
following a change in output current equal to the	Voltage (X10 Range): 2.2mV + .007% (Vo) +	9. N
current rating of the supply	% T.C. RF (VO + 50)	
Model.6826A:	Current:	
7 100µsec is required for output voltage recovery:		집안
within 60mV pl nominal output voltage.		
/Model 6827A:	Model 6827A:	
100µsec is required for output voltage fecevery	Voltage (X1-Range): .5mV + .007% (Vo) +	
within 100mV of nominal output voltage.	% T.C. RF (YO + 10)	* Y
	X Voltage (X10 Range): .4mV + .007% (VO) +	
Resolution:	% T.C. RF (VO + 100)	7 +
Typical output voltage of current change-that can be	Current: .016% (IO) + 33µA + % T.C. RF (IO)	<b>*</b>
obtained using front panel controls,		

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1.3



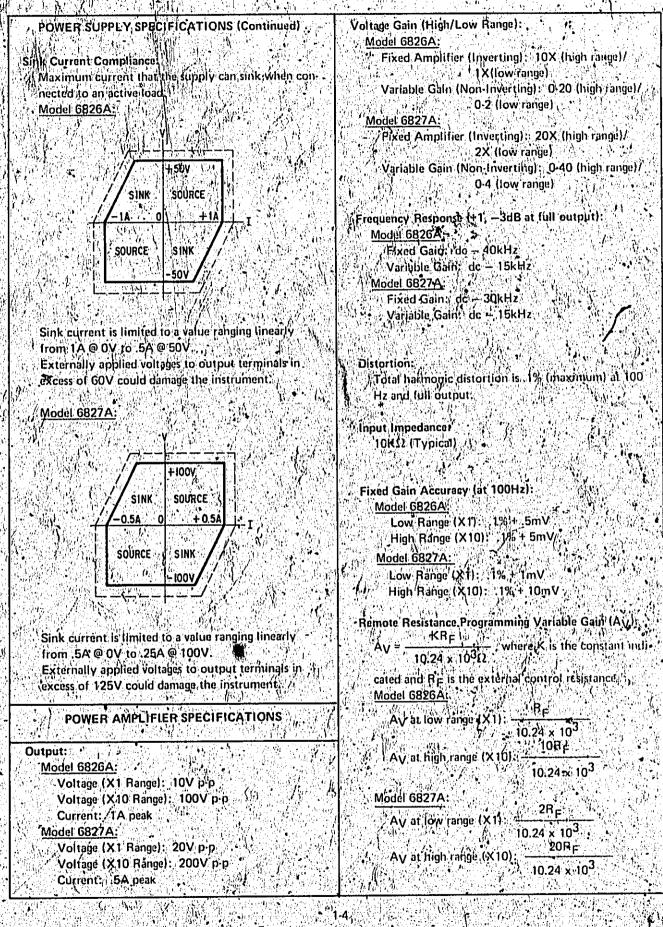


Table 1-1. Specifications, Model s6826A and 6827A (Continued)

Variable Accuracy:		• 1		
Accuracy in high ra	inge at 10	)0H2 usir	ng an ex	ternal
control resistance	Rr) at ou	itput vol	tage (V	0), %
R <sub>F</sub> is the accuracy	of the co	ntrol res	istance	RF
Model 6826A:. (	.05%*+ %	R <sub>F</sub> ) VO	+ 5mV	
Model 6827A: (	.05% +'%	RF) VO	+ 10m)	1
			11 Y I	5 A 1

Remôte Voltage Control Coefficient Fixed gain amplifier mode, voltage coefficient

#### OPTIONS 1-14

Options are customer requested factory modifica-1-15 tions of a standard instrument. The option described below applies to Models 6826A and 6827A

Description Option No.

007

Ten-turn Output Voltage Control: Replaces standard single-turn voltage control to allow greater resolution in setting the output voltage of supply.

#### ACCESSORIES 1,16

The accessories listed in the following chart may L17 🗇 be ordered with the instrument or separately from your local Hewlett-Packard sales office (refer to list at rear of manual for addresses).

Description HP Part No.

5060 87.62

Dual Rack Adapter: Kit for rack mounting . one or two supplies in standard 19-inch rack.

Combining Case for mounting one or two

Cooling kit for above combining case, 115

Blank Panel: Filler panel to block unused 5060-8760 / half of rack when mounting only one supply Carrying handle easily attached for portabil-

ity and handling convenience.

units in standard 19 Inch rack.

11057A 🗄

1052A

5060-0789

5060-0796

Vac, 50-60Hz. Cooling kit for above combining case, 230 Vac. 50-60Hz.

Model 6826A

Voltage (X1) Range): 1 volt/volt ± 1% Voltage (X10 Range): 10 volts/volt ± ,1% Model 6827A:59

Voltage (X1 Range): 2 volts/volt ± .1% Voltage (X10 Range); 20 volts/volt ± .1%

Constant Current, voltage coefficient, (the following applies/to variable gain amplifier, fixed gain amplifier,

and power supply modes of operation):

Models 6826A and 6827A: 1 ampere/volt ± .5%

### 1-18 INSTRUMENT IDENTIFICATION

Hewlett-Packard power supplies are identified by a 1-19 three part serial number. The first part is the power supply model number. The second part is the serial number prefix, consisting of a number letter combination denoting the date of a significant design/change and the country of manufacture. The first two digits indicate the year (12 = 1972, 13 = 1973, 20 = 1980, etc); the second two digits indicate the week (01 through 52); and the letter "A", "G", "J", or 70 designates the U.S.A., West Germany, Japan; or the United Kingdom, respectively, as the country of manufacture. The third part is the power supply serial number; a different 5digit sequential number is assigned to each power supply. starting with 00101.

If the serial number prefix on your unit does not 1.20, agree with the prefix on the title page of this manual, change sheets supplied with the manual or manual backdating changes in Appendix A define the differences between your instrument and the instrument described by this manual.

#### ORDERING ADDITIONAL MANUALS 1-21

One manual is shipped with each instrument. Add 1-22 itional manuals may be purchased from your local Hewlett-Packard field office (see list at rear of this manual for addresses). Specify the model number, serial number prefix, and HP part number shown on the title page.

#### 

2-13

#### 2-1 INITIAL INSPECTION •

2-2 Before spipment, this instrument was inspected and found to be free of mechanical and electrical defects. As soon as the instrument is received, proceed as instructed in the following paragraphs:

#### 2-3 MECHANICAL CHECK

2-4 If external damage to the shipping carton is evident, ask the carrier's agent to be present when the instrument is unpacked. Check the instrument for external damage such as broken controls or connectors, and dents or scratches on the panel surfaces. If the instrument is damaged, file a claim with the carrier's agent and notify your local Hewlett-Packard Sales and Service Office as soon as possible (see list at rear of this manual for addresses).

#### 2-5 ELECTRICAL CHECK

2-6 Check the electrical performance of the instrument as soon as possible after receipt. Section V of this manual contains performance check procedures which will Verify instrument operation within the specifications stated in Table 1-1. This check is also suitable for incoming quality control inspection. Refer to the inside front cover of the manual for the Certification and Warranty statements.

#### 2-7 REPACKAGING FOR SHIPMENT

2-8 To insure safe shipment of the instrument, it is recommended that the package designed for the instrument be used. The original packaging material is reusable. If it is not available, contact your local Hewlett-Packard field office to obtain the materials. This office will also furnish the address of the nearest service office to which the instrument can be shipped. Be sure to attach a tag to the instrument specifying the owner, model number, full serial number, and service required, or a brief description of the trouble.

### 2-9 INSTALLATION DATA

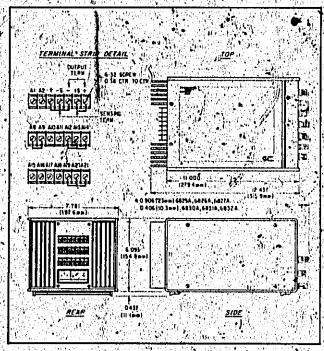
2-10 The instrument is shipped ready for bench operation. It is necessary only to connect the instrument to a source of power and it is ready for operation.

#### 2-11 LOCATION

212 This instrument is convection cooled. Sufficient space should be allotted so that a free flow of cooling air can reach the top and rear of the instrument when it is in operation. It should be used in an area where the ambient temperature temains between 0°C and +55°C.

#### OUTLINE DIAGRAM

2-14 Figure 2-1 Illustrates the outline shape and dimensions of Models 6826A and 6827A.





#### 2-15 RACK MOUNTING

2-16 The Model 6826A and 6827A BPS/A's may be rack mounted using either the dual rack adapter kit or the combining case (with appropriate cooling kit) described in Paragraph 1-16. The necessary installation instructions are provided with the accessories. Refer to Paragraph 5-91 before proceeding with the rack mounting installation instructions.

### 2-17 INPUT POWER REQUIREMENTS

2.18 S Models 6826A and 6827A may be operated continuously from either a nominal 120 volt of 240 volt, 48-63 Hz power source. A two-position selector switch (...) blocated within these crower module on the rear panel selects the power source, check that the selector switch setting matches the nominal line voltage of the source. If required, move the switch to the other position. Note that the power cable must be removed, the plastic door on the power module must be moved aside, the fuse extractor must be pulled ourward and the fuse must be removed in order to gain access to the selector switch.

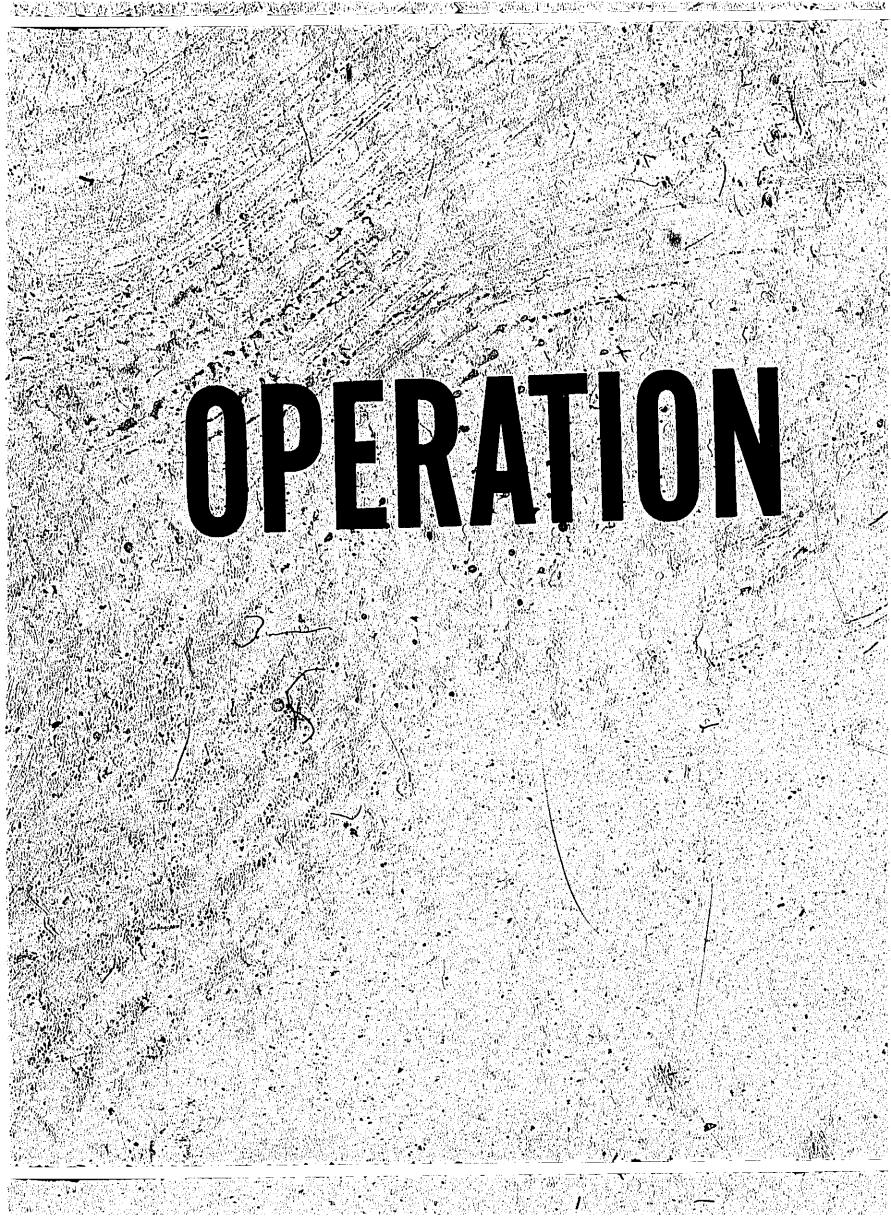
2-19 When the instrument leaves the factory, the proper fuse is installed for 115 volt operation. An envelope containing a fuse for 230 volt operation is attached to the instrument. Make sure that the correct fuse is installed if the position of the slide switch is changed (2A for 115 volt of eration, and 1A for 230 volt operation).

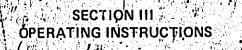
#### 2-20 POWER CABLE

· } · · .

2.21. To protect operating personnel, the National Electrical Manufacturers' Association (NEMA) recommends that the instrument panel and cabinet be grounded. This instrument is equipped with a three conductor power cable. The third conductor is the ground conductor and when the cable is plugged into an appropriate receptacle, the instrument is grounded. The offset pin on the power cable's three-prong connector(is the ground connection.

2-22 To preserve the protection feature when operating the instrument from a two contact outlet, use a three prong to two-prong adapter and connect the green lead on the adapter to ground.





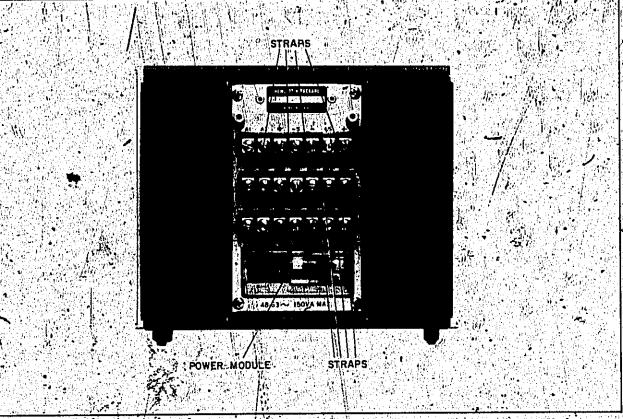


Figure 3-T: Bipolar Power Supply Amplifier, Models 6826A and 6827A, Rear View

### 3.1 INTRODUCTION

3.2 This section describes the operating controls and indicators, the turn on checkout sequence, and operating modes of Bipolar Power Supply/Amplifier (BPS/A) models 6826A and 6827A. "Uccal and remote programming operations are also described.

#### 3-3 REAR TERMINALS AND AC INPUT

3.4 The Bipolar Power Supply/Amplifier (BPS/A) is shipped with the rear terminals strapped for local programming (using front panel controls) as shown in Figure 3-1. Remote programming strapping requirements are described in subsequent paragraphs. The power module contains fuse F1:(2A for 115Vac or 1A for 230Vac) and a slide switch for connecting 115Vac or 230Vac input power to the instrument. To turn on the BPS/A, set the LINE switch fitem (1) Figure 3-2) to ON. The LINE ON indicator (2) should light. Fuse F1 protects the main power supply. At initial turn-on, an internal circuit protects any loads connected to the BPS/A from turn-on transients by shorting the output terminals and disabling the BPS/A's power output circuits. This circuit operates similarly at turn-off to protect any loads from turn-off transients.

#### 3.5 OPERATING CONTROLS AND INDICATORS

#### 3-6 MODE SWITCH

1

3.7 The MODE switch (3) allows the BPS/A to operate as a power supply, variable gain amplifier, or a fixed gain amplifier. In the power supply operation, the BPS/A provides a variable bipolar dc output voltage dependent upon the RANGE switch (4) and VOLTAGE control. (5). settings. The dc output voltage ranges are as follows:

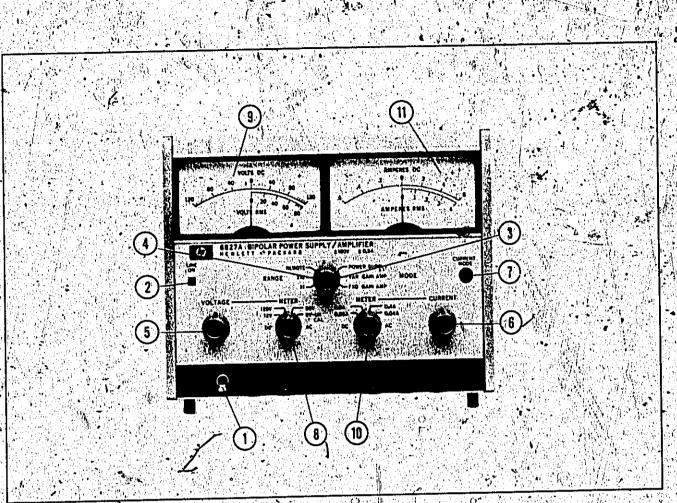


Figure 3-2. Operating Controls and Indicators

3 2

and the second second				
	DC OUTPÙT VOLTAGE			
MODEL	HIGH RANGE	LOW RANGE		
6826A 6827A	-50V to +50V -100V to +100V	-5V to +5V -10V to +10V		

3.8 In variable gain amplifier operation, the BPS/A can amplify or attenuate an external input signal (dc to 15kHz) applied to the HI and LO IN terminals. The gain is variable from 0 to a maximum depending upon the RANGE switch and VOLTAGE control . 5 settings, The variable

gain ranges are astfollows:

	VARIABLE VOL	TAGE GAIN
MODEL	HIGH RANGE	LOW RANGE
6826A	0-20 0-40	0-2. 0-4

3-9 In fixed gain amplifier operation, the BPS/A inverts

and amplifies an external input signal applied to the HI and LO IN terminals. For fixed gain amplifier operation, the 6826A has a frequency response from DC to 40kHz and the 6827A has a frequency response from DC to 30kHz. The fixed voltage gain provided in the high or low output range is as follows:

	FIXED VOLTAGE GAIN
MODEL	HIGH RANGE
6826A "	X10 X1
6827A	X20 X2

#### 3-10 RANGE SWITCH

3-11 The RANGE switch (4) allows selection of the high (X10) or low (X1) output ranges for power supply, variable gain amplifier, or fixed gain amplifier operation. The REMOTE position allows the high or low range to be externally selected via the rear terminal strip (see Paragraph 3-45).

#### 3-12 VOLTAGE CONTROL

3.13 The VOLTAGE control (b) controls the output level (power supply operation) or gain (variable gain amplifier operation) of the BPS/A. In power supply operation, the VOLTAGE control varies the output voltage from a maximum negative value (full counterclockwise) through zero (midposition) to a maximum positive value (full clockwise). In variable gain amplifier operation, the gain is variable from zero to the maximum gain as the VOLTAGE control is varied from full counterclockwise to full clockwise. In fixed gain amplifier operation, the VOLTAGE control source from the potention, the VOLTAGE control does not control circuit operation.

#### 3-14 CURRENT CONTROL

3-15 The CURRENT control (6) sets the constant current output of the BPS/A. This control is operable in all three modes of operating (power supply, variable gain amplifier, and fixed/gain amplifier) and controls the output curtent from 0 to the maximum rated output (1.0A for the 6826A and 0.5A for the 6827A). When the instrument switches from constant voltage to constant current operation, the CURRENT MODE indicator (7) lights. Selection of constant voltage or constant current operation is described in Paragraphs 3-27 and 3-28.

#### 3-16 VOLTAGE METERING

3.17 The VOLTAGE METER switch (a) permits monitoring the DC or AC output voltage on voltmeter (a) The shaded area on the voltmeter face indicates the amount of output voltage that is available in excess of the normal rated output. The voltmeter upper scale reads the bipolar DC voltage from a maximum negative value through OV to a maximum positive value, DC accuracy is ±3% of full scale. The lower scale reads the RMS output voltage from 0 to a maximum level. MC accuracy is ±5% of full scale. The volt meter ranges selected by the VOLTAGE METER switch are as follows:

	an the state of the second state of		entre de la servició de la secola de la secol
HODEL	VOLI	METE	R RANGES
MODEL	DC		AÇ (RMS)
6826A _6827A	[10] S. Markara, A. S. Markara, M. Markara, A. S. Markar Markara, A. S. Markara, A Markara, Markara, A. S. M	1. 1.9 6 1. 1	0-4V (uncal), 0-40V 0-8V (uncal), 0-80V

#### 3-18 CURRENT, METERING

3:13 The CURRENT METER switch (10) permits . monitoring the DC or AC output current on ammeter (1) The shuded area on the ammeter face indicates the amount of output current that is available in excess of the normal, rated output. The animeter upper scale reads the bipdlar DC current from a maximum negative value through OA to a maximum positive value. DC accuracy is £3% of full scale. The lower scale reads the RMS output current from 0 to a maximum level. AC accuracy is 5% of full scale. The ammeter ranges selected by the CURBENT METER switch are as follows:

MODEL	AMMETE	R RANGES,
	۲. DC	AC (RMS)
6826A 6827A	0 to ±0.12A,0 to ±1.2A 0 op ±0.06A,0 to ±0.6A	

#### 3-20 TURN-ON CHECKOUT PROCEDURES

생은 말을 다 같아.

# CAUTION

Rear terminal strip cover must be in place when instrument is in use.

3.21 The following turn on and checkout procedures are performed utilizing the front panel controls (Figure 3.2) and the normal rear terminal strapping connections as received from the factory. Also, the Local/Auto switch, located inside the instrument on board A2, is in the Locat position (pushed to the right or toward the rear of the instrument) as received from the factory. The AUTO position is used for auto-series and auto-parallel operations (see Paragraphs 3-57 through 3-61). The following procedures check both power supply and amplifier to ensure that the BPS/A is operational.

POWER SUPPLY CHECKOUT PROCEDURE

- Set front panel controls as follows: MODE switch (3) - POWER SUPPLY
- RANGE switch (4) X1
- VOLTAGE control 5 midposition
- CURRENT control 6 full clockwise
- VOLTAGE METER switch (1) low range DC CURRENT METER switch (1) - high range DC

b. Set LINE switch () to ON and observe that LINE ON indicator (2) lights.

c. Adjust VOLTAGE control (5) from full counterclockwise (--) to full clockwise (+) range through OV and note that maximum output is attained as indicated on meter (9)

d. Set VOLTAGE METER switch (1) to high tange DC and RANGE switch (1) to X10 position.

e. Adjust VOLTAGE control 5 Clockwise and counterclockwise through entire bipolar output voltage

range through 0 and note that max mumoutput is attained as indicated on meter (9) Adjust output voltage to

150V. To checkout the constant current circuit, first tuck off BPS/A. Short circuit the front panel terminals (HI OUT to LO'OUT).

g. Turn do supply and observe that CURRENT MODE indicator. () Indicates 0 volts.

h. Adjust CURRENT control 6 from full cw to full ccw and note that minimum current is attained as indicathd on meter 11

Turn off supply and remove short from output term-

j. Turn on supply and adjust VOLTAGE CONTROL (5). tor,an output of -50V.

k. Turn off supply and reconnect short across the HI and LO OUT terminals.

dicatole (2) lights and meter (9) Indicates 0 volts.

m. Adjust CURRENT control (6) from full cw to full ccw and note that minimum current is attained as indicated on meter (1)

n: Turn off BPS/A and remove short from output terminals.

VARIABLE GAIN AMPLIFIER CHECKOUT PROCEDURE

MODE switch 3 -VAR GAIN AMP

RANGE switch @ \_ X1

VOLTAGE control 6 - midposition

CURBENT control. . - full clockwise

-. VOLTAGE METER switch (1) - low ratige AC CUBBENT METER switch, (10) - high range AC

CURRENT METER switch. (0) - high range AC p. Connect a 1.75V rms, 100Hz mout signal to the

front panel input terminals (HI and LO IN). q: Turn on supply and adjust VOLTAGE control (5) through entire RMS range and note that maximum voltage is attained as indicated on meter (9)

r. Set VOLTAGE METER switch (B) to high range AC, RANGE switch (4) to X10, and adjust VOLTAGE control (5) through entire RMS range and note that maximum voltage is attained as indicated on meter (9) FIXED GAIN AMPLIFIER CHECKOUT PROCEDURES

s. Set MODE switch 3 to FXD GAIN AMP posi-

tion and increase input signal to 3.5V rms. •t. Adjust VOLTAGE control (5) through entire. RMS range and note that maximum voltage is attained as indicated on meter (9)

### 3-22 OPERATING MODES

#### CAUTION

Rear terminal strip cover must be in place when instrument is in use.

The position of the front panel MODE switch de 3.23 termines whether the instrument will be used as a power supply or an amplifier. In addition, the instrument may be controlled locally using the front panel VOLTAGE, and CURRENT controls or remotely via terminals on the rearof the unit. The front panel output terminals (HI and LOS OUT) and input terminals (HI and LO IN) are repeated as (+ and -) and (A1, and A2) respectively on the rear terminal strip. The rear terminal strip includes sensing (+Siand -S) terminals and other terminals for remote control of the BPS/A as shown in Figure \$3. These terminals connect to various control points within the instrument and allow strapping connections to be made which enable the power supply or amplifier to be utilized in many applications. The following paragraphs describe the procedures for utilizing the various operational capabilities of the power supply. A more theoretical description concerning the operational, features of/this supply is contained in Application Note 90 and in varibus Tech. Letters. Copies of these can be obtained from your local Hewlett-Packard field office.

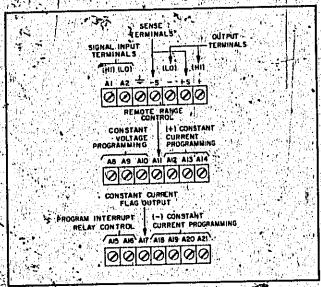


Figure 3-3. Rear Terminal Strip

#### 3-24 LOCAL PROGRAMMING

3.4

3-25 The BPS/A is shipped with its rear terminal strapping connections arranged for constant voltage/constant current, local programming, local sensing, single unit mode of operation. This strapping pattern is illustrated in Figure 3-4. Also, the Local/Auto switch on board A2 (see Paragraph 3-54) is in the Local position when the instrument is shipped from the factory. This switch must be in the Local position for single unit mode of operation.

3-26 The operator selects either power supply, variable gain amplifier, or fixed gain amplifier operation (MODE

switch) and also selects either constant voltage or a constant current output using the front panel VOLTAGE and CUR RENT controls (for local programming, no strapping changes are required). Constant voltage or constant current operation are selected as described in the following paragraphs.

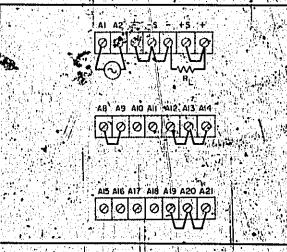


Figure 3-4. Normal Strapping Pattern (LOCAL Programming)

3-27 Constant Voltage. To select a constant voltage output, proceed as follows:

a. Remove load from output terminals, turn on supply, and adjust VOLTAGE control for desired output voltage.

b. Short output terminals and adjust CURRENT control for maximum output current allowable. (current limit) as determined by load conditions and voltage range selected in step (a). If a load change takes place and causes the output current to exceed this setting, the power supply will automatically crossover to constant current mode and output current will be constant at the level set by the CURRENT control. The CURRENT MODE indicator will come on and output voltage will drop proportionately to maintain constant current.

3-28 Constant Current. To select a constant current output, proceed as follows:

a. Short output terminals and adjust CURRENT control for desired output current.

b. Open output terminals and adjust VOLTAGE control for maximum output voltage allowable as determined by load conditions and current selected in step (a). If a load change causes the voltage to rise, the power supply will automatically crossover to constant voltage at the voltage 'setting and output current will drop proportionately.

3-29 OPERATION OF SUPPLY BEYOND RATED

3-30

31

The shaded area on the front panel meters indicate.

the amount of output voltage and current that is available in excess of normal rated output. Although the BPS/A cur by operated in this region without damagy it cannot be guadored to meet all of its performance specifications.

#### A 31 A. REACTIVE LOAD CONSIDERATIONS

3.32 "The life and performance of the instrument can be preserved if the following simple precaution is observed when driving reactive loads." Always set/program the VQLT AGE sontfol for zero output before removing a capacitive load or interrupting an inductive load.

#### 3-33 CONNECTING LOAD

3.37. Each load should be connected to the power supply output terminals (front or rear) using separate pairs of connecting wires. This will minimize mutual coupling effacts between loads and will retain full advaitage of the low putput impedance of the power supply. Each pair of connecting wires should be as short as possible and twisted or shielded to reduce noise pickup. (If a shielded pair is used, connect the shield to ground at the power supply and leave the other end unconnected.)

3 35. If load considerations require that the output power distribution terminals be remotely located from the power supply, then the power supply output terminals should be connected to the remote distribution terminals via a pair of twisted or shielded wires and each load should be separately, connected to the remote distribution terminals. For this case, remote sensing should be used. (Refer to Paragraph 3-39).

3-36 Always use two leads to connect the load to the supply, regardless of where the setup is grounded. This will eliminate any possibility of output current return paths through the power source ground. The supply can also be operated up to 300V dc above ground if heither output terminal is grounded.

#### 3-37 REMOTE SENSING

3.5

3.38 Remote sensing is used to maintain good regulation at the load and reduce the degradation of regulation, which would occur due to the voltage drop in the leads between the power supply, and the load. Remote selsing is accouplished by duilding this strapping pattern shown in Figure 3.5. The power supply, should be toned of between thang, ing strapping patterns. The leads from the sensing (15) terminals to the load will carry much less current than the load leads and it is implicible to that these reads be a heavy as the load leads. The week that these reads be a heavy to minimize poise bickup.

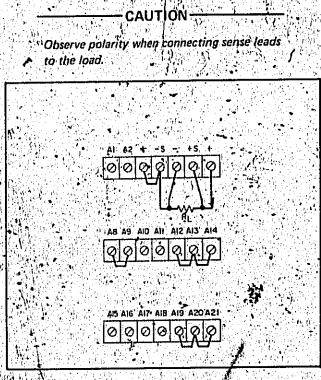


Figure 3-5. Remote Load Sensing

3-39 For reasonable load lead lengths, remote sensing , limits degradation of the performance of the supply. However, if the load is located a considerable distance from the supply, added precautions must be observed to obtain satisfactory operation. Notice that the voltage drop in the load leads subtracts directly from the available output voltage. Because of this, it is recommended that the drop in <u>each</u> load lead not exceed 1.0 volt. If a larger drop must be tolerated; please consult an HP Sales Engineer.

#### NOTE

Due to the voltage drop in the load leads, it may be necessary to readjust the constant current crossover limit setting in the remote sensing mode

3-401 REMOTE PROGRAMMING

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#### - CAUTION

External programming resistors must be connec Steel to the appropriate rear terminals before bower is applied to the Instrument.

3.41-47 The constant voltage and constant current outputs. 7of the BPS/A can be programmed (controlled) from a re- 43 motely located device such as HP 6940A Multiprogrammer.

or HP 6941A Multiprogrammer Extenders. Either a resistance or voltage source can be used as the programming device. The wires connecting the programming terminals on the rear of the BPS/A to the remote programming device. should be twisted or shielded to reduce noise pickup.

3.42 Resistance Programming Constant Voltage. A programming resistor (Rpv), connected as shown in Figure 3.6, can be used to control the voltage output or gain provided that the MODE switch is in the POWER SUPPLY or the VARIABLE GAIN AMP position. Resistance programming of the constant voltage output is not applicable in the FXD GAIN AMP mode of operation. The VOLTAGE control on the front panel is disconfigured (disabled) for the strapping connections shown in Figure 3-6. To maintain the stability and temperature coefficient of the instrument, use programming resistors that have stable low noise and low temperature characteristics (less than 20 ppm/°C.). Also, they should operate at less than 1/30th of their wattage rating to minimize short term temperature effects

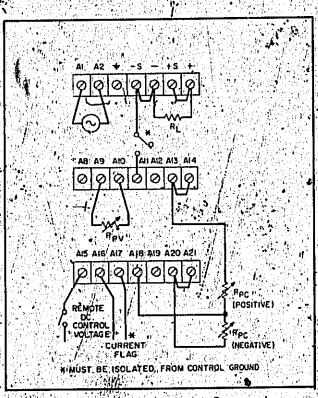


Figure 3-6. Remote Resistance Programming, Constant Voltage/Constant Current

3.43 Power Supply.. For power supply operation, the bipolar output voltage varies linearly from a maximum negative value through zero to a maximum positive value according to the value of the programming resistance Rpv. The voltage output ranges and corresponding values of RPV are as follows:

1		, 6826A		6827A	
1. See . S	RÞV VALUE		LOW RANGE	HIGH RANGE	LOW
5	0	-51,2V	-5.12V.	-102.4V.	-\$10. <b>2</b> 4∨-
	10,24KΩ	ov.	ov .	0V:	οv
	20.48ΚΩ	+51.2V	+5.12V	+102.4V	+10.24V

3.44. As noted above, the output voltage should be zero volts with 10.24K connected to the programming terminals, The output may be adjusted to zero by adjusting the V ZERO ADJ potentiometer as described in Paragraph 5-104. The output voltage varies from the maximum negative value to the maximum positive value through 0 at a rate determined by the resistance programming coefficient as follows:

MODEL	PROGRAMMIN	G COEFFICIENT
MODEL	HIGH RANGE	LOW RANGE
		2000 ohms/volt ± ,1% 1000 ohms/volt ± ,1%

CAUTION-

When remote control programming is employed, the FLAG (A17) and REMOTE RANGE (A11) programming connections must be isolated from a the computer ground.

3-45 The switch connected between the A11 and -S terminals allows remote selection of the high (X10) or low (X1) range. Note that the front panel RANGE switch must be in the REMOTE position in order to utilize the remote selection feature. The remote de control voltage connections between terminals A15 and A16 activate an internal relay. When the control voltage is applied, the internal relay is energized momentarily disabling the input driver to the BPS/A error amplifier. This feature is used to prevent, transients from affecting the output when the programming input is changed. Terminal A17 provides an indication to the external programming device when the BPS/A is in constant current operation.

3-46 Variable Gain Amplifier. For variable gain amplifier operation, an external input signal (dc to 15kHz), applied to terminals A1 (HI IN) and A2 (LO IN), is amplified or attenuated. The gain is variable from 0 to a maximum value as the value of Rpv varies from 0 to 20.48K ohms. The variable gain at the high and low ranges is as follows:

MODEL	VARTABLE GAIN
	HIGH RANGE
6826A. 6827A	0.20. - 0.20 - 0.40

The voltage applied to the input terminals, HI IN (A1) and LO IN (A2), must not exceed 50V (maximum) of the instrument may be damaged.

3.47 , Resistance Programming, Constant Current, Programming resistors (RP/C), connected as shown in Figure 3.6, can be used to control the constant current output, The front panel CURRENT control is disconnected (disabled) when the remote RPC resistors are connected as indicated ( Resistance programming of the constant current octput ' can be accomplished in all three modes of operation (power supply, variable gain amplifier, and fixed gain amplifier) Individual RPC resistors control positive and negative constant current outputs respectively. The positive on negative output current is variable at a rate determined by the programming coefficient as follows:

MODEL	OUTPUT CURRENT	PROGRAMMING COEFFICIENT
6826A	0 to 1.024Å	,10 ohms/mA ± .1%
6827A	0 to .512A	- 10 ohms/mA ± .1%

#### -CAUTIO

A load must be maintained at all times during constant current operation. The load can be a 100K $\Omega$  resistor for the 6826A or a 400K $\Omega$  resistor for the 6827A.

3-48 Zero output current for zero programming resistance can be assured through proper adjustment of the front panel +1 and --1 ZERO ADJ potentiometers (see Paragraph 5-107).

3-49 Voltage Programming, Constant Voltage, Voltage programming of the output voltage can be accomplished in the variable gain or fixed gain amplifiel mode of operation. Voltage programming is not applicable in the power supply mode.

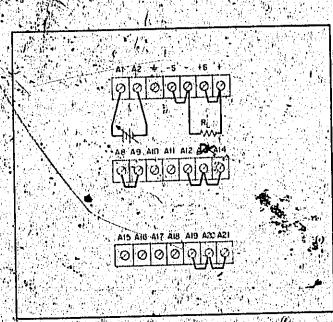


Figure 3.7. Remote Voltage Programming,

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3.8

Variable Gain Amplifier. AC signals or a dc level 3:50 (positive or negative) can be amplified or attenuated in the variable gain amplitier mode. Figure 3-7 shows a variable de level-(programming voltage) applied to the input terminals A1 (HI IN) and A2 (LO IN). Since the BPS/A is noninverting in the variable gain amplifier mode, a positive input (A1 positive, A2 negative) results in a positive output and a negative input (A1 negative, A2 positive) results in amegative output. The other connections on Figure 3.7 are shown for local control sing front panel controls, however, remote control using external controls may also be employed. front panel or remote voltage controls can be used to agenuate or amplify the input as required. With the front panel VOLTAGE control or remote programming resistor set for maximum output, the programming coefficient is as follows:

	MAXIMUM OUTPUT		OUTPUT		AMMING ICIENT	
MODEL	HIGH		HIGH RANGE	LOW RANGE		
6826A	±50V	±5V .	20 volts/ volt,	2 volts/		
6827A.	±100V.	±10V	40 volts/ volt	4 volts/ volt		

\*With front panel VOLTAGE control or remote programming resistor set for maximum rated output.

3-51 Fixed Gain Amplifier. AC signals up to 40kHz (6826A) or 30kHz (6827A) or a dc level (positive or negative) can be amplified in the fixed gain amplifier mode. Figure 3-7 shows a variable dc level (programming voltage) applied to the HI (A1) and LO (A2) input terminals. Since the BPS/A provides an inverted output in the fixed gain amplifier mode, a positive input (A1 positive, A2 negative) results in a negative output and a negative input (A1 negative, A2 positive) results in a positive output. The front panel or remote programming voltage controls are not applicable in this mode. The programming coefficient in the fixed gain amplifier mode is as follows:

	MAXIMUM OUTPUT		PROGRAMMING COEFFICIENT	
MODEL	HIGH RANGE	A LOW.	HIGH RANGE	LOW.
6826A 6827A	±50V ±100V	۲ ±5۷ ۱ <sup>.</sup> ±10۷	10 volts/ volt 20 volts/ volt	1 volt/ volt 2.volts/ volt

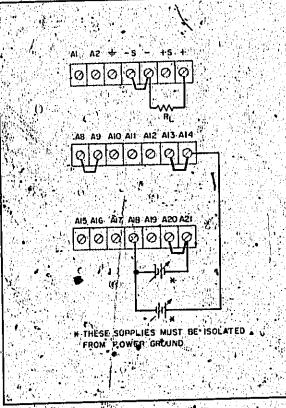


Figure 3-8.<sup>®</sup> Remote Voltage Programming, Constant Current

3-52 ... Voltage Programming, Constant Current. Voltage programming of the output current can be accomplished in all three operating modes (power supply, variable gain amplifier, and fixed gain amplifier). Positive and negative dc

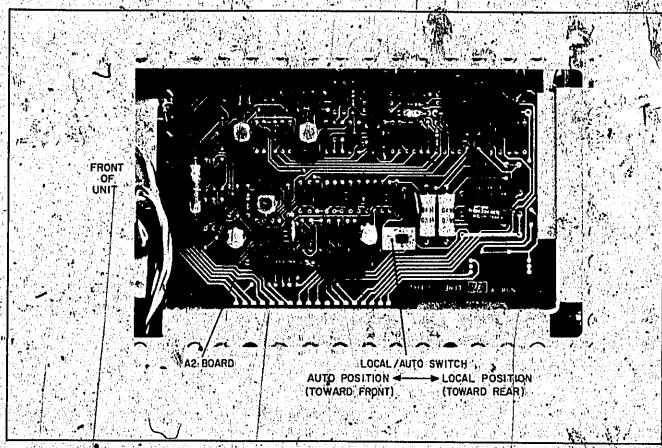


Figure 3-9. Local/Auto'Switch

programming voltages are connected to terminals A14 and A21 respectively as shown in Figure 3-8. The positive or negative output current will vary linearly with changes in! the programming voltages. The output current varies at a rate determined by the programming coefficient. For models 6826A and 6827A, the programming coefficient is 1 amp/ 1 volt. The maximum rated output current for the 6826A is 1A, therefore, the maximum programming voltage for 6826A is 1 volt. The maximum rated output current for the 6827A is 0.5A, therefore, the maximum programming voltage for 6827A is 0.5V.

#### 3-53 SERIES AND PARALLEL CONNECTIONS

3-54 The following paragraphs describe the connections' required for combining BPS/A's for series and parallel operations. These connections are employed whenever it is required to extend the voltage/gain or current capability beyond one supply. For series operation, the total output voltage/gain is the sum of the voltages/gains of the individual supplies. For parallel operation, the total output current is the sum of the output current from the individual supplies. For series or parallel operation, the BPS/A's must be operated in the same mode (power supply, variable gain amplifier, or fixed gain amplifier). Also, each supply must have its Auto/Local switch A2S1 (see Figure 3.9) in the Local position (pushed toward the rear of the instrument). Note that the external signal applied to the A1 and A2 terminals is internally disconnected when the BPS/A's are in the power supply mode.

3-55 Series Connections. Two or more supplies may be connected in series to obtain a higher voltage/gain than is available from a single supply. Figure 3-10 illustrates the series connections for three supplies. Each of the supplies must be adjusted in order to obtain the desired output/ voltage gain.

3-56 • Parallel Connections: Parallel operation of BPS/A is possible because of the constant voltage/constant current crossover feature. Two or more power supplies can be connected in parallel to obtain a total output current greater than that available from one power supply. The total output current is the sum of the output currents of the individual power supplies: The load must be selected so that the current limit of one supply is exceeded allowing it to operate in the constant current mode. The output CURRENT controls of each power supply can be separately set. The output voltage controls of one power supply should be set to the desired output woltage; the other power supply should be set (or a

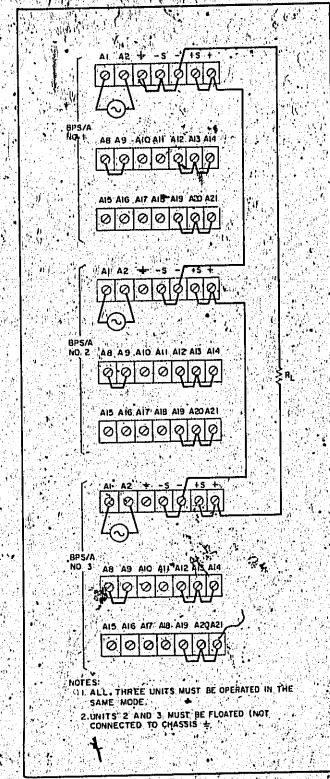
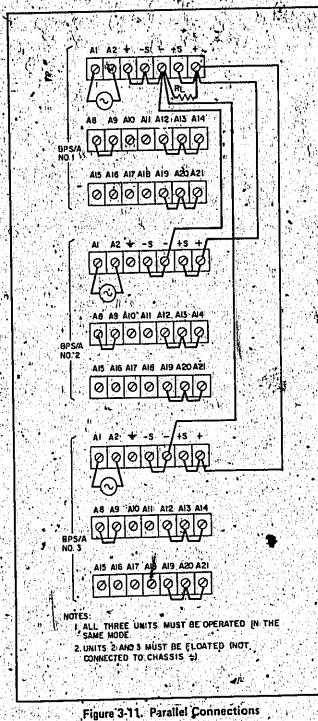


Figure 3-10. Series Connections

slightly larger output veltage. The supply set to the lower output voltage will act as a constant voltage source; the supply set to the higher output will act as a constant current source, dropping its output voltage until it equals that of the other supply. The constant voltage source will deliver only

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that fraction of its total rated output current which is necessary to fulfill the total current demand. Figure 3-11 illustrates the parallel connections for three units.



#### 3-57. AUTO-SERIES AND AUTO-PARALLEL CONNECTIONS

3-58 The following paragraphs describe the connections' required for combining BPS/A's in auto-series and auto-

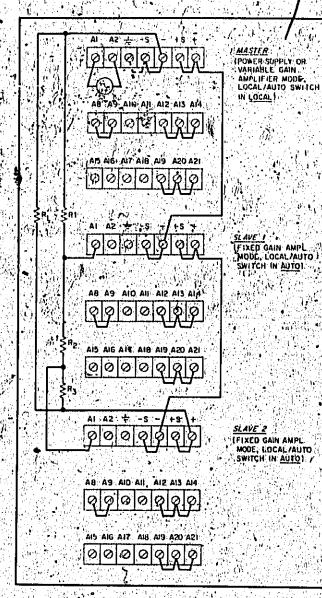


Figure 3-12. Auto-Series Connections, Three Units

parallel. These connections are employed whenever it is required to extend the voltage/gain or current capability beyond one supply. For auto-series operation, the output voltage of each slave supply varies in accordance with that of the master supply. For auto-parallel operation, complete control of the output current from one master is allowed. Diagrams are included for the strapping connections required between master and slaves for both auto-series and auto-parallel operations. In either case, the master must be in the power supply or variable gain amplifier mode and the slaves must be in the fixed gain amplifier mode. Also, for auto-series or parallel operation, the master supply's Local/ Auto switch A2S1 (see Figure 3-9) must be in the Local position and each slave supply must have its Local/Auto switch in the Auto position. The diagrams show the master strapped

3-11

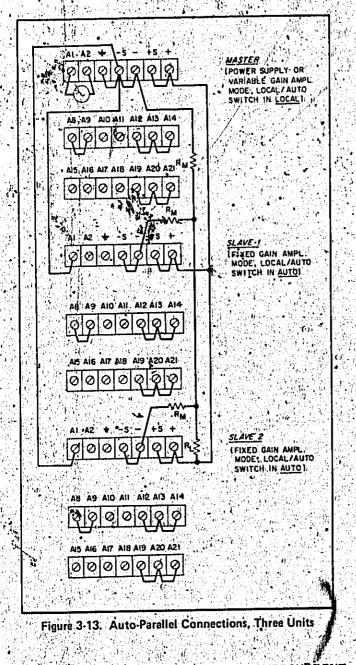
for local programming and with an external signal applied to the amplifier input terminals. However, the same auto-series or auto-parallel connections could be used with the master strapped for remote programming. Also, with the master supply in the power supply mode, the external signal applied to the A1 and A2 terminals is internally disconnected.

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3-59 Auto-Series Operation. Two or more BPS/A's can be connected in an auto-series arrangement to obtain a higher output voltage than that available from a single supply. Figure 3-12 illustrates the auto-series connections for the supplies. When this arrangement is used; the output vultage of each slave supply varies in accordance with that of the master supply; thus, the total output voltage of the combination is determined by the master supply's front panel VOLTAGE control (or remote programming input). The front panel CURRENT controls (or remote programming inputs) of all three units are operative and the current limit is equal to the lowest setting. The slave units must be floated off ground. Instruments can be operated floating up to 300 volts off ground whether operated singlely or in series. This limits model 6826A (±50V @ 1.0A) to six units in series and model 6827A (±100V @ 0:5A) to three units in series.

3.60 For instantaneous equal voltage sharing, resistors R1, R2, or R3 must be equal. Since any variation in R1, R2, or R3 will result in a change in the voltage divider rate and hence the output of the slave supply, it is important that these resistors be stable, low temperature coefficient. (20 ppm/°C or better). Also, they should have power rating of at least 10X, their actual power dissipation. The resis tors should be selected at the normal operating voltage levels so that the current through them is about 1 to 2mA,

3-61 Auto-Parallel Operation. Two or more BPS/A's can be connected in auto-parallel arrangement to obtain an output current greater than that available from a single supply. Figure 3/13 illustrates the auto-parallel connections for three supplies to allow increased output current in cont. stant voltage operation. When this arrangement is used, current sharing under all load conditions is permitted under control (front panel CURRENT control or remote programming) of the master supply. Because the CUBRENT controls (or remote programming) of each slave are operative, they should be set to a maximum to prevent the slave toverting to constant current operation; this could occur if the master output current setting exceeded) the slave's. For equal current sharing, the leads from RM to the load and to the (-) terminals should be approximately equal in length. To maintain instrument acquracy and stability, RM should be a stable, low temperature coefficient resistor of sufficient rating to prevent any apprediable self-heating (typically,  $1\Omega$ ). 8W, ±20 ppm/°C, ±1%).



# 3-62 BIPOLAR OVERVOLTAGE AND OVERCURRENT

3-63 Bipolar overvoltage and overcurrent limit circuits prevent excessive BPS/A voltage or current outputs. The voltage limiting circuit prevents the output voltage from exceeding approximately ±55 volts (6826A) or ±110 volta-(6827A). The current limiting circuit limits the transient output current to a value approximately two times the maximum rated output of 1.0A (6826A) or 0.5A (6827A).

### REVERSE VOLTAGE AND CURRENT LOADING

3-64

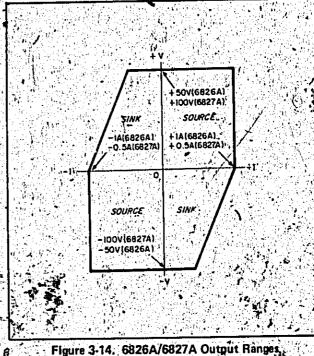
3-65: Current limit circuits also protect the BPS/A from active loads that force energy in or out of the BPS/A (sink condition). This can appear as current flow into the HI OUT (+) terminal when the terminal is positive, or current Ylow out of the terminal when it is negative. Figure 3-14 shows the normal operating locus of the BPS/A. As shown, the 6826A BPS/A will limit the sink current to a value/ ranging linearly from 1A at OV to 0.5A at 50V and the 6827A BPS/A will limit sink current to a value ranging linearly from 0.5A at OV to 0.25A at 100V.

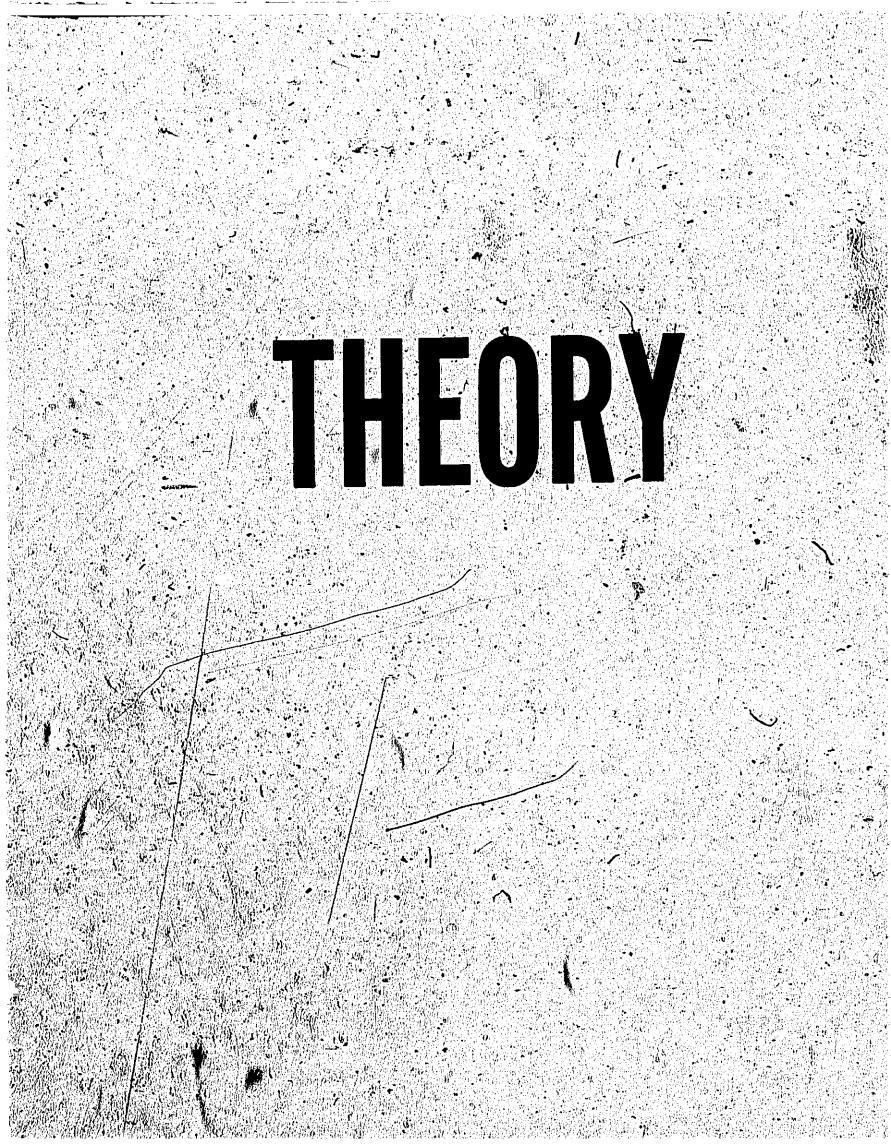
3.66 An active load can easily be accommodated by the. BPS/A as long as the following precautions are adhered to: a. The active load must not be applied unless the BPS/A is in its active state.

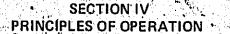
b: Program to zero output before disconnecting load.

## ---- CAUTION ----

Externally applied voltage to output terminals in excess of 60V (6826A) or 125V (6827A) could damage the instrument.







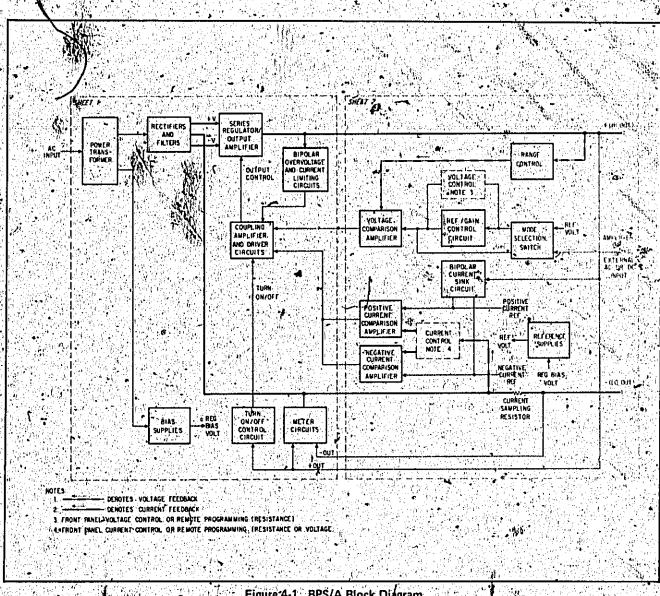


Figure 4-1. BPS/A Block Diagram

#### **OVERALL DESCRIPTION**

#### GENERAL 4.2

4-3 The following paragraphs provide an overall description of Bipolar Power Supply/Amplifier, Models 6826A and 6827A. The BPS/A can be operated as a pover supply or a power amplifier. As a power supply, the BPS/A provides a precise low noise, low drift bipolar output voltage. The output voltage can be varied from positive to negative continu-

pusly through zero using the front panel VOLTAGE control or a remote programming control. A crossiver feature automatically changes the supply mode from constant voltage to constant current. Constant voltage (CV)/constant current (CC) operation is described in Paragraph 4-15. The BPS/A is also capable of sinking current; that is, current from an active load can flow back into the BPS/A when the butput terminal is positive or current can flow out of the output ter minal when the voltage is negative. The BPS/A can sirk current up to 50% of the rated current output. The BPS/A can

also function as a variable gain or fixed gain amplifier to amplify externally applied dc and ac signals. The variable gain can be controlled locally (front panel VOLTAGE control) or remotely and is accurate to within 0.1%. The variable gain amplifier is non-inverting and has a frequency response from dc to 15kHz. Total harmonic distortion is less than 0.1%. The fixed gain amplifier is inverting and has a frequency response from dc to 35kHz.

#### 44 BLOCK DIAGRAM DESCRIPTION

4-5 Figure 4-1 is a basic block diagram of the BPS/A showing the major circuit blocks, together with the principle input/output signals of each block. The sheet numbers correlate the blocks shown on this diagram with the schematic sheets at the rear of the manual. The following descript, tion pertains to BPS/A Models 6826A and 6827A.

The ac line voltage is applied to the power transfor-4.6 mer and, after being altered in level, is rectified and filtered." The resulting raw dc of both polarities is fed to the series regulator/output ampliffer, which varies its conduction (positive or negative) in response to feedback signals to provide. the proper output voltage or current. During power supply operation, this circuit functions as a series regulator to provide the proper output voltage. During amplifier operation it acts as an output amplifier to provide the proper gain for externally applied ac or dc signals. The MODE switch allows selection of the power supply mode or amplifier mode (fixed or variable gain). The series regulator/output amplifier is part of a feedback loop consisting of the ampliffer and driver circuits, and the voltage and current comparison amplifier circuits.

4-7 The amplifier and driver circuits receive an error signal from the voltage or current comparison amplifiers in order to control the conduction of the series regulator/ output amplifier transistors. A positive or negative going error signal is amplified by the appropriate amplifier and driver transistors (positive or negative) and then fed back to control the appropriate series regulator/output amplifier transistors.

4.8 During constant voltage operation, the voltage comparison amplifier compares a portion of the output voltage (feedback) with a reference voltage. In the power supply or variable gain amplifier mode, the reference voltage is received from the reference/gain control-circuit. In the fixed gain amplifier mode, the reference voltage is an externally applied ac or dc signal. If the feedback and reference voltages are not equal, the voltage comparison amplifier protudes an amplified error signal which is further amplified by the low level amplifier and driver circuits and then fed to the series regulator/output amplifier to control the output. In this manner, the voltage comparison amplifier maintains a constant output voltage and also generates the signal necessary to set the output level according to the reference voltage or the externally applied ac or dc signal. Note that the output voltage feedback signal is applied to the voltage comparison amplifier via a range control circuit. This circuit provides the proper scaling of the output in the high and low output ranges.

In the power supply mode, the voltage comparison 4.9 amplifier and output amplifier (amplifiers, drivers, and series regulator) blocks can be viewed as a power operational amplifier whose inputs consist of the feedback signal and a control signal from the reference/gain control circuit block. The control signal is derived from an internal dc reference voltage which is applied to the reference/gain control circuit via the MODE selection switch." As the result of a summing action, a bipolar output can be obtained whose magnitude and polarity depend only upon the setting of the VOLTAGE control (or remote programming resistance) connected across the reference/gain control circuit (refer to Paragraph 4-43 for a detailed description of this circuit). In the variable gain amplifier mode, an external dc or ac signal is applied to the reference/gain control circuit via the MODE switch. For variable gain amplifier operation, the magnitude of the output depends upon the setting of the VOLTAGE control (or remote programming resistance) and the polarity of the output is the same polarity as the input signal. In the fixed gain amplifier mode, an external ac or dc signal is applied to the voltage comparison amplifier via the MODE switch (the reference/gain control circuit is bypassed). For fixed gain amplifier operation, the output signal is inverted. The range control circuit in the voltage feedback path allows high or low range scaling of the output in all three modes of operation. The range control circuit may be controlled locally (front panel RANGE control) or remotely (rear terminal strip). The range control circuit is described in detail in Paragraph 4-47.

4-10 The current comparison amplifiers control the switching of BPS/A operation between constant voltage. and constant current (see Paragraph 4-15) and provide a constant current/output when the BPS/A is operating as a constant current source. During constant current operation, positive and negative current comparison amplifiers detect any difference between the voltage drop across the current sampling resistor and a fixed stable reference. The voltage across the sampling resistor is applied to the amplifiers through the front panel CURRENT control or remote curent programming control. Any change in load current whether by variation of the CURRENT control resistance (or remote current programming input) or by changes in the current through the current sampling resistor causes an error voltage proportional to the current to be applied to the amplifier and driver circuit.

Consequently, the series regulator/output amplifier conduction will be altered thereby restoring the load current to some initial value. Either the positive or the negative current comparison amplifier can be in control depending upon the polarity of the current.

4.11 The bipolar overvoltage and current limiting circuits monitor the output voltage and current. The voltage limiting circuit prevents the output voltage from exceeding approximately 10% of the maximum rated output voltage. The current limiting circuit limits the output current to a value approximately two times the nominal rated output in order to protect the instrument during the transition from constant voltage to constant current operation.

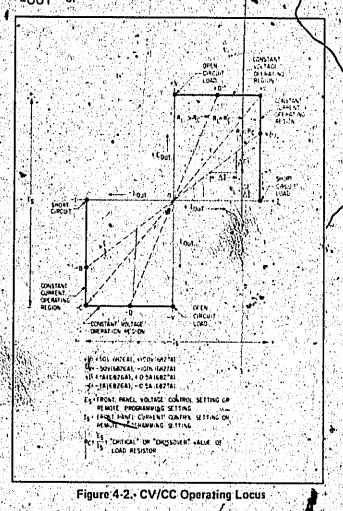
4-12 The turn-on/off circuit protects the load from power/jurn-on and turp off transients by shorting the BPS/A output and disabling the amplifier and driver circuits during turn-on and turn-off.

4-13 4<sup>4</sup>. The bias supply converts the ac input to regulated dc voltages which are used throughout the instrument for biasing purposes. Also, the reference voltages used in the voltage and current comparison circuits are derived from the bias voltage. In addition, the bias supply provides the voltage to operate the turn-on/off circuit.

4-14 Meter circuits are provided for monitoring the BPS/A output voltage and current (ac and dc). Compensation circuits are included for meter loading effects.

#### 4-15 CONSTANT VOLTAGE/CONSTANT CURRENT OPERATION

4-16 In order to maintain a constant voltage output, the voltage comparison amplifier tends to achieve zero output impedance by altering the output current whenever the load resistance changes. In order to maintain a constant output current, the current comparison amplifiers attempt to achieve infinite output impedance by changing the output voltage in response to any load resistance variations. Thus, it should be noted that the voltage and current comparison amplifiers cannot operate simultaneously. For any given value of load resistance, the BPS/A must act either as a constant voltage or a constant current supply. Transfer between operation is accomplished automatically by switchable decoupling circuits at a value of load resistance equal to the ratio of the output voltage control (VOLTAGE control or remote voltage programming control) setting and the current control (CURRENT control or remote current programming control) setting. Figure 4-2 shows the output characteristics of a constant voltage/constant current power supply when operated within the bipolar output voltage and current ranges. With no load attached (RL =  $\infty$ ), IOUT = 0, and EOUT = ES, the front panel voltage or remote programming control setting. When a load resistance is applied to the output terminals of the power supply, the output current increases, while the output voltage remains constant; point D thus represents a typical constant voltage operating point. Further decreases in load resistance are accompanied by further increases in IOUT with no change in the output voltage until the output current reaches IS, a value equal to the front panel current or remote programming control setting. At this point the supply automatically changes its mode of operation and becomes a constant current source; still further decreases in the value of load resistance are accompanled by a drop in the supply output voltage with no accompanying change in the output current value. With a short circuit across the output load terminals; IOUT = IS and EOUT = 0.



4.17 Thus, at voltage and current settings within the bipolar ranges, the "crossover" value of load resistance can be defined as  $R_{C} = E_{S}/I_{S}$ . Adjustment of the voltage and current settings permits this "crossover" resistance  $R_{C}$  to be set to any desired value within the rating of the instrument. If the magnitude of  $R_{L}$  is greater than  $R_{C}$ , the supply is in constant voltage operation.

### 4-18 DETAILED CIRCUIT DESCRIPTIONS

#### 4-19 GENERAL

The following paragraphs provide detailed circuit . 4-20 descriptions of BPS/A Models 6826A and 6827A. Baragraphs 4-21 through 4-66 cover Model 6826A and Paragraphs 4-57 through 4-78 cover the 6827A. The descriptions are based on simplified schematic of Figure 7-1, and the detailed schematics of Figure 7-2 (6826A) and Figure 7-3 (6827A). The simplified schematic pertains to both models and illustrates, in simplified form, the circuitry depicted on Figures 7-2 and 7-3. The sheet numbers on Figure 7-1 correlate the simplified circults with the circuits on the detailed schematics. The simplified schematic is provided for ease of understanding and should be referred to in conjunction with the appropriate detailed schematic. Each detailed schematic consists of two sheets; sheet 1 illustrates the output power amplifier and input power circuits, and sheet 2 illustrates the voltage and current control circuits. To avoid redundancy, similar circuits are described once and the differences between the models are noted.

#### 4-21 MODEL 6826A OUTPUT POWER AMPLIFIËR CIRCUITS (Figure 7-2, Sheet 1)

4-22. AC Input: AC input power is applied to the chassis mounted power transformer T1 via the power module on the rear of the unit and the LINE ON switch S1 on the front panel. The power module contains fuse F1 (2A for 115Vac or 1A for 230Vac input power) and a slide switch for connecting 115 or 230Vac to the primary of the power transformer. The power transformer secondary provides the proper magnitude ac inputs to the rectifier filter and to the bias supply.

4:23 Rectifier-Filter. The rectifier-filter circuits contained on the interconnect and power supply board A1, provide the main dc power outputs. These circuits consist of rectifier diodes arranged in full-wave center-tapped rectifier configurations with associated filter capacitors and bleeder resistors to provide ±65 and ±80 voltraw dc outputs. The front panel LINE ON indicator DS1 is connected across the +65 volt output to indicate when the BPS/A is turned on. The ±65 volt outputs are the main input lines to the series regulator/output amplifier. The ±80 volt outputs are the bias supply voltages for the amplifier and driver circuits on board A3.

4-24 Bias Supply. The bias supply circuit provides stable ±15 volt outputs which are used throughout the instrument for biasing purposes and to develop the reference voltages. The bias supply also provides 20 volt (filtered and unfiltered) auxiliary outputs. Two series regulator type circuits maintain the ±15 volt outputs constant. Since the circuits are identical, only the +15 volt circuit is discussed. Transistor A102 is a voltage comparison circuit that compares the +15 volt output with a fixed reference voltage. The +15 volt output is applied to the base circuit of A102 through resistors A1R29 and A1R30, whereas the reference voltage is furnished in the emitter circuit by A1VR1. If the +15 volt output changes, voltage comparator A102 produces an error signal which is applied to the base of series regulator A101. The error signal causes A101 to change its conduction so as to correct the output voltage.

4-25 Series Regulator/Output Amplifier. NPN power transistors Q1 through Q4, mounted on the heat sink assembly, are utilized as series regulators during power supply operation and as a single ended push-pull amplifier during amplifier operation.

4-26 During power supply operation, parallel connected transistors Q1, Q2, and Q3, Q4 serve as series control elements in the positive and negative output lines, respectively. The series regulators are controlled by the positive and negative driver circuits on board A3. When the positive driver circuits are in control, the series regulators Q1 and Q2 are conducting and the series regulators Q3 and Q4 are turned off. (For this condition, the supply furnishes a positive output. The reverse is true when the negative driver circuits are in control, the supply furnishes a positive output. The reverse is true when the negative driver circuits are in control, Q3 and Q4 are turned off, and the supply provides a negative output.

4.27 Note that NPN power transistors Q1 and Q2 and associated NPN driver transistors A3Q12 and A3Q14 through A3Q16 are connected as cascaded emitter followers which respond to a positive going signal. In order to respond to negative going signals, NPN power transistors Q2 and Q3 are connected with PNP driver transistors A3Q13 and A3Q17 through A3Q19 in a pseudo PNP configuration using local feedback. This configuration allows NPN power transistors to be employed as series control elements for negative outputs.

4-28 During amplifier operation, the transistors serve as a single-ended, push-pull output amplifier. Although the schematic shows Q1, Q2 and Q3, Q4 drawn as a conventional series regulator, the circuit could be redrawn as a pushpull amplifier without changing any of the connections. The output amplifiers are biased for class AB operation and are connected in a complementary configuration.

4-29 Coupling Amplifier and Driver Circuits. The coupling amplifier and driver circuits on board 'A3 amplify the error signal received from the voltage and current control circuits on board A2. This amplified signal controls the conduction of the veries regulator/output amplifier transistors, thus controlling the amplitude and polarity of the BPS/A output. The amplifier and driver circuits consist of positive amplifier and driver stages (Q6-Q8, Q12, Q14-Q16), and negative amplifier and driver stages (Q9-Q11, Q13, Q17-Q19) on board A3.

4-30 The error signal from the voltage or current control circuits is applied to the positive and negative voltage control amplifier circuits on board A3. For a positive going control signal the positive amplifier conducts more and the negative amplifier less. The reverse is true for a negative going control signal. Since the positive and negative sections of the amplifier and driver are symmetrical, only the positive section is discussed in detail.

4-31 The positive voltage coupling amplifier is comprised of transistor stages Q6, Q7 and Q8. Coupling amplifier stage Q7 serves as a "level changing" transistor coupling the error signal to the output driver circuits. The gain of the coupling amplifier is about 1.6X. Notice that the supply voltages for the input circuits are low level and referenced to 2 common (see Figure 7-1, sheet 2). The other amplifier and driver stages; however, use high-level supply voltages (±65 and ±80V) that are referenced to U common. Transistor Q8, in the emitter circuit of coupling amplifier Q7, serves to minimize unwanted ground current from flowing in the low output sense terminal. The negative going output of coupling amplifier stage Q7 is applied to voltage amplifier Q6. The positive (Q6-Q8) and negative (Q9-Q11) coupling amplifiers provide a combined gain of approximately 36X. Each section (positive and negative) provides a gain of approximately 18X. As a result of the voltage amplification, the voltage across R28 biases the positive (NPN) driver transistors (Q12, Q14 through Q16) into conduction provided that a turn-on condition is present (see Paragraph 4-35). The positive driver transistors drive the positive series regulator/output amplifier transistors Q1 and Q2. These transistors are connected in series with the +65V supply voltage and thus control the BPS/A output. Capacitors C9, C10 and resistor R27 form networks which in addition to capacitor C11 connected between the HI and LO output terminals help to shape and stabilize the BPS/A output response. Additional local stabilization is afforded by network (C14, R53) in the positive driver circuits, and network (C15, R55) in the negative driver circult.

4-32 The negative section of the power amplifier operates in the same manner as that described above except that it is activated by negative going error signals and provides negative BPS/A-outputs. The negative section is comprised of negative voltage coupling amplifier stages (Q9-Q11), and negative (PNP) driver transistors (Q13, Q17 through Q19).

4.33 At zero output voltage; both the positive and negative driver sections are conducting a small current through diodes CR14, CR15; and CR16 to provide the voltage drop necessary to forward bias Q12 and Q13 simultaneously. This eliminates "dead spots" when the BPS/A is programmed; through zero: 4-34 Bipolar Overvoltage and Current Limiting Circuits. The bipolar overvoltage and current, limiting circuits are located on board A3. Zener diodes, VR1 and VR2, connecred in the base circuits of Q12 and Q13, prevent the output voltage from exceeding approximately 255 volts. Diodes CR20, CR21, and CR22 form current limiting circuits. These diodes monitor the output current flowing through the series regulator/output amplifier and limit, the transient current to a value approximately 2 times the nominal rated output during the transition from the constant voltage toconstant current operation.

4-35 Turn On/Off Circuit. The turn on/off circuit is comprised of transistor stages Q1 through Q5 on board A3 and relay K1 on board A1. The purpose of this circuit is to limit turn on/off transients which might affect the load. To accomplish this, the output is clamped at a low level when the BPS/A is turned on or off.

Before power is applied to the BPS/A, relay A1K1 4-36 is deenergized connecting the HI OUT (+) to LO OUT (-) line via 🕐 common through resistor R60 (1Ω, 3W) Also, with A1K1 deenergized, an open circuit is present at the emitter of A3Q1. When power is applied, relay A1K1 will not become energized for approximately 0.2 seconds due to RC time constant (R32, R37, C2). Thus, the open circuit condition is present at the emitter of A3Q1 at initial turn-on. The +20V (unfiltered) supply voltage, however, causes transistors A3Q4 and A3Q5 to be forward biased. Gonsequently, transistors A302 and A303 are turned on drawing current away from the bases of driver transistors A3Q12 and A3Q13 respectively, effectively turning thesestages off. After the delay (approximately 0.2 seconds) has elapsed, relay ATK1 becomes energized removing the ... common path to the HI OUT terminal and connecting, (2) common to the emitter of A3Q1 causing the collector of A3Q1 to drop to about 0.1V. For this condition, the for ward bias for transistor A3Q4 is removed causing A3Q4 to turn off which in turn causes transistors A302 and A303 to turn off removing the clamping action at the bases of A3012 and A3013. Driver transistor A3012 or A3013 will now conduct depending upon the magnitude and pula ity of the error signal:

turning these stages off during the decay of stored voltages.

Meter Circuits. The meter circuits provide contin-4-38 uous indications of output voltage and current. VOLTAGE-METER MI's connected across the BPS/A output and can be used to monitor ac or dc output voltage depending upon the position of switch A1S1. With A1S1 in the AC position, diode-A1CR20 rectifies the ac output voltage in order to obtain an rms reading. Variable resistors A1R8 (dc adjust) and A1R13 (ac adjust) are used when calibrating the voltmeter, CURRENT-METER M2 is connected across the current sampling resistor A2R27 whose voltage drop is proportional to the output current. Meter M2 can measure ac on dc output current depending upon the position of switch A1S2. With A1S2 in the ac position, current meter driver A2U5 and diode,CR18 amplify and rectify the ac input (applied through C13) In order to obtain an rms reading. Variable resistors A1R20 (dc adjust) and A1R18 (ac adjust) are used when calibrating the ammeter.

4-39 Switches A1S1 and A1S2 are arranged to allow the meter circuits to provide indications of output voltages and currents when the RANGE switch is used to scale the output by 10:1. Each switch provides two ranges, with a 10:1 ratio for each of the dc and ac functions. Resistor R54 is a thermistor which in conjunction with R55 compensates for temperature effects:

#### 4-40 MODEL 6826A VOLTAGE AND CURRENT CONTROL CIRCUITS (Figure 7-2, Sheet 2)

4-41 The voltage control circuits consist of the mode selection, voltage reference/gain control, and voltage comparison circuits. The current control circuits consist of positive and negative current comparison circuits. Each of these main circuits and associated components are described in the following paragraphs.

4-42 Mode Selection. The front panel MODE switch (sections A5S2A and A5S2B) allows the selection of the power supply, variable gain amplifier, or fixed gain amplifier operating mode. In the power supply mode, a positive dc reference voltage is converted to a variable bipolar dc output voltage by operational amplifier techniques. In the variable gain amplifier mode, an externally applied dc or ac signal is attenuated or amplified by the voltage reference/gain control circuit for application to the voltage comparison amplifier. In the fixed gain amplifier mode, the voltage reference/gain control circuit is bypassed and an externally applied dc or ac signal is applied directly to the voltage comparison amplifier. Each of the above conditions is described in subsequent paragraphs.

4-43 Voltage Reference/Gain Control Circuit. In the power supply mode, voltage reference/gain control amplifier

A2U2 provides a signal (0 to -10V) at the junction of A2R6 and A2R7 depending upon the setting of the front panel VOLTAGE control A5R2 (or remote programming input). With rear terminals A8 and A9 shorted and A10 open, local control is allowed through A5R2. Remote control is allowed by connecting a programming resistance between A9 and A10 with A8 open.

4-44 A fixed +5V reference voltage, derived from the +15V regulated bias supply and zener diode A2VR4, is applied to the inverting input (pin 2) of A2U2 through section S2A of the MODE switch. Depending upon the front panel VOLTAGE control (A5R2) setting (or remole programming input), A2U2 provides a 0 to -10V output. This output is summed at the junction of A2R6 and A2R7 with the +5V reference which is applied through section S2B of the MODE switch. This summing action provides a variable bipolar voltage output.

4-45 In the variable gain amplifier mode, the +6V reference is removed and an external signal (dc or ac), applied to the HI IN (A1) and LO IN (A2) terminals, is fed to the inverting input of A2U2. For this mode, the VOLTAGE control A5R2 (or remote programming input) controls the gain of A2U2 from 0 to 2X and summing with the dc references is not performed.

4-46 Diodes A2CR1 and A2CR2 limit the maximum input to the A2U2 amplifier protecting it from excessive voltage excursions. Variable resistors A1R1 (V ZERO on front panel), A2R58 (course adjustment), and A2R59 (fine adjustment) in the reference voltage circuits are used to calibrate zero output voltage and the reference voltages.

4-47 Voltage Comparison Amplifier. Voltage comparison amplifier A2U1 continuously compares the output voltage with a reference voltage. The inverting input (pin 2) of A2U1 is the summing point which receives a portion of the output voltage (feedback voltage) from the (+S) terminal and the variable reference voltage from A2U2 or from the HI and LO IN terminals (A1 and A2). The non-inverting input (pin 3) of A2U1 receives a fixed dc blas: If a difference exists between these inputs, the comparison amplifier produces an "error" voltage at pin 6 whose amplitude is proportional to the difference. The error signal is then applied to the series regulator/output amplifier via the coupling amplifier and driver circuits. The feedback voltage is applied to the summing point (pin 2 of A2U1) from the high sense, terminal (+S) via a range network consisting of resistors A2R16, A2R42 and relay A2K3. Relay A2K3 changes the range of the power amplifier by changing the feedback resistance by a factor of 10. In the X10 range resistors A2R16 and A2R42 are in the feedback path. In the X1 range resistor A2R42 is shorted out. Relay

A2K2 switches in the proper value equalizing network for each range; A2C7 and A2R14 in the X10 range or these components in parallel with C6 and R15 in the X1 range. Relays A2K2 and A2K3 are controlled by the RANGE switch A5S2C (positions X1, X10, or REMQTE). In the X10 position, the junction of A2K2, K3, and CR4 anode is removed from [2] return which disables the relays to their normally open condition. However, with A5S2C in the X1 position, the return to: [2], is completed and the relays are activated from the +15Vdc blas supply. With RANGE switch A5S2C in the REMOTE position, remote selection of the X1 or X10 range is allowed via rear terminal A11.

448 Changes in the error signal magnitude and polarity instantaneously cause the summing point potential to change. This change causes comparison amplifier A2U1 to provide the proper correction voltage to the low level amplifier and driver circuits. The correction voltage levels at the low level amplifier input are from approximately -2.5V to --4.5V and correspond to the output voltage range of +50V to -50V. A correction voltage of approximately -3.5V corresponds to an output voltage of OV. Zener diode A2VR8, diodes A2CR20, and CR21, and resistor A2R41 prevent A2U1 from going deep into saturation. Diodes A2CR18 and A2CR19 limit the maximum input to the comparison amplifier thus protecting it from overvoltage conditions. Variable resistors A2R60 and A2R61, connected to the +6.2V and -6.2V reference voltage circuits through resistors A2R36 to A2R39 and A2R51 through A2R54, are used for output zero and offset adjustments, Relay A2K1 opens the input path to A2U1 when the BPS/A is remotely controlled and the programmed data is changed, thus, preventing data transients from affecting the output voltage. The AUTO/LOCAL switch A2S1 in the feedback loop is normally left in the LOCAL position. The AUTO position is used for auto-series or auto-parallel operation when the summing junction of the error amplifier must be available for external error signal connections from other units.

4-49 Output Voltage/Gain Control Summary. As stated previously, the BPS/A output voltage is developed utilizing operational amplifier techniques. In the power supply mode, the bipolar output characteristic is developed through the summing of the internal fixed reference voltage (VREF) and a a voltage which is dependent only on a single programming control (VOLTAGE control A5R2 or a remote programming resistance). EQ is given by the following equations for the X1 and X10 ranges:

$$E_{O} = +V_{REF} \left( \frac{R_{PV}}{A1R42} \right) \times \frac{R_{F}}{A2R6} \left( -V_{REF} \left( \frac{R_{F}}{A2R7} \right) \right)$$
where:

Rpv = 0 to 20.48KΩ' (front panel VOLTAGE control or remote-programming resistance)

In the X1 range:  

$$E_{O} = 5.12V \left( \frac{R_{PV}}{10.24K} \times \frac{10.24K}{10.24K} \right) - 5.12V \left( \frac{10.24K}{10.24K} \right)$$
  
 $E_{O} = 5|12V'| \left( \frac{1}{10.24K} - 1 \right)$   
therefore;  $E_{O} = -5.12V$ , if  $R_{PV} = 0$   
 $E_{O} = 0$ , if  $R_{PV} = 10.24K$   
 $E_{O} = +5.12V$ , if  $R_{PV} = 20.48K$ 

In the X10 range:

th

$$E_{O} = 5 \cdot 12V \left( \frac{R_{PV}}{10.24K} \times \frac{102.4K}{40.24K} \right) - 5 \cdot 12V \left( \frac{102.4K}{10.24K} \right)$$
  

$$E_{O} = 5 \cdot 12V \cdot \frac{R_{PV}}{1.024K} - 10 \cdot \frac{102.4K}{10.24K} - 10 \cdot \frac{102.4K}{10.24K} - 10 \cdot \frac{102.4K}{10.24K} - 10 \cdot \frac{102.4K}{10.24K} - \frac{102.4K}$$

4-50. In the variable gain amplifier mode, the BPS/A controls the gain of an externally applied dc or ac signal. For this mode, the internal fixed dc reference voltage is disconnected and the reference/gain control circuit attenuates or amplifies the externally applied signal from 0 to 2X depending upon the setting of the VOLTAGE control A5R2 (or remote programming resistance). The feedback resistor(s) provide a gain of 1 in the X1 range and a gain of 10 in the X10 range. Consequently, the variable gain is from 0 to 2X in the X1 range and from 0 to 20X in the X10 range. In the fixed gain applifier mode, the gain is controlled only by the feedback resistor(s) which provide a gain of 1X in the X1 range and 10X in the X10 range.

4-51 Current Comparison Amplifiers. Current comparison amplifiers A2U3 (positive) and A2U4 (negative) control BPS/A operation between constant voltage and constant current by continuously monitoring the voltage drop across the current sampling resistor (A2R27). This voltage drop is applied to the current comparison amplifiers via the front panel CURRENT control A5R1 of the remote programming input terminals. The other input to the current comparison amplifiers is a stable fixed reference current. Any disturbance in load current whether by variation of the CURRENT control (or remote programming input) or in the current flow through the sampling resistor (as in line or load changel will cause a corrective voltage to alter the appropriate series

regulator (positive or negative) conduction theraby restoring the load current to some initial value.

4.52 Positive current comparison amplifier A2U3 monitors positive output currents and negative current comparison amplifier A2U4 monitors negative output currents. These amplifiers control switching the BPS/A between constant voltage and constant current operation. In constant voltage operation, they are in saturation, reverse blasing A2CR13 and A2CR14 and preventing any current control action. In constant current operation, they become linear comparison amplifiers allowing BPS/A operation as a constant current source. Also, for current sink conditions, they limit the output current to 1/2 maximum rated output through separate control circuits consisting of A2CR3, CR4, CR7, CR8, CR11, CR12, R28, and R29. Because the two comparison amplifiers are similar, only the positive current comparison amplifier is described in detail.

4.53 The voltage drop across the current sampling resistor A2R27 is applied to pin 3 of A2U3 via the front panel CURRENT control A5R1 (or the remote programming input terminals). Current control through A5R1 (local control) is achieved with rear terminals A12, A13, and A14 strapped together for positive currents and with A19, A20, and A21 strapped together for negative currents. External digital resistance control can be implemented by connecting the proper resistances between A13, A14 (strapped together) and A18 for positive currents, and between A20, A21 (strapped together) and A18 for negative currents. Another method of control of the current is through voltage programming via terminals A14 and A18 and A20 and A18 for positive and negative currents respectively.

A fixed reference current is applied to the other. 4-54 input 1pin 2) of A2U3. During constant voltage operation, A2U3 is saturated causing the output to be positive. Zener diode A2VR5 and piode A2CR5 clamp the output at +7.5V preventing A2U3 from going too far into saturation. For his condition, diode A2CR14 is back biased and PNP. witching transistor A205 is turned off causing A2CR9 and A2CR10 to be forward blased. With A2CR14 back biased, constant voltage operation is enabled and constant current operation is disabled (the negative constant current diode A2CR13 must also be back blased for this condition). With A2CR9 forward biased, transistor A101 is turned-on allowing capacitor A2C9 to charge during constant voltage operation. This will speed up the transition from constant voltage to constant current operation. With A2CR10 forward biased, the CURRENT MODE indicator DS1 is off (A2O2 turned-on and A203 turned-off) and the FLAG output is disabled flow level, FLAG output with A2Q4 turned-on). Networks consisting of A2C11, R63, R64 and A2C12, R65, R66 are included in the inputs of A2U3 and A2U4 respectively. These networks IN conjunction with local compensation represented by A2C9, R46, and R47. (common to both A2U3 and A2U4) provide response stabilizing compensation.

4-55 If the output current increases above the set value, the input to pin 3 of A2U3 becomes less positive. For this condition, the output (pin G) of A2U3 goes negative forward biasing A2CR14. With A2CR14 forward biased, the BPS/A switches from constant voltage to constant current operation and an error signal is applied to alter the series regulator (positive) conduction and maintain the output current at the desired value. Also, for this condition, A2O5 is switched on back biasing dlodes A2CR9 and A2CR10. With A2CR9, back biased, A2O1 is turned-off. With A2CR10 back biased, the CURRENT MODE indicator DS1 lights (A2O2 off, A2O3 on) and the FLAG output is enabled (A2O4 turned off providing a high FLAG output).

During current sinking operations, the input to 4-56 A2U3 (negative voltage case) is altered causing the current being sinked to increase or decrease in response to the voltage magnitude of the active load. When the output voltage is negative, diodes A2CR3 and A2CR12 become forward biased through A2R28 altering the reference current to A2U3. This condition in conjunction with the voltage change across A2R27 will cause the output of A2U3 to adjust the drive to the appropriate output transistors to limit the imposed load current. The operation of A2U4 is similar in principle for the positive voltage case. Front panel controls +I ZERO (A1R2) and - I ZERO, (A1R3), in the positive and negative current reference circuits are used to adjust the respective zero for programming accuracy Variable resistors A1R19 and A2R21 are used to calibrate the positive and negative current references.

#### 4-57 MODEL 6827A OUTPUT POWER AMPLIFIER CIRCUITS (Figure 7-3, Sheet 1)

4-58 AC Input. AC input power is applied to the chassis mounted power transformer T1 via the power module on the rear of the unit and the LINE ON switch S1 on the front panel. The power module contains fuse F1 (2A for 115Vac or 1A for 230Vac input power) and a slide switch for connecting 115 or 230Vac to the primary of the power transformer. The power transformer secondly provides the proper magnitude ac inputs to the rectifier filter and to the bias supply.

4-59 Rectifier-Filter. The rectifier-filter circuits, contained on the interconnect and power supply board A1, provide the main dc power outputs. These circuits consist of rectifier diodes arranged in full-wave center-tapped rectifier configurations with associated filter capacitors and bleeder resistors to provide ±140 and ±155 volt unregulated

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dc outputs. The front panel LINE ON indicator DS1 is connected across the  $\pm 140$  volt output to indicate when the BPS/A is turned on. The  $\pm 140$  volt outputs are the main input lines to the series regulator/output amplifier. The  $\pm 155$  volt outputs are the bias supply voltages for the amplifier and driver circuits on board A3.

4-60 Bias Supply. The bias supply circuit provides stable ±15 volt outputs which are used throughout the instrument for biasing purposes and to develop the reference voltages: The bias supply also provides 20 volt (filtered, and unfiltered) auxiliary outputs. Two series regulator circuits maintain the ±15 volt outputs constant: Circuit operation is identical to that described in Paragraph 4-24 for Model 6826A.

Series Regulator/Output Amplifier. NPN power 4-61 transistors Q1 through Q6, mounted on the heat sink assembly, are utilized as series regulators during power supply operation or as a power amplifier during amplifier operation. During power supply operation, transistors Q1 through Q6 serve as series control elements. Power transistors Q1 through Q3 are connected in series with the +140V supply and Q4 through Q6 are connected in series with the -140V supply. The conduction of Q3 or Q4 is controlled directly by the output of driver stages A3Q12 (positive) or A3Q10 (negative) depending upon which section is active at the time. The conduction of the other series regulator transistors Q1, Q2 or.Q5, Q6 is controlled by the positive or negative bias transistors (A3Q17-Q20 or A3Q13-Q16) in response to the output voltage magnitude as determined by the VOLTAGE control or remote resistance setting.>

4-62 Coupling Amplifier and Driver Circuits. The coupling amplifier and driver circuits on board A3 amplify the voltage or current error signal received from the voltage or current control circuits on board A2. This amplified signal controls the conduction of the series regulator/output amplifier transistors, thus, controlling the output of the BPS/A output. The amplifier and driver circuits consist of coupling stages (Q6, Q7), single ended amplifier stage Q8, positive driver stages (Q11, Q12 and Q17 through Q19) and negative driver stages (Q9, Q10, and Q13 through Q16). For positive output voltages, the positive driver stages are conducting and the negative stages are cut off. The reverse is true for negative voltages.

4-63 Coupling amplifier stage Q7 serves as a "level changing" transistor coupling the relatively small error signal level input to the considerably higher output levels used in the driver circuits. Notice that the supply voltages for the Q7 input circuits are low level and referenced to 2 common (see Figure 7-3, sheet 2). The amplifier and driver stages on board A3, however, use high-level supply voltages (±140V and ±155V) that are referenced to 1 common. Transistor Q6, in the emitter circuit of coupling amplifier Q7, serves to minimize unwanted ground current from flowing in the low output sense terminal.

4.64 Transistor Q8 is a voltage amplifier, having a gain, of approximately 50X, while the driver stages provide most of the current gain of the power amplifier. Hence, the voltage at the bases of positive (Q11) and negative (Q9) input stages is essentially equal to the output voltage of the BPS/A. Q8, together with VR3, R31-R33, CR11, CR12, CR15, CR16 and VR4 form a voltage divider in the base circuits of input stages. The conduction of Q8 controls the current flowing through the voltage divider and, thus, the bias at the bases of Q11 and Q9. Consequently, Q11 or Q9 is driven into conduction provided that a turn-on condition is present (see Paragraph 4-69):

4.65 Driver stages Q12 (positive) and Q10 (negative) drive the power output transistors Q1-Q3 and Q4-Q6 respectively. The conduction of power output transistor Q3 or Q4 is controlled directly by the output of Q12 or Q10 depending upon which driver is active at the time. The conduction of the other series power output transistors (Q1, Q2 or Q5, Q6) is controlled by the positive or negative bias transistors (Q17-Q20 or Q13-Q16).

The function of the blas networks is to divide the 4-66 voltage drop (and thus the power dissipation) among the three series connected power transistors in the active branch. This is accomplished by sensing the programmed output voltage level and using it to develop two additional voltages; one representing the output voltage plus 2/3 of the difference between 140 volts and the output voltage, and the other representing approximately 1/3 of the same value. For the positive bias network, R56 and R58 develop the 2/3 voltage function while R55 and R57 develop the 1/3 voltage function. (R42, R43, R45, and R46 perform the same function for the negative bias network.) The 2/3 volt age level at the junction of R56 and R58 is power amplified by compound emitter followers Q20 and Q19 and appears at approximately the same 2/3 voltage level at the emitter of power transistor Q1. The 1/3 voltage level is similarly amplified by Q18 and Q17 and appears at the emitter of power transistor Q2. From this it can be seen that 1/3 of the voltage drop between-140 volts and the programmed. voltage level appears across each of the three series connected power transistors, . The negative bias network operates in a similar manner.

4.67 The remaining components of the bias networks improve general circuit operation. Diodes CR18 through CR21 protect the base emitter junctions of the bias transistors from becoming excessively reversed biased. Resistors R44, R47; R50-R53, R59-R63 offset undesireable leakage currents. Capacitors C13, C14, and C17 permit the circuit to respond to rapid changes in programmed output voltage. Capacitors C16, C18 and resistor R70 connected between

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the HI (+) and LO (-) output terminals help to shape and stabilize the BPS/A output response.

4-68 Bipolar Overvoltage and Current Limiting Circuits. Bipolar overvoltage and current limiting circuits are located on board A3. Zener diodes VR5 through VR8 connected in the base circuits of Q11 and Q9 prevent the output voltage from exceeding approximately ±110 volts. Diodes CR22 through CR25 monitor the current flowing through the output power transistors and limit the current to a value approximately 2 times the nominal rated output during the transition from constant voltage to constant current operation.

4-69 Turn On/Off Circuit. The operation of this circuit is exactly the same as described in Paragraph 4-35 for the 6826A model except that the control action, in this case, is to remove drive from A309 or A3011 depending upon the polarity being programmed.

Meter Circuits. The meter circuits provide contin-4.70 uous indications of output voltage and current. VOLTAGE-METER M1 is connected across the BPS/A output and can + be used to monitor ac or dc output voltage depending upon the position of switch A1S1. With A1S1 in the AC position, diode A1CR19 and A1CR20 rectify the ac output voltage in order to obtain an rms feading. Variable resistors A1R8 (dc adjust) and A1R13 (ac adjust) are used when calibrating the voltmeter, CURRENT-METER M2 is connected across the current sampling resistor A2R27 whose voltage drop is proportional to the output current. Meter M2 can measure ac or dc output current depending upon the position of switch A1S2. With A1S2 in the ac position, current meter driver A2U5 and diode CR22, 23 amplifier and rectify the ac input (applied through C13 and CR18) in order to obtain an rms reading. Variable resistors A1R20 (dc adjust) and A1B18 (ac adjust) are used when calibrating the ammeter.

4-71 Switches A1S1 and A1S2 are arranged to allow the meter circuits to provide indications of output voltages and currents when the RANGE switch is used to scale the output by 10:1. Each switch provides two ranges, with a 10:1 ratio, for each of the dc and ac functions. Resistor R54 is a thermistor which in conjunction with R55 compensates for temperature effects.

## 4-72 MODEL 6827A VOLTAGE AND CURRENT CONTROL CIRCUITS (Figure 7-3, Sheet 2)

473 These circuits are similar to the 6826A voltage and control circuits described in Paragraphs 4-40 through 4-56 except for certain component additions and value differences due to the difference in instrument specifications. For example, the values of the feedback resistors (scaling resistors) A2R16 and A2R42 are larger in order to obtain the higher output voltage ranges of the 6827A. As described in Paragraph 4-99, the BPS/A output voltage (EQ), in the power supply mode, is a function of the internal fixed reference voltage (VREF) and the programming control (VOLTAGE control or remote programming resistance). For the 6827A instrument, EQ is given by the following equations in the X1 and X10 output ranges:

$$O_{\tau} + V_{REF} \int \frac{R_{PV}}{A1R42} \times \frac{H_{F}}{A2R6} = V_{REF} \int \frac{h_{F}}{A2R7}$$

where \*\*

Rpv = 0 to 20.48KΩ (front pahel VOLTAGE control of remote programming resistance)

R<sub>F</sub> = feedback resistance (A2R 16 or A2R 16 + A2R42) = 20.48KΩ or 204.8KΩ (X1 or X10 range respectively)

A1R42 = A2R6 = A2R7 = 10.24KΩ, and VREF = 5.12V

In the X1-range

$$E_{O} = 5.12V \left(\frac{R_{PV}}{10.24K} \times \frac{20.48K}{10.24K}\right) - 5.12V \left(\frac{20.48K}{10.24K}\right)$$

$$E_0 = 5.12V \left(\frac{2R_{PV}}{10.24K} - 2\right)$$

herefore; 
$$E_0 = -10.24V$$
, if  $R_{PV} = 0$   
 $E_0 = 0$ , if  $R_{PV} = 10.24K$   
 $E_0 = +10.24V$ , if  $R_{PV} = 20.48K$ 

In the X10 range:

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$$EO = 5.12V \left(\frac{R_{PV}}{10.24K} \times \frac{204.8K}{10.24K}\right) - 5.12V \left(\frac{204.8K}{10.24K}\right)$$

4.74 In the variable gain amplifier mode, the BPS/A controls the gain of an externally applied dc or ac signal. For this mode, the internal fixed dc reference is disconnected and the reference/gain control circuit attenuates or amplifies the externally applied signal from 0 to 2X depending upon the setting of the VOLTAGE control A5R2 (or remote programming resistance). The feedback resistor(s) provide a gain of 2 in the X1 range or 20 in the X10 range. Consequently, the variable gain is from 0 to 4X in the X1 range and from 0 to 40X in the X10 range. 4-75 In the fixed gain amplifier mode of operation, the gain is controlled only by the feedback resistor(s) which provide a gain of 2X in the X1 range and 20X in the X10 range.

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4-76 The operation of the 6827A current comparison amplifier circuits is identical to that described in Paragraphs 4-51 through 4-56 for Model 6826A. Certain components are added (A2R23 and A2R26) in the 6827A model because of the difference in current ratings.

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# SECTION V

## 5.1 INTRODUCTION

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5-2 The performance checks (Paragraph 5-5) should be made to check the operation of the BPS/A after repairs or for periodic maintenance. These checks are also suitable for incoming inspection. If a fault is detected in the BPS/A while making the performance check or during normal operation, proceed to the troubleshooting procedures (Paragraph 5-60). After repair and replacement (Paragraph 5-84), perform any necessary adjustments and calibrations (Paragraph 5/98), Before returning the BPS/A to normal operation, repeat the performance check to ensure that the fault has been properly corrected and that no other faults exist. Before performing any maintenance checks, turn on the BPS/A and, allow a half-hour warm up.

## 5-3 TEST EQUIPMENT REQUIRED

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5-4 Table 5-1 lists the test equipment required to berform the various procedures described in this section.

• Туре		USE .	
Differential Voltmeter	Sensitivity: 500μV full scale (min.). Input impedance: 100MΩ (min.).	Measure dc voltages; calibration procedures. Measure amplifier gain (Option 001).	HP345013 with Option 001
Digital Voltmeter Oscilloscope	Accuracy: 0.004% Sensitivity: 1μV, floating input. Sensitivity and bandwidth: 1mV/ cm and 50MHz.	Measure do voltages, calibration procedures. Measure ripple: display transient recovery waveforms: measure noise spikes. Measure response.	HP3462A or HP3420B HP180A plus 1801A, and 18212 plug-ins./
Function Generator	100Hz/squarewave and sinewave.	Measure frequency response and output impedance.	HP3310A
Distortion	Accuracy: ±3% from 10Hz to 1MHz.	Measure amplifier distortion.	HP331A
Variable Voltage Transformer	Current rating: 2A; Range: 90- 130Vac; Equipped with voltmeter accurate within 1 volt.	Vary ac input for high line to low p line regulation.	
Repetitive Load	Rate: 60-400Hz; 2µsec rise and fall time;	Measure transient response.	See Figure 5-4.
Current Sampling - Resistor	Value: 1Ω ± .1%, 24W	Measure output current, calibrate ammeter.	
Resistive Loads	Valde: See Figure 5-1,±1%, 50W	Load resistors.	
Terminating Resistors	Value: 50 ohms, ½W, ±5%, non- inductive: 4 required.	Noise spike measurement.	
Blocking Capacitors	Valdes: 0.01µF, 100Vdc, 2 required; 1000µF, 60Vdc, 1 required.	Noise spike measurement; output impedance measurement.	
Programming Resistors	5.12K ± .05% 10.24K ± .05% 20.48K ± .05%		0811-2957 0811-2958 0811-2959 (Micro Ohm Type 132F)

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Table 5-1. Test Equipment Required

#### 5-5 PERFORMANCE TEST

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The following test can be used as an incoming in-5.6 spection check and appropriate portions of the test can be repeated either to check the operation of the instrument after repairs or for periodic maintenance tests. The tests are performed using a 115Vac; 60Hz, single phase input power source.

#### POWER SUPPLY MODE TESTS 5-7

58 All measuring devices must be connected to the rear sensing terminals of the supply and not to the front output terminals if maximum accuracy is to be obtained in the following measurements. In addition, the measuring devices must be connected as close to the sensing terminals as possible. This is particularly important when measuring the transient response, regulation, or ripple of the power. supply. Note that under no circumstances should the measuring instruments be connected across the load. A measurement made across the load includes the impedance of the leads to the load and such lead lengths can easily have an impedance several orders of magnitude greater than the supply impedance, thus invalidating the measurement.

5.9 To avoid mutual coupling effects, each monitoring device must be connected to the sensing terminals by a separate pair of leads. Twisted pairs or shielded two-wire cables should be used to avoid pickup on the measuring leads. The load resistor should be connected across the output terminals as close to the supply as possible. When measuring the constant voltage performance specifications, the current controls should be set well above (at least 10%) the maximum output current which the supply will draw, since the onset of constant current action will cause a drop in output voltage, increased ripple, and other performance changes not properly ascribed to the constant voltage operation of the supply.

5-10 DC Voltage Output and Voltmeter Accuracy. To check the DC voltage output and voltmeter accuracy, proceed as follows:

#### NOTE

The CURRENT MODE light should be off during this test.

Connect appropriate high range load resistor а. (RL) across output terminals (see Figure 5-1).

- b. Connect DVM across +S and -S terminals. c. •Set BPS/A front panel controls as follows:
- POWER SUPPLY MODE switch: a **RANGE** switch: X10
- midposition VOLTAGE control:

BPS/A UN	DER TEST			DIGITAL OR DIFFERENTIAL THETEN TOVINI
				<u>}</u>
	RĽ			
	MODEL NO.	RL (O		
	6826A	50		
$\mathcal{C}$	6827A,	200	20	le ser ser ser ser ser ser ser ser ser se
Gaya (ag				

Figure 5-1. Power Supply Mode Test Setup

<b>} •</b> (;			
	CURRENT control	fully clockwise	
	· VOLTAGE METER	: high range DC	
가지 않는 것이다. 이 것은 것이 있는 것이	CURRENT METER		
	I. Turn on BPS/A and	allow a five-minutely	Narin;
	. Turn' VOLTAGE co	1	
indicates i	maximum rated positi 6826A:	ve.output voltage, as 1 +60V	ollows: -
	6827A:	+100V	
<b>ا</b> ر الم	. Observe that front p	anel voltmeter reads	DŞ
follows:			
	6826A:	+50V ± 1.5V +100V ± 3V	$\mathbf{v}_{i_1}$
	6827A: J. Turn VOLTAGE co		co until
DVM indi	cates maximum rated		
follows:			

68	26A:				-50	IV 🔆	
68	27A:		i jî l		-10	0V	
h. Ol	bserve 1	hat fr	ont pa	nel vol	tmete	r read	ls a
		6 <b>1</b> 1	1.2.10	11.1	6 - 6 - 6		

6826A	-50V ± 1.5V	ŕ.
6827A	-100V ± 3V	1. 1.

i. Turn off BPS/A. Change load resistor RL to appropriate low range value (see Figure 5-1) and set RANGE switch to X1.

j. Repeat steps (d) through (h) for following DVM 1. and front panel voltmeter readings (use low range DC scale)

Model Voltmeter	
6826A +5V/-5V +5V±150mV/-5V±1	50mV .
6827A +10V/-10V , +10V±300mV/-10V	1000V

follows:

Source Effect (Line Regulation). Definition: The change  $\Delta E_{OUT}$  in the static value of dc output voltage resulting from a change in ac input voltage over the specified range from low line (usually 104/208 volts) to high line (usually 127/254 volts), or from high line to low line.

### NOTE

## The CURRENT MODE light should be off during this test.

5-12 To check the line regulation, proceed as follows: a. Connect the test setup shown in Figure 5-1. Use high range (X10) load resistor value.

b. Connect variable auto transformer between the input power source and the BPS/A power input.

c. Adjust variable transformer for a 104 volts ac

d. Set BPS/A front panel	controls as follows:
MODE switch:	POWER·SUPPLY
RANGE switch:	X10
VOLTAGE control:	midposition
CURRENT control:	fully clockwise
VOLTAGE METER:	high range DC
CURRENT METER:	high range DC
e. Connect a DVM to the	-S and +S terminals

the BPS/A.

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f. Turn on BPS/A and adjust VOLTAGE control clockwise for maximum rated positive output voltage (high range), +50V (6826A) or +100V (6827A), as indicated on DVM.

g. Adjust variable auto transformer for a 127 volts ac input.

h. Reading on DVM should not vary from reading in step (f) by more than i

6826A	1	10mV	
69274.		20mV	

i. Set variable auto transformer for a 104Vac input. j. Adjust VOLTAGE control counterclockwise, for

maximum rated negative high range output voltage, -50V (6826Å) or -100V (6827Å), as indicated on DVM.

k. Adjust variable auto transformer for a 127Vac input.

I. Reading on DVM should not vary from reading in step (j) by more than:

6826A: 6827A:

20mV

, 10mV

m. Turn off BPS/A and change load resistor to

low range (X1) value, and RANGE switch to X1. n. Adjust variable auto transformer for a/104Vac input. o. Turn on BPS/A and adjust VOLTAGE control clockwise for the maximum rated positive output voltage (low range), +5V (6826A) or +10V (6827A) as indicated on DVM.

p. Adjust variable auto transformer for a 127Vac

q. Reading on DVM should not vary from reading

6826A: 6827A: r. Set variable auto transformer for a 104Vac in-

put. s. Adjust VOL VAGE control counterclockwise for maximum rated negative low range butput voltage, -5V, (6826A) or -10V (6827A), as indicated on DVM.

L Adjust variable auto transformer for a 127Vac

u. Reading on DVM should not vary from reading in step (s) by more than:

6826A:	1	· · ·				l >	$e^{t}$	1mV
, 6827A				1	1			•2mV

Load Effect (Load Regulation). Definition: The change  $\Delta EOUT$  in the static value of dc output voltage resulting from a change in load resistance from open circult to a value which vields maximum rated output current (or vice versa).

## NOTE

The CURRENT MODE light should be off dur-

5-14 The load regulation check is performed at low line conditions. To check load regulation, proceed as follows: a. Connect the test setup shown in Figure 5-1. Use the high range (X10) load resistance value.

b. Connect variable auto transformer between the input power source and the BPS/A power input. Adjust variable auto transformer for a 104Vac input.

c. Set BPS/A front pa	nel controls as follows:
MODE switch	POWER SUPPLY
RANGE switch:	X10
VOLTAGE control	
CURRENT control	
VOLTAGE METE	R: high range DC
CURRENT METE	R high range DC
d. Connect a DVM to	the -S and +S terminal

the BPS/A.

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e. Turn on BPS/A and adjust VOLTAGE control clockwise for the maximum rated positive output voltage (high range), +50V, (6826A) or +100V (6827A), as indicated.

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, on DVM.

f. Disconnect load resistor. Reading on DVM should not vary from the reading in step (e) by more than: 6826A: 6mV

		- <u>-</u>	
6827A:		11	tmV –

g. Adjust VOLTAGE control counterclockwise for maximum rated negative output (high range), -50V (6826A) or -100V (6827A), as indicated on DVM.

h. Connect load resistor (high range value). Reading on DVM should not vary from reading in step (g) by more than:

6826A:	- ni		1			, Gr	n۷		
6827A:		- 11.			N.	11	m	1.	٠.
J	· · · _		 Y 1 2	11	- 11	1.1			· .

range (X1) value and RANGE switch to X1.

(j. Turn on BPS/A and adjust VOLTAGE control clockwise for the maximum rated positive output voltage (low range), +5V (6826A) or +10V (6827A), as indicated on DVM.

k. Disconnect load resistor. Reading on DVM should not vary from the reading in step (j) by more than: 6826A: 0.6mV

6827A: 1.3mV

J. Adjust VOLTAGE control counterclockwise for the maximum rated negative output voltage (low range), -5V (6826A) or -10V (6827A), as indicated on DVM.

m. Connect load resistor (low range value). Reading on DVM should not vary from reading in step (e) by more than:

6826A		0.6m	V
		1.3m	v .
6827A	<b>V</b>	•••••••••••••••••••••••••••••••••••••••	•

5-15 PARD (Ripple and Noise).

Definition: The residual AC voltage which is superimposed on the DC output of a regulated power supply. Ripple and noise may be specified and measured in terms of its BMS or (preferably) peak-to-peak value.

Ripple and noise measurement can be made at any input AC line voltage combined with any DC output voltage and load current within rating.

5-16: The amount of ripple and noise that is present on the power supply output is measured either in terms of the RMS or (preferably) beak to peak value. The peak-to-peak measurement is particularly important for applications where noise spikes could be detrimental to a sensitive load, such as logic circuitry. The RMS measurement is not an ideal representation of the noise, since fairly high output noise spikes of short duration could be present in the ripple and not appreciably increase the RMS value.

5-17 The technique used to measure high frequency noise or "spikes" on the output of a power supply is more

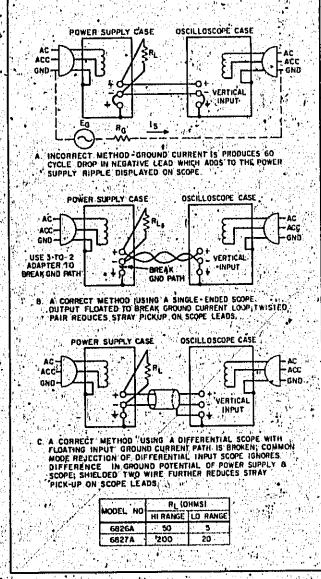


Figure 5-2. Ripple and Noise, Test Setup

critical than the low frequency ripple and noise measure ment technique; therefore the former is discussed separately in Paragraph 5-25.

5-18 Ripple and Noise Measurements. Figure 5-2A shows an incofrect method of measuring p p ripple. Note that a continuous ground loop exists from the third wire of the isput power cord of the supply to the third wire of the input power cord of the oscilloscope via the grounded power supply case, the wire between the negative output terminal of the power supply and the vertical input of the scope, and the grounded scope case. Any ground current circulating in this loop as a result of the difference in potential EG between the two ground points causes an 1R drop, which is in series with the scope liput. This, IR drop, normally having a GOHz line frequency fundamental, plus any pickup on the unshielded leads interconnecting the power supply and scope, appears on the face of the CRT. The magnitude of this resulting noise signal can easily be much greater than the true ripple developed between the plus and minus output terminals of the power supply, and can complifiely invalidate the measurement.

5.19 The same ground current and pickup problems can exist if an RMS voltmeter is substituted in place of the oscilloscope in Figure 5.2. However, the oscilloscope display, unlike the true RMS meter reading, tells the observer immediately whether the fundamental period of the signal displayed is 8.3 milliseconds (1/120Hz) or 16.7 milliseconds (1/60Hz). Since the fundamental ripple frequency present on the output of an HP supply is 120Hz (due to full-wave rectification), an oscilloscope display showing a 120Hz fundamental component is indicative of a "clean" measurement setup, while the presence of a 60Hz fundamental usually means that an improved setup will result in a more accurate (and-lower) value of measured ripple.

5-20 Figure 5-2B shows a correct method of measuring the output ripple of a constant voltage power supply using a single ended scope. The ground loop path is broken by floating the power supply. Note that to ensure that no potential difference exists between the supply and the oscilloscope it is recommended that whenever possible they both be plugged into the same ac power buss." If the same buss cannot be used, both ac grounds must be at earth ground potential.

5-21 Either a twisted pair or (preferably) a shielded twowire cable should be used to connect the output terminals of the power supply to the vertical input terminals of the scope. When using a twisted pair, care must be taken that one of the two wires is connected to the grounded input terminal of the oscilloscope. When using stilleded two-wire, it is essential for the shield to be connected to ground at one end only so that no ground current will flow through this shield, thus inducing a noise signal in, the shielded leads.

5-22 To verify that the oscilloscope is not displaying ripple that is induced in the leads or picked up from the grounds, the (+) scope lead should be shorted to the (-) scope lead at the power supply terminals. The ripple value obtained when the leads are shorted should be subtracted from the actual ripple measurement.

5-23 In most cases, the single-ended scope method of Figure 5-2B will be adequate to eliminate non-real components of ripple and noise so that a satisfactory measurement may be obtained. However, in more stubborn cases it may be necessary to use a differential scope with floating input as shown in Figure 5-2C. If desired, two single conductor

ίų.

shielded cables may be substituted in place of the shielded two-wire cable with equal success. Because of its common mode rejection, a differential oscilloscope displays only the difference in signal between its two vertical input terminals, thus ignoring the effects of any common mode-signal introduced because of the difference in the ac potential between the power supply case and scope case. Before using a differential input scope in this manner, however, it is imperative that the common mode rejection capability of the scope be verified by shorting together its two input leads at the power supply and observing the trace on the CRT. If this trace is not a straight line, then the scope is not rejecting the ground signal and must be realigned in accordance with the manufacturer's instructions until proper common mode rejection is attalned.

5.24 To check the ripple and noise output, proceed as tollows:

a. Connect the oscilloscope or RMS voltmeter as shown in Figures 5-2B or 5-2C. Set MODE switch to POWER SUPPLY. Turn CURRENT control fully clockwise, b. Adjust VOLTAGE control in the X1 and X10 ranges until front panel meter indicates maximum rated output voltage. Check both maximum rated positive and negative output voltages.

c. The observed ripple and noise mould be less than:

Model		<u>X1</u>	Range		/. <u>x10</u>	Range		
6826A	• 2	mVrms	/10mVr	.р./(	SmVrms	/35mV	p-p <sub>3</sub>	<b>.</b> .
6827A			ms/15m					
00217								

5-25 Noise SpiRe Measurement. When a high frequency spike measurement is being made, an instrument of sufficient bandwidth must be used an oscilloscope with a bandwidth of 20MHz or more is adequate. Measuring noise with an instrument that has insufficient bandwidth may concealhigh frequency spikes detrimental to the load.

5-26. The test setups illustrated in Figures 5-2A and 5-2B are generally not acceptable for measuring spikes; a differential oscilloscope is necessary. Furthermore, the measurement concept of Figure 5-2C must be modified if accurate spike measurement is to be achieved: a. As shown in Figure 5-3, two coax cables, must

be substituted for the shielded two-wire cable. b. Impedance matching resistors must be included to eliminate standing waves and cable ringing, and the capacitors must be connected to block the DC current path.

c. The length of the test leads outside the coax is critical and must be kept as short as possible; the blocking capacitor and the impedance matching resistor should be connected directly from the inner conductor of the cable to the power supply terminals.

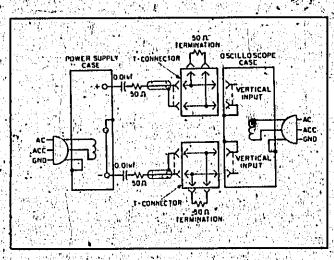


Figure 5-3. CV Noise Spike Test Setup

d. Notice that the shields of the power supply end of the two coax cables are not connected to the power supply ground, since such a connection would give rise to a ground current path through the coax shield, resulting in an erroneous measurement.

e: Since the Impedance matching resistors constitute a 2-to-1 attenuator, the noise spikes observed on the oscilloscope should be less than:

Model No.	X1 Range	X10 Range
6826A	5mVp·p instead of	17.5mVp-p instead
	10mVp·p	of 35mVp p
6827A	7.5mVp-p instead	25mVp-p instead of
지 않는 아님, 아파 아파	of 15mVp p	50mVp-p

Transient Recovery Time. Definition: The time "X" for the output voltage recovery to within "Y" millivolts of the nominal output voltage following a "Z" amp step change in load current, where: "Y" is specified as 50mV (6826A) or 100mV (6827A), the nominal output voltage is defined as the dc level between the static output voltage before and after the imposed load change, and "Z" is the specified load current change of the full load current rating of the supply.

5-27

5-28 Transient recovery time may be measured at any input line voltage combined with any output voltage and load current within rating.

5-29 Reasonable care must be taken in switching the cload resistance on and off. A hand operated switch in series with the load is not adequate, since the resulting one-shot displays are difficult to observe on most oscilloscopes, and the arc energy occurring during switching action completely masks the display with a noise burst.

5-30 A mercury-wetted relay, as connected in the load switching circuit of Figure 5-4 should be used for loading and unloading the supply. When this load switch is connected to a 60Hz ac input, the mercury-wetted relay will open and close 60 times per second. Adjustment of the 25K control permits adjustment of the duty cycle of the load current switching and reduction in jitter of the oscilloscope display.

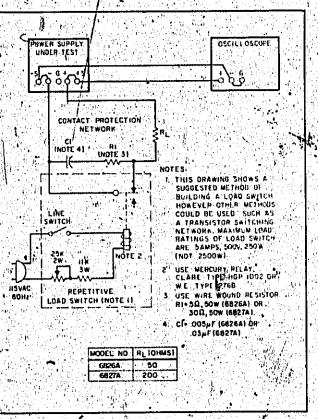


Figure 5-4. Transient Recovery Time Test Setup

5-31 To check the transient recovery time, proceed as follows:

a. Connect test setup shown in Figure 54. Set . MODE switch to POWER SUPPLY and RANGE switch to X10.

b. Turn CURRENT control fully clockwise. c. Turn on supply and adjust VOLTAGE control clockwise until front panel ammeter indicates maximum positive rated output current.

d. Close line switch on repetitive load switch set : up.

e. Set oscilloscope for internal sync and lock on either positive or negative load transient spike.

f. Set vertical input of oscilloscope for ac coupling so that small dc level changes in power supply output voltage

5-6

will not cause display to shift.

g. Adjust the vertical centering on the scope so that the tail ends of the no load and full load waveforms are symmetrically displayed about the horizontal center line of the oscilloscope. This center line now represents the nominal output voltage defined in the specification.

h. Adjust the horizontal positioning control so that the trace starts at a point coincident with a major graticule division. This point is then representative of time zero. i. Increase the sweep rate so that a single transient

spike can be examined in detail.

j. Adjust the sync controls separately for the positive and negative going transferts so that not only the recovery waveshape but also as much as possible of the rise time of the transfert is displayed.

k. Starting from the major graticule division representative of time zero, count to the right 100µsec and vertically 50mV for 6826A or 100mV for 6827A. Recovery should be within these tolerances as illustrated in Figure 5-5.

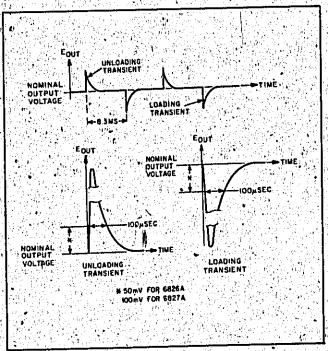


Figure 5-5. Translent Recovery Time, Waveforms

5-32 Programming Speed. To check the unit's programming speed, a square wave is applied to the unit and it is operated in the amplifier mode. This has the same effect as rapidly programming the unit, up and down, in the power supply mode. To make this test, proceed as follows:

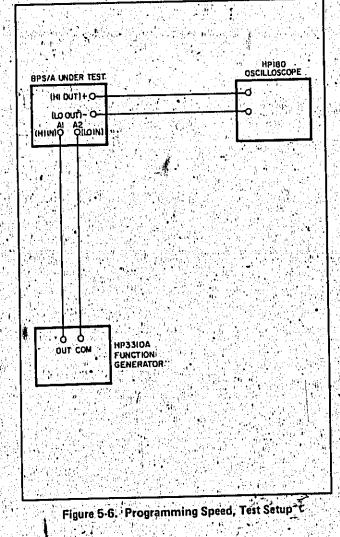
a. Connect test setup as shown in Figure 5-6.

b. Set MODE switch to VAR GAIN AMPL. RANGE switch to X10, and turn unit on.

B

c. Rotate VOLTAGE control fully clockwise.

d. On function generator, set input frequency to



about 100Hz squarewave and adjust amplitude to obtain maximum rated peak-to-peak output signal on oscilloscope (-50V to +50V on Model 6826A and -100V to +100V on Model 6827A).

e. Adjust oscilloscope to observe rise time of one squarewave. The waveshape should be within the tolerances shown on Figure 5-7 (output should change from maximum rated negative value to maximum rated positive value in less than 50µsec).

f. Check the fall time of one squarewave. It should be almost identical to the rise time except for inversion.

5-33 Output Impedance. To check the output impedance, proceed as follows:

a. Connect test setup as shown in Figure 5-8.

b. Set MODE switch to POWER SUPPLY, RANGE switch to X10, and turn unit on.

c. Adjust VOLTAGE control until front panel meter reads +50V for Model 6826A or +100V for Model 6827A.

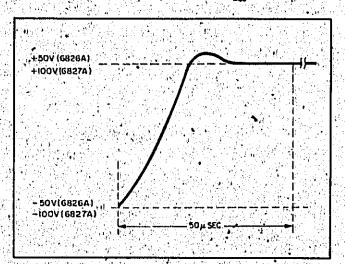


Figure 5-7. Typical Programming Speed Waveforms

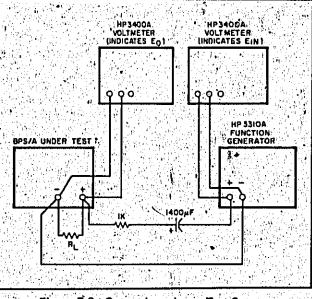


Figure 5-8. Output Impedance, Test Setup

d. Set AMPLITUDE control on Oscillator to 10 volts (Ein), and FREQUENCY control to 100Hz sinewave:
 e. Record voltage across output terminals of the power supply (Eo) as indicated on AC voltmeter.

f. Calculate the output impedance by the following formula:

$$Z_{out} = \frac{E_0 R}{E_{in} - E_0}$$

- E<sub>0</sub> = rms voltage across power supply output terminals.
- R = 1000 Ein = 10 volts g. The output impedance should be less than: 6826A: 6827A: 2 milliohms

#### Temperature Coefficient.

5-34

Definition: The change in output voltage per degree Centigrade change in the ambient temperature under conditions of constant input ac line voltage, output voltage setting, and load resistance.

5.35 The temperature coefficient of a power supply is measured by placing the power supply in an oven and varying it over any temperature span within its rating. (Most HP power supplies are rated for operation from 0°C to 55°C.) The power supply must be allowed to thermally stabilize for a sufficient period of time at each measurement temperature.

5-36 The temperature coefficient given in the specifications is the maximum-temperature-dependent output voltage change which will result over any one degree Centigrade interval. The differential voltmeter or digital voltmeter used to measure the output voltage change of the supply should be placed outside the oven and should have a long term stability adequate to insure that its drift will not affect the overall measurement accuracy.

5-37 To check the temperature coefficient, proceed as follows:

a, Connect load resistance (high range) and differential voltmeter as illustrated in Figure 5-1.

#### NOTE

Connect voltmeter to ±S terminals, NOT across load.

b. Set MODE switch to POWER SUPPLY and RANGE switch to X10. Turn CURRENT control fully clockwise.

c., Adjust front panel VQLTAGE control until front panel-voltmeter indicates maximum rated output voltage.

d. Place power supply in, temperature-controlled oven (differential voltmeter and load remains outside oven). Set temperature to 30<sup>0</sup>C and allow 30 minutes for stabilization.

e. Record differential voltmeter reading. f. Raise temperature to 4Ω<sup>Q</sup>C and allow 30 minutes for stabilization.

g. Observe differential voltmeter reading. Difference in voltage reading between step (e) and (g) should be less than the following: 6826A: 80mV

826A		360 B	1. 1.		2
	11			19 A.	é.
827A		• • • • • •	11.1		2

5-8

h. Repeat steps (a) through (g) with low range (X1) load resistance connected as shown in Figure 5-1. Set RANGE switch to X1.

760mV

i. Observe differential voltmeter readings. Difference in voltage reading between step (e) and (g) should be less than the following: 8.5mV

17mV

6826A: 6827A:

## 5<sup>1</sup>38 Drift (Output Stability).

Definition: The change in output voltage for the first eight hours following 30minute warm-up period. During the interval of measurement all parameters, such as load resistance, ambient temperature, and input line voltage are held constant.

This measurement is made by monitoring the out-5-39 ful of the power supply on a differential voltmetter or digital voltmeter over the stated measurement interval; a strip chart recorder can be used to provide a permanent record. A thermometer should be placed near the supply to verify that the ambient temperature remains constant during the period of measurement. The supply should be put in a location immune from stray air currents (open doors or windows, air conditioning vents), if possible, the supply should be placed in an oven which is held at a constant temperature. Care must be taken that the measuring instrument has a stability over the eight hour interval which is at least an order of magnitude better than the stability specification of the power supply being measured. The supply will drift considerably less over the eight hour measurement interval than during the half-hour warm-up.

5.40 To check the output stability, proceed as follows: a. Connect load resistance (high range) and differential voltmeter as illustrated in Figure 5-1.

b. Set MODE switch to POWER SUPPLY and RANGE switch to X10. Turn CURRENT control fully clockwise.

c. Adjust front panel VOLTAGE control clockwise until differential voltmeter indicates maximum rated output voltage.

d. Allow 30 minutes warm-up, then record differ-, ential voltmeter reading.

e. After 8 hours, differential voltmeter should change from reading recorded in step (d) by less than the following:

6826A: 25mV (pot wiper jump effect may add 50mV) 6827A: 50mV (pot wiper jump effect may add 100mV)

f, Repeat steps (a) through (e) with low range (X1) load resistance connected as shown in Figure 5-1. Set

RANGE switch to X1. g. Observe differential voltmeter reading. Differ-

ence in voltage reading between step (d) and (e) should be less than:

• 6826A: 2.5mV (pot wiper jump effect may add 5mV) 6827A: 5.0mV (pot wiper jump effect may add 5mV)

## NOTE

If remote programming is employed, the potentimeter wiper jumper effect is eliminated.

## 5.42 CONSTANT CURRENT TESTS

5-43 The instruments, methods, and precautions for the proper measurement of constant current power supply characteristics are for the most part identical to those already described for the measurement of constant voltage power supplies. There are, however, two main differences: First, the power supply performance will be checked between short circuit and full Ic ad rather than open circuit and full load. Second, a current monitoring resistor is inserted between the output of the power supply and the load.

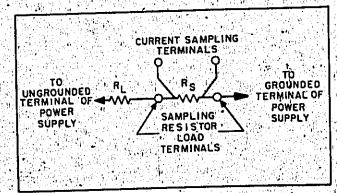


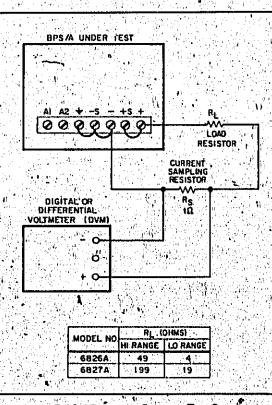
Figure 5-9. Current Sampling Resistor Connections

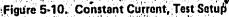
5-44 For all output current measurements the current sampling resistor must be treated as a four terminal device. In the manner of a meter shunt, the load current is fed to the extremes of the wire leading to the resistor while the sampling terminals are located as close as possible to the resistance portion itself (see Figure 5-9). Generally, any current sampling resistor should be of the low noise, low temperature coefficient (less than 20ppm/<sup>O</sup>C) type and should be used at no more than 10% of its rated power so that its temperature rise will be minimized.

### NOTE

The CURRENT MODE light should be on during these tests.

5-45 Rated Output and Meter Accuracy. a. Connect test setup shown in Figure 5-10. Use high range load resistor (RL) connected in series with the  $1\Omega$  resistor (RS).





) b.	Set BPS/A front panel controls as for
	MODE switch: POWER SUPPLY
Y = 1	RANGE switch: X10
	VOLTAGE control: fully clockwise
	CURRENT control: fully counterclockwise
	VOLTÄGE METER: high range DC
	CURRENT METER: high range DC
c.	Turn on BPS/A and adjust CURRENT control

until front panel ammeter indicates maximum rated positive output current; +1.0A (6826A) or +0.5A (6827A).

<b>.</b>	U V IVI SI	iouia re	80.92	IOIIOM2	
,		an an an an a	41, 14 d		
	6826A:				+1.0V
10	0010111		14 14 14		
÷.,	6827A:				+0.5V
. ÷.	0021M.				

e. 'Turn VOLTAGE control fully counterclockwise and adjust CURRENT'control until front panel ammeter indicates maximum rated negative output current, -1.0A (6826A)'or -0.5A (6827A).

E.	DVI	M she	bluc	read	as	foll	ows:		ł
1.	• •	6A:						-1.0	V.
	682	7A:			5.6			-0.5	V

5-46

Source Effect (Line Regulation). Definition: The change  $\Delta_{10UT}$  in the static value of dc output current resulting from a change in ac input voltage over the specified range from low line (usually 104 volts) to high line (usually 127 volts), or from high line to low line. To check the line regulation, proceed as follows: a. Utilize test setup and front panel settings of Paragraph 5-45.

b. Connect variable autb transformer between input power source and power supply power input.

c. Adjust auto transformer for 104 Vac input.

d. Turn VOLTAGE control fully clockwisef

e. Adjust CURRENT control until front panel ammeter reads exactly maximum rated positive output current, f. Read and record voltage indicated on differential voltmeter.

g. Adjust variable auto transförmer for 127Vac

h. Reading on differential voltmeter should not vary from reading recorded in step (f) by more than the following:

6826A:

6827A:

5.47

, ±350⊉V ±300µV

i. Turn VOLTAGE control fully counterclockwise and repeat steps (e) through (h) for negative output current.

> Load Effect (Load Regulation) Definition: The change Alour in the static value of the dc output current resulting from a change in load resistance from short circuit to a value which yields maximum rated output voltage.

48 To check the constant current load regulation, pro ceed as follows:

a. Utilize test setup and front panel settings of Paragraph 5-45.

b. Turn VOLTAGE control fully clockwise...

c. Adjust CURRENT control until front panel meter reads exactly maximum rated positive output voltage. d. Read and record voltage indicated on differential voltmeter.

e. Short circuit load resistor (RL).

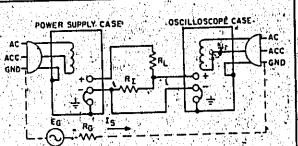
f. Reading on differential voltmeter should not vary from reading recorded in step (d) by more than the following:

6826A:	±350µV
6827A:	±300µV
T NOT TACE	

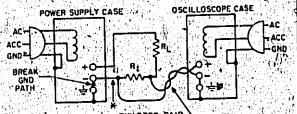
g. Turn VOLTAGE control fully counterclockwise and repeat steps (c) through (f) for negative output voltage.

> Ripple and Noise. Definition: The residual ac current which is superimposed on the dc output current of a regulated power supply. Ripple and noise may be specified and measured in terms of its RMS or (preferably) peak-topeak value.

5-49



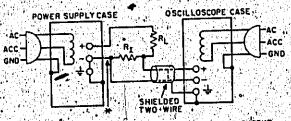
A. INCORRECT METHOD - GROUND CURHENT IS PRODUCES 6P CYCLE DROP IN NEGATIVE LEAD WHICH ADDS TO THE POWER SUPPLY RUPPLE DISPLAYED ON SCOPE



TWISTED PAIR-

A LENGTH OF LEAD BETWEEN RI AND OUTPUT TERMINAL OF POWER SUPPLY MUST BE HELD TO ABSOLUTE Minimum.

B.A CORRECT METHOD USING A SINGLE - ENDED SCOPE. OUTPUT FLOATED TO BREAK GROUND CURRENT LOOP, TWISTED PAIR REDUCES STRAY PICKUP ON SCOPE LEADS.



\* LENGTH OF LEAD BETWEEN AT AND GROUNDED DUTPUT TERMINAL OF POWER SUPPLY MUST BE HELD TO ABSOLUTE MINIMUM

C. A CORRECT METHOD USING A DIFFERENTIAL SCOPE WITH FLOATING INPUT GROUND CURRENT PATH IS BROKEN, COMMON, MODE REJECTION OF DIFFERENTIAL INPUT SCOPE IGNORES DIFFERENCE IN GROUND POTENTIAL OF POWER SUPPLY & SCOPE, SHIELDED TWO-WIRE FURTHER REDUCES STRAY PICKUP ON SCOPE LEAD.

MODEL NO.	C R <sub>L</sub> ( C.	8t	
6826A	ia: 49Ω*	iΩ,0.1%	
6827A		1Ω,0,1%	



b iz

5.50 Most of the instructions pertaining to the ground loop and pickup problems associated with constant voltage ripple and noise measurements also apply to the measurement of constant current ripple and noise. Figures 5.11 and 5.12 illustrate the most important precautions to be observed when measuring the ripple and noise of a constant current supply. The presence of a 120Hz waveform on the oscilloscope is normally indicative of a correct measurement method. A waveshape having 60Hz as its fundamental com<sup>2</sup>. ponent is typically associated with an incorrect measurement a setup.

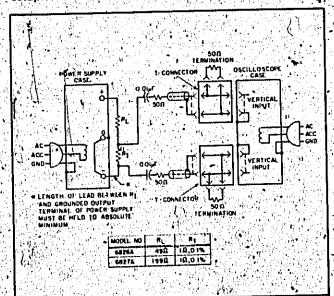


Figure 5-12. Constant Current Noise Spike Test Setup

5-51 Ripple Measurement. To check the output ripple, proceed as follows:

a. Connect the oscilloscope as shown in Figures 5-11B or 511C.

b. Rotate the VOLTAGE control fully cw.

c. Set RANGE switch to X10, MODE switch to POWER SUPPLY and turn on BPS/A.

d. Adjust CURRENT control until front panel meter reads exactly the maximum rated positive output current.

f, Turn VOLTAGE control fully counterclockwise and repeat steps (d) and (e) for maximum rated negative output current.

5-52 Noise Spike Measurement. To check the noise spike output, proceed as follows:

a. Connect test setup shown in Figure 5-12.

b. Turn VOLTAGE control fully clockwise.

c.\* Set RANGE switch to X10, MODE switch to

POWER SUPPLY, and turn on BPS/A. d. Adjust CURRENT control until front panel ammeter indicates the exact maximum rated positive output current.

e. Since the impedance matching resident constitute a 2:1 divider, the observed noise spikes should be less than:

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	5.5		1.2.2.1.4.2	: 14 C -	682	ma	- 1. C. S.		24 23		11.11.1		.5m			<ul> <li>C</li> </ul>	
:		1.2.2.2							(a) a a a	14 J. S. A.	21.22	· · _ :				33. C.	
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-	- 2.	· · · · ·	1 4 1 A				こうどうもう	1.1.1.1	れるい			- C. A.	· • • • • • •		· · · · ·	, °	
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5-53. Current Sink Test. The current sink test is performed using two BPS/A's. One is used as a set instrument, and the other is used as a source instrument. Two identical BPS/A's are preferred to perform this test.

#### CAUTION-

If two BPS/A's of the same model are not available, this test can be performed utilizing anyother <u>Bipolar</u> supply. However, it is of the <u>utmost importance</u> that the BPS/A output voltage be set <u>below</u> the other supply so that it will <u>sink</u> rather than force the other supply to sink which it may not be capable of doing.

To check the current sink performance of the BPS/A, proceed as follows:

a. On the test instrum	ent, set controls as follow
MODE switch:	POWER SUPPLY
RANGE switch:	× X10
VOLTAGE control	: fully.clockwise
CURRENT control	: fully clockwise
b. On the source instr	ument, set controls as

MODE switch: RANGE switch: C. Turn on test instrument and set output to: 6826A: +50V

follows

6827A: d. Connect function generator to terminals A1

and A2 of source instrument. Turn on and adjust source instrument output as follows:

6826A: 100V,p-p at 100Hz (approximately)

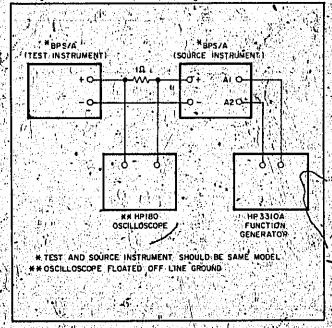
6827A: • 200V p-p at 100Hz (approximately) "e. Turn-off test and source instruments and connect test setup of Figure 5-13.

f. Turn on both instruments simultaneously and observe that waveform sampled across the 1 ohm resistor is as illustrated in Figure 5-14.

g. Repeat test with VOLTAGE control on test in strument set fully counterclockwise. Waveform should be same as Figure 5-14.

5-54 Overcurrent Protection Test. To check the over current protect circuit, proceed as follows:

a. Set BPS/A front panel controls as follows: MODE switch: FXD GAIN AMP RANGE switch: X10 VOLTAGE control: fully clockwise CURRENT control: fully clockwise





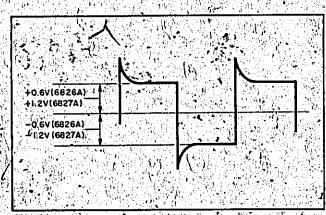


Figure 5-14. Current Sink Test Waveform

b. Apply a 5V p-p, 100Hz squarewave to the A1 (HI IN) and A2 (LO IN) terminals.

c: Connect a 1 $\Omega$ , 5W resistor across + (HI OUT) and - (LO OUT) terminals. Connect oscilloscope across 1 $\Omega$  resistor.

d. Turn on BPS/A and observe waveforms (see Figure 5-15). Overshoot should not exceed:

1	68264	1.1		1.2	5V p	<u>, г</u>
	6827		N (SE S) Za za		IV p	
ļ	00277	14.				<u></u>

5-55 Turn-on/off Transient Protect. To test the turn-on (c)

а.	Set front panel controls as follows:	
	MODE switch: POWER SUPPLY	Yí,
4	RANGE switch: X10	- di []
		्रत
िर्दे	방문 좀 통 먹은 흔들이 나라서 가지 않는 것만 다른 것 지지 않지 않는 것이다.	6
ا م ما	CURRENT control: fully clockwise	

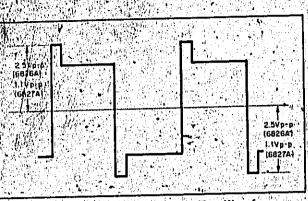


Figure 5-15. Overcurrent Protect Test Waveform

Connect a clip lead from base of ASO1 to ground

c. Turn on BPS/A. Output should be from 0 to 1.5Vdc.

d. Remove clip lead, output should be +50V (6826A) or +100V (6827A).

e., Repeat steps (a) through (d) except turn VOLT-AGE control fully ccw for -50V (6826A) or -100V (6827A) output

### 5-56 AMPLIFIER MODE TESTS

Gain and Meter Accuracy Test. To check gain and 5.57 the meter accuracy in the amplifier modes, proceed as follows

a. Connect the test setup as shown in Eigure 5-16. Use the appropriate low range load resistor (RL).

aN	Set BPS/A front panel controls as follows:
45 <b>D.</b> (16)	MODE switch: VAR GAIN AMP
	RANGE switch: X1
알븵	VOLTAGE control: fully clockwise
•	CURRENT control: fully clockwise
	VOLTAGE METER: Iow.range AC
11	CURRENT METER: high range AC
	Set generator frequency 100Hz and output

at 5Vac peak-to-peak.

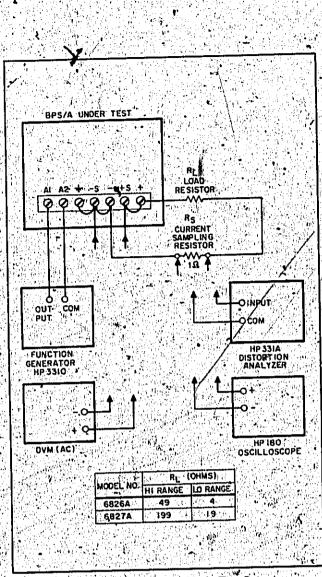
d: Turn on BPS/A and allow a five minute warmup period.

e. Connect oscilloscope to +S and -S terminals f. Adjust VOLTAGE control to obtain a 10V p-p (6826A) or 20V p-p (6827A) reading on the oscilloscope.

g. Observe that front panel voltmeter reads 3.5V rms (6826A) or 7V rms (6827A) and the front panel am-

meter reads 7A rms (6826A) or .3 A rms (6827A). h. Turn off BPS/A and connect appropriate high range load resistor. Set RANGE switch to X10 and VOLT AGE METER switch to high (range AC.

i. Turn on BPS/A and observe oscilloscope for a 100V p-p (6826A) or 200V pa (6827A) signal. . Observe that front panel voltmeter reads 35V tins (6826A) or 70V rms (6827A) and front panel ammeter reads .7A rms (6826A) or .35A rms (6827A)



Fisure 5-16. Amplifier Mode Test Setup

k: Set MODE switch to FXD GAIN AMP MODE and increase generator output to 10V p-p.

I. Observe a 100V p-p (6826A) or 200V p-p (6827 A) signal on oscilloscope.

S. 17 .

4 5-58 - Frequency Response. To check amplifier mode frequency response, proceed as follows;

a. Connect the test setup as shown in Figure 5-16.

b. Set MODE switch to VAR GAIN AMP and set VOLTAGE and CURRENT controls fully clockwise. c. Set HP3310A generator output at 100Hz sine wave and adjust signal amplitude to provide 100V p-p (6826A) or 200V p.p (6827A) output.

d. Adjust the generator frequency until output drops to 71V p-p (6826A) or 142V p-p (6827A): This ye quency should not be less than 15kHz.

Set MODE switch to FXD GAIN AMP and re peat steps (c) and (d) above. Frequency should not be less 35kHz. 4.6826A. than: 25kHz 6827A:

5'13

5-59 Distortion Test. To check the total harmonic distortion (THD) in the amplifier output, proceed as follows:

a, Connect the test setup as shown in Figure 5-16.

b. Set MODE switch to VAR GAIN AMP.

c. Set generator at 100Hz sinewave and adjust output for full BPS/A output voltage and current with appropriate load.

d. Measure the distortion at the output using HP 331A Distortion Analyzer.

e. The THD should be less than .1%.

## NOTE

The above is a difficult measurement because the THD is so low. Most audio generators will contain more than .1% THD in their output. A first order figure can be obtained by the following relationship:

THD of (gen. +amp) - THD gen.

THD of Amplifier =

## 5-60 TROUBLESHOOTING

## WARNING

The following troubleshooting procedures are performed with power applied to the BPS/A while its protective covers are removed. Be careful when performing the procedures as line voltage is always present on the power input connector, fuse holder, and in the power supply rectifier circuits. In addition, when the supply is on, energy available at many points; particularly the power transistors on the rear heat sink, may result in personal injury or death when contacted.

## 5-61 GENERAL

5.62 Before attempting to troubleshoot this instrument, ensure that the fault is with the instrument and not with an associated circuit. The performance test (Paragraph 5-5) enables this to be determined without removing the instrument's covers. A good understanding of the principles of operation is a helpful aid in troubleshooting; and it is recommended that the reader review Section IV of the manual before attempting to troubleshoot the instrument. Once the principles of operation are understood, refer to the trouble isolation procedures.

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5.63 Figure 7-1 is a simplified schematic of the BPS/A and is useful in tracing signal flow through the entire instrument. Figures 7-2 and 7-3 are detailed schematics (2 sheets each) of the 6826A and 6827A instruments respectively. The circled test point numbers in Figures 7-2 and 7-3 are also marked on the component location diagrams which accompany the schematics. References are made to these test points in the troubleshooting procedures.

#### 5-64 OVERALL TROUBLE ISOLATION PROCEDURE

5-65. Figure 5-17 illustrates the overall scheme of the trouble isolation and troubleshooting procedures which follow. The trouble isolation procedures represented by the boxes in the left-han column are intended to localize a problem to a particular area, both by direct testing and a process of elimination. Instructions at each stage of the isolation procedure direct you to the appropriate troubleshooting instructions, if required. These steps must be followed in the order in which they are given so that circuits are operational that are needed for testing other circuits. It is not necessary to make any calibration adjustments until troubleshooting has been completed. At that time, any necessary \* adjustments should be made and then the performance test of Paragraph 5-5 should be completed.

## -CAUTION -

Trouble isolation by swapping a good board fora suspected faulty one is not recommended unless it is certain that the fault is not destructive.

5-66 Preliminary Trouble Isolation Checks. Make the" following checks for obvious troubles before continuing with the troubleshooting procedures.

1. Check that the rear terminal strapping is correct for local or remote programming (see Section 111).

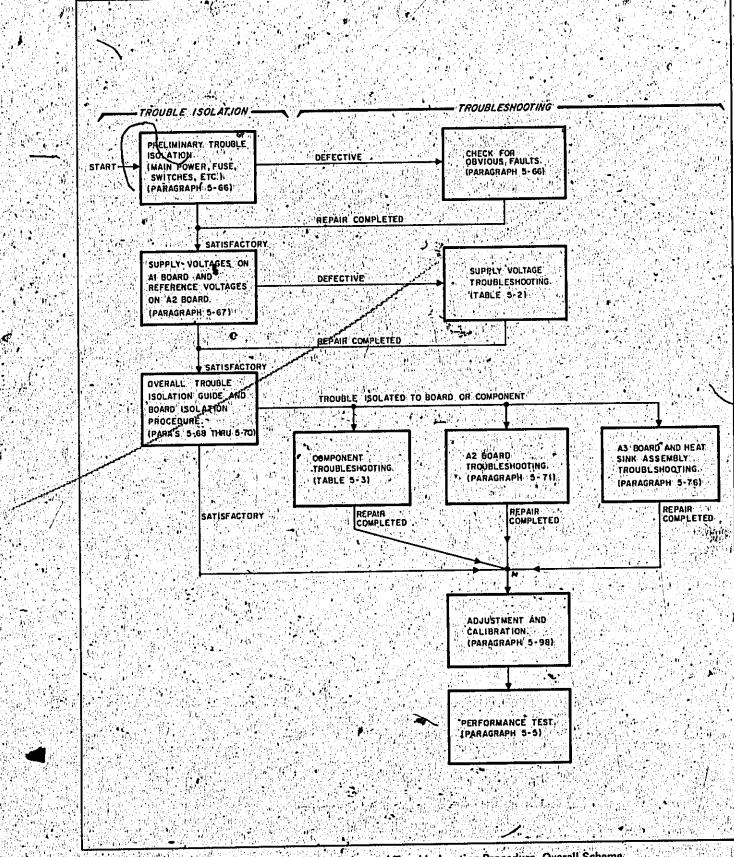
The rear terminals must be strapped correctly before power is applied to the instrument.

- CAUTION

2. Ensure that the MODE and RANGE switches are in the desired position.

3. Check the line fuse. If the line fuse is open, proceed as follows:

 a. Ensure that the proper ac input (115 or 230Vac) is selected (slide switch on power module) and install a fuse of proper rating; 2A for 115Vac or 1A for 230Vac.



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Figure 5-17. Trouble Isolation and Troubleshooting Procedure, Overall Scheme

b. Check the following:

- On chassis check for short circuits Main power transformer T1 and filter capaci-
- tors C1, C2 (6826A) or C1, C2, C3 (6827A). On board A1 check for short circuits – All filter capacitors and rectifier clodes, Also, check for shorts across power tracks on board:
- On board A3 check for short of open circuits -

6826A: A3C11, Q12-Q19 (shorted) -A3CR14-CR17 (opened)

6B27A: A3C18, Q9-Q12 (shorted)

A3CR12, CR15 (opened)

On heat sink assembly - Check output power transistors.

4. Check that the LOCAL/AUTO switch on board A2 is in the LOCAL position (see Figure 3-9). For normal operation of the BPS/A, this switch must be left in the LOCAL position. The AUTO position is used only for autoseries, auto-parallel operation (see Section 111).

5. Check continuity of ribboh cables W1 and W2 from the A1 board to the heat sink assembly and rear terminal strips respectively,

6. Check for defective meter(s), power cord, and loosely connected circuit boards. Visually inspect circuit boards for mechanical damage and discolored or charred

#### components.

7. If steps (1) through (6) have not isolated the trouble, check the supply voltages (Paragraph 5-67).

5-67 Supply Voltage Checks. In almost all cases, the trouble can be caused by an incorrect supply voltage (main, bias, or reference voltage); thus, it is a good practice to check these voltages (see Table 5-2). Although isolation of the trouble source to a particular board is desireable, possible trouble in one of the internal power sources should be investigated first. The tests described in Table 5-2 constitute a relatively fast check for trouble in this area. In many, cases, these checks can save many hours of troubleshooting circuits which are actually operating property. If the supply voltage checks have not isolated the trouble, proceed according to the overall trouble isolation guide (Paragraph 5-68).

### NOTE

There are two separate supply voltage returns in the BPS/A designated ① and ② \* in addition to chassis ground When making voltage or waveform measurements, be sure to use the appropriate return. The DVM or oscilloscope used must have a floating input since the ① and ② returns are not at chassis ground,

METER COMMON	METER POSITIVE	NORMAL READING	CHECK IF NOT CORRECT
	Main Supply Voltages		
Ū	<b>.</b>	<u>6826A</u> +65 ± 3Vdc +140 ± 7Vdc	A1G7, C9, C10, CR12, CR13, R5
TP2		-65 ± 3Vdc -140 ± 7Vdc	A1C11, C12, C15, CR14, CR15, R6
• D.	ТРЗ	+80 ± 4Vdc +155 ± 8Vdc	A1C5, C6, C8, CR10, CR11
TP4		-80 ± 4Vdc -155 ± 8Vdc	A1C13, C14, C16, CR16, CR17
	Bias Supply Voltages		
থ্য :	TP5	+15 ± .8Vdc	- A1C3, C17, CR3, CR5, CR6, O1, O2, V
TP6	2		A1C4, C18, CR7, CR8, O3, O4, VR3
3	TP7.	+20 ± 2Vdc (unfil)	_ A1CR1, CH2,
	Reference Voltages		
- <b>S</b>	TP9	+6.2 ± .35Vdc	A2VR4
TP10	• • <b>-</b> S	-6.2 <b>± .</b> 35∨dc	A2VR3
<b>,OUT</b>	TP11	+6.2 ± .35Vdc	A2VR2
, TP12	-OUT	<b>≻6:2 ± .35Vdc</b>	A2VR1

Table 5-2. Main Supply, Bias Supply, and Reference Voltages

5-68 Overall Trouble isolation Guide. After checking the supply voltages, disconnect the load and examine Table 5-3. This table contains a list of symptons and probable causes that may cut down on troubleshooting time. For each trouble symptom, Table 5-3 isolates the trouble to a component or group of components or directs the reader to additional procedures if further isolation of the trouble is necessary.

5.69 In general, if the BPS/A operates properly in the power supply mode, it should also operate properly in the amplifier mode (variable gain or fixed gain amplifier mode). The trouble symptoms listed in Table 5-3 isolate the trouble. to detective components or groups of components (functional circuit areas). The voltage control stages on board A2 in conjunction with the output power amplified stages on ) board A3 and the heat sink assembly provide the desired

output voltage/gain. The voltage control stages A2U1 and A2U2 are common to both positive and negative outputs. The bipolar amplifier circuits on board A3 and the bipolar series regulator/output amplifier stages on the heat sink assembly aconsist of positive and negative stages for positive and negative outputs respectively. The current control circuits consist of positive current comparison stage (A2U3) and negative current comparison stage (A2U4) and associated common circuits consisting of dual ganged CURRENT control A5R1, speedup network (A2Q1, C9), and current. sampling resistor A2R27. The CURRENT MODE indicator A5DS2 lights and a FLAG indication is present (high level at terminal A17) when the BPS/A is in constant current operation: During constant current operation, stages A2Q5 or A206 provide the proper level to control the CURRENT MODE Jamp driver (A202, 03) and FLAG output driver. (A2Q4) stages for positive or negative output current respec-tively.

SYMPTOM	PROBÁBLE CAUSE
No output voltage (All modes: POWER SUPPLY, VAR GAIN AMP, FXD GAIN AMP)	<ul> <li>a. Fuse blown or incorrect rear terminal strip strapping, etc. (see Paragraph 5-66).</li> <li>b. Main, bias, or reference voltages defective (see Paragraph 5-67).</li> <li>c. Relay A1K1, circuit board (A2 or A3), or output power transistors on heat sink assembly defective (see Paragraph 5-70).</li> </ul>
Zero or low putput voltage (POWER SUPPLY mode only)	a. MODE switch defective b. Internal positive de reference defective (A2C10, R3, R58, R59)
Zero or low output voltage (POWER SUPPLY and VAR GAIN AMP modes only).	a. Voltage reference/gain control amplifier stage A2U2 defective (see Paragraph 5-71). b. VOLTAGE control A5R2 defective.
No output (VAR GAIN AMP and FXD GAIN AMP modes only).	<ul> <li>a. MODE switch not in proper position.</li> <li>b. Improper connections to rear terminals A1 and A2 or front panel terminals.</li> <li>HI IN and LO IN.</li> </ul>
Output voltage correct in X10 range, but incorrect in X1 range or vice versa.	a. RANGE switch defective. b. Relays A2K2 and/or A2K3 defective.
Negative output normal but zero or low positive output.	<ul> <li>a. Check the main positive supply voltages (see Table 5-2): +65V, +80V for 6826A or +140V, +155V for 6827A.</li> <li>b. Positive turn on/off circuit defective (A2O2 shorted).</li> <li>c. Defective positive output power transistor stage on heat sink assembly: Q1, Q2 (6826Å) or Q1-Q3 (6827Å).</li> <li>d. Defective positive coupling amplifier or driver stages on A3 board.</li> </ul>
	6826A: ,,A3Q6, Q7, Q8 (opened), VR2 (shorted) 6827A: A3Q11, Q12 (opened), VR5, VR6 (shorted)
Output voltage latohed to maxist mum positive	<ul> <li>a. Amplifier stage on A3 board defective: A3Q6 shorted (6826A) or A3Q8 opened (6827A).</li> <li>b. Positive current comparison amplifier output diode (A2CR14) shorted.</li> </ul>

Table 5-3. Overall Trouble Isolation Guide

<b>SYMPTOM</b>	PROBABLE CAUSE
Positive output normal, but zero or low negative output.	<ul> <li>a. Check the main negative supply voltages (see Table 5-2): -65V, -B0V for 6826A or -140V, -155V for 6827A.</li> </ul>
	b. Negative turn on/off circuit defective (A203/shorted).
	c. Defective negative output power transistor on heat sink assembly: Q3, Q4 (6826A) or Q4, Q5, Q6 (6827A).
	d. Defective negative coupling amplifier or driver stages on A3 board: 6826A; A309, 010, 011 (opened), VR1 (shorted) 6827A: A309, 010 (opened), VR7, VR8 (shorted)
Output voltage latched to maximum negative:	a. Amplifier stage on A3 board defective: A3011 shorted (6826A) or A30B, shorted (6827A).
	b. Negative current comparison amplifier output diode (A2CR13) shorted.
No constant current operation.	a. Check reference voltages at TP11 and TP12 and bias voltages at TP5 and TP6 (see Table 5-2).
	b. Check circuit components common to positive (A2U3) and negative (A2U4) comparison amplifiers Dual ganged CURRENT contubl – A5R2 Speed up network – A2C9, C1, R27, R46, R47
No positive constant current op- eration (negative constant current circuits operate property).	<ul> <li>a. Check positive reference voltage at TP11 (see Table 5-2).</li> <li>b. Positive current comparison amplifier A2U3 defective (see Paragraph 5-71).</li> <li>c. A2CR5, CR14, or VR5 defective.</li> </ul>
No negative constant current operation (positive constant cur- rent circuits operate properly).	<ul> <li>a. Check negative reference voltage at TP12 (see Table 5-2).</li> <li>b. Negative current comparison amplifier A2U4 defective (see Paragraph 5-21).</li> <li>c. A2CR13 or VR6 defective.</li> </ul>
Positive constant current circuits operate properly but CURRENT MODE indicator does not light.	PNP switch A205 opened.
Negative constant current cir- cuits operate properly but CURRENT MODE indicator does not light.	NPN switch A2Q6 opened.
CURRENT MODE indicator always on (FLAG oùtput low).	Defective lamp driver circuit: A2Q2 opened or A2Q3 shorted.
FLAG output (terminal A17) always high (about +16V).	FLAG driver A2Q4 opened.
CURRENT MODE indicator always on and FLAG output always high.	Diode A2CR14 opened.
Genstant current circuits operate, normally but CURRENT MODE indicator does not light.	a. Indicator (LED) A5DS2 defective. b. Defective lamp driver circuit: A2O2 shorted or A2O3 opened.

Table 5-3. Overall Trouble Isolation Guide (Continued)

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	Table	5.3	Overall	Trouble	Isolation	Guide	(Cont	inued)	18	
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<b>SYMPTOM</b>	PROBABLE CAUSE
Constant current circuits operate normally but no FLAG output (always low).	a. FLAG driver A2Q4 shorted. b. Jumper A2W1 not installed.
Positive current sink inoperative.	Defective A2CR3, CR11, CR12, R28 (6826A) or A2CR3, CR11, CR12, R23, " R28 (6827A).
Negative current sink inopera-	Defective A2CR4, CR7, CR8, R29 (6826A) or A2CR4, CR7, CR8, R26, R29 (6827A).
Bandwidth too narrow in VAR GAIN AMP mode.	A2U1 defective,

5.70 Board Isolation Procedure. The board Isolation procedure describes how to isolate trouble to the turn on/off circuit on boards A1 and A3, the voltage/current control circuits on board A2, or to the output Dower amplifier stages on board A3 and the heat sink assembly. The board isolation procedures assumes that an output problem exists in all three modes of operation and all trouble isolation procedures up to this point have been completed. To isolate the trouble to the defective board(s), proceed as follows:

# WARNING

The following troubleshooting procedures are performed with power applied to the BPS/A while its protective covers are removed. Be careful when performing the procedures as line voltage is always present on the power input connector, fuse holder, and in the power supply rectifier circuits. In addition, when the supply is on, energy available at many points, particularly the power transistors on the rear heat sink, may result in personal injury or death when contacted.

a. Remove covers and A3 board from the instru-

ment. b. Remove load and connect a DVM to the +S and -S rear terminals.

. Set controls of	n front p	anel as fo	ollows:
MODE switch		POWEF	SUPPLY
RANGE switc	h: 🕻	X10	
VOLTAGE co	ntrol		ockwise
CURRENT co		fully cl	ockwise;
		fully cl	ockwise

VOLTAGE METER: high range DC CURRENT METER: high range DC d. Turn on power and observe that LINE indica

tor lights. e. Check that turn on/off relay A1K1 is operating properly by connecting ohmmeter between A1K1 pin 1 and

short circuit (zero ohms) is present, check relay A1K1 and associated components (A1C2, CR4, R32, R37). If open circuit is present, proceed to step (f).

f. Turn off power and isolate the turn on/off circuit on board A3 by lifting the connections from diodes A3CR3 and A3CR4 to the collectors of transistors A3Q2 and A3Q3 respectively. Install A3 board in instrument.

g, Turn on power. Is output voltage is normal (max. positive), the turn on/off circuit (A3Q1-A3Q5) is defective. If output is zero or low, proceed to step (h).

h. Turn off power and reconnect diodes A3CR3 and A3CR4. Connect –5Vdc to the Af<sup>1</sup>(HI'IN) and A2 (LO IN) terminals.

i. Set MODE switch to FXD GAIN AMP position and turn on power. If output voltage is, normal (max. positive), the voltage/gain reference stage A2U2 is probably defective (see Paragraph 5-71). If output is zero, proceed to step (j).

; j. Turn power off. Remove the A2 board. Connect a variable dc voltage source (-2.5V to -4.5V) between A3 pin 5 and 20. Connect negative potential to A3 pin 5.

k. Turn on power and vary the negative source voltage from -2.5V to -4.5V. Output voltage should vary accordingly from maximum positive to maximum negative value through zero. If output voltage is normal, the A2 board is defective (see Paragraph 5-71). If output is not normal, the A3 board or output power transistor stages on heat sink assembly are defective (see Paragraph 5-76).

5-19

#### 5-71 A2 BOARD TROUBLESHOOTING

#### NOTE

For normal operation of the BPS/A, the Local/ Auto switch A2S1 <u>must</u> be in the "Local" position (see Figure 3-9). The "Auto" position is used only for auto-series, auto-parallel, or autotracking operation (see Section III). If this switch is left in the "Auto" position for normal operation, the output will be latched at full positive or negative depending on other control settings.

5-72 The A2 plug-in board contains the voltage and current control circuits which can be separated functionally allowing the trouble to be isolated to the individual circuit level. The following paragraphs provide troubleshooting procedures for the voltage control, current control, and RMS current meter driver circuits located on board A2.

5-73 Voltage Control Circuits. Integrated circuit amplifies A2U1 (voltage comparison amplifier) and A2U2 (voltage reference/gain control amplifier) with their circuit components are part of the constant voltage feedback loop. The following procedure consists of a series of fast checks to isolate trouble in these circuits.

a. Remove top and right side covers from instrument. Remove the A3 board.

b. Ensure that rear terminal strip is strapped correctly for local operation (see Section III).

. Set front panel controls	as follows:
MODE switch:	POWER SUPPLY
1 Walk of the state of the stat	X1
VOLTAGE control:	fully counterclockwise
CURRENT control:	fully clockwise
. Connect a DVM betwee	en A2U2 pin 6 (TP14)

and -S.

e. Turn VOLTAGE control through its range and observe that DVM reading varies from 0 to -10V. If voltage reading is correct, proceed to step (f). If the output at pin 6 is ±15V, check A2CR1, CR2, U2 for short circuits. If the output at pin 6 is zero, VOLTAGE control A5R2 is open or defective, or A2U2 is defective.

f. Set VOLTAGE control for reading of -5V on DVM.

g. Set MODE switch to VAR GAIN AMP positive and connect oscilloscope between A2U1 and -S.

h. Apply a 100Hz sinewave (about 40mV p-p) to the HI IN (A1) and LO IN (A2) terminals. If A2U1 is operational, a sinewave (approximately 8V p-p) should be observed on oscilloscope. If there is no output, A2U1 or A2K1 is defective. If the output at A2U1 pin 6 is ±15Vdc, A2CR18, CR19, or A2U1 is shorted.

5-20

5.74 Current Control Circuits. Integrated circuit amplifiers A2U3 (positive current comparison amplifier) and A2U4 (negative current comparison amplifier) control constant current operation for positive and negative output currents respectively. An "OR" function results if either circuit is operational and control is established. To check these circuits proceed as follows:

a. Remove top and right side covers from instrutment. Remove the A3 board.

b. Remove strap between terminals A13 and A14 and apply a small variable dc voltage (approximately  $\pm 0.2$ . Vdc) between terminals A14 and A18:

c: Connect a DVM between A2U3 pin 6 (TP16) and -S. Turn on power and note that DVM reads from approximately +7V to -8V as the source voltage is varied through zero. If voltage reading is correct, proceed to step (d). If reading is ±15V, check A2U3 for short. If reading is zero, A2U3 is defective.

d. Turn power off and replace straps between terminals A13 and A14. Remove straps between terminals A20 and A21. Apply a small variable dc voltage (approximately ±0.2Vdc) between terminals A21 and A18.

e. Connect a DVM between A2U4 pin 6 (TP17) and -S. Turn on power and note the DVM reads approximately +7V to -8V as the source voltage is varied through zero. If reading is not correct, the A2U4 stage is defective.

5:75 RMS Current Meter Driver. Integrated circuit A2U5 provides the gain necessary to drive diode detector A1CR18 which allows ac current to be metered through the detection process. To determine if A2U5 is operational apply a sinewave (2V p.p. 100Hz) with a dc offset of -0.2 Vdc to the -OUT side of A2C13. Observe that a sinewave of approximately 28-30V p.p is present at pin 6 of A2U5. Connect oscilloscope between A2U5 pin 6 and . () for this measurement.

### 5-76 A3 BOARD AND HEAT SINK ASSEMBLY TROUBLESHOOTING

## WARNING

The following troubleshooting procedures are performed with power applied to the BPS/A while its protective covers are removed. Be careful when performing the procedures as line voltage is always present on the power input connector, fuse holder, and in the power supply rectifier circuits. In addition, when the supply is a on, energy available at many points, particularly the power transistors on the rear heat sink, may result in personal injury or death when contacted. 5.77 The A3 plug in board contains positive and negative amplifier and driver stages which amplify the control voltage from board A2 in order to control the conduction of the output power transistors on the heat sink assembly. The A3 board and heat sink assembly stages can be isolated from the voltage and cutrent feedback loops by removing the A2 board from the instrument and providing an external control voltage input to the A3 board. The following paragraphs describe troubleshooting procedures for the A3 board and heat sink assembly circuits.

5-78 Output Amplifier Stages. To troubleshoot the amplifier and driver stages on the A3 board and the output power transistors on the heat sink assembly, proceed as follows:

a. Remove the A2 board from the unit and remove the load from the output terminals.

b. Connect function generator (HP3310A) output terminals between the connector side of A3R15 (6826A) or A3R29 (6827A) and ② . Set output of function generator for a sinewave of approximately 2V p p at 100Hz with a dc offset of -3.5V. Connect an oscilloscope to +S and -S terminals.

c. Turn on power and observe a sinewave output of 100V p-p (6826A) or 200V p-p (6827A). The sinewave should not be clipped or distorted.

d: If either polarity of the sinewave is missing or distorted, troubleshoot by tracing the sinewave back to the source. Refer to Figure 7-2 (sheet 1) for the 6826A or

Figure 7-3 (sheet 1) for the 6827A. Also, check the turn on/off circuit (Paragraph 5-79).

#### NOTE

When troubleshooting the power amplifier circuits, keep in mind that possible trouble areas exist in the interconnections (A1 board, W1, and W2 ribbon cables) as well as the A3 board circuits and the output power transistors Q1-Q4 (6826A) or Q1-Q6 (6827A) on the heat sink assembly. 5.79 Turn On/Off Circuit. The turn on/off circuit on board A3 can be isolated from the main amplifier driver circuits by disconnecting A3CR3 and/or A3CR4. If the trouble is in the turn on/off circuit, the output should rise to the proper level with the diode(s) disconnected. To check the operation of the turn on/off circuit (diodes A3CR3, CR4, are connected), short the base of A3O1 to [2], and the sinewave output will drop to .5V p-p. When the short is removed, the output will return to the full sinewave output.

5-80 Overvoltage Protection Circuit. The overvoltage protection clamping diodes are another potential trouble area. These diodes A3VR1, VR2 (6826A) or A3VR5-VR8 (6827A) can be lifted (disconnected) individually or together while observing the amplifier output. If one or more are shorted, the complete sinewave will be restored when the defective diode is disconnected.

5-81 Overcurrent Protection Circuit. Protection against overcurrent during the transition from constant voltage to constant current operation is provided by diode(s). A3CR22 (6826A) or A3CR22, CR23 (6827A) on the negative output and diodes A3CR20, CR21 (6826A) or A3CR24, CR25 (6827A) on the positive output. If these diodes are defective, the output will be badly clipped or the output level will be much lower than normal.

# 5-82 DEGRADED PERFORMANCE PROBLEMS

5-83 Table 5-4 contains a list of less common troubles and their probable causes. The troubles in this table are less catastrophic than those previously described in that, generally, they lead to degraded performance rather than complete failure.

5-21

Table 5-4.	<b>n</b> - <i>i</i>	~ ~ ~ ~ ~	<b>D</b>	1.1.0.00	-

алана селата стар на најди и сулат, са селата стара на реките и настар ди селат. На селата на селата на селата и кој селата се селата на реките на селата на 100 селата се селата селата на 100
PROBABLE CAUSE
Bias and reference supply: Check A1Q1-Q4, A1VR1, A2VR3, A2VR4
Bias and reference supply: Check A1Q1-Q4, A1VR1, A2VR1, A2VR2
<ul> <li>a. Constant current operation taking place: Check setting of CURRENT control.</li> <li>b. A2U1, A2U2 defective.</li> <li>c. Check measurement technique.</li> </ul>
a. CURRENT control set too low. b. <sup>t</sup> A2U3, A2U4 defective. c. Check measurement technique.
<ul> <li>a. Ground loop through test equipment, check test setup.</li> <li>b. Excessive ripple in reference voltages. Check reference voltages (Table 5-2).</li> <li>c. Supply crossing over into constant current operation, check setting of CURRENT control (may be set too close to crossover point).</li> <li>d. Defective rectifier circuits (half wave instead of full wave rectification).</li> </ul>
<ul> <li>a. Supply crossing over into constant current operation. Check setting of CURRENT control.</li> <li>b. Defective component in amplifier circuit. Check A3CR14-CR17, R29, R30 (6826A) or A3CR11, CR12, CR15 (6827A).</li> </ul>

5-22

# 5-84 REPAIR AND REPLACEMENT

5-85 Section VI of this manual contains a list of replaceable parts, Table 5-5 contains replacement data for the semiconductors used in the BPS/A described by this manual. When replacing a semiconductor, use a Hewlett-Packard part or a commercial replacement part, if applicable. In cases where neither of these parts are immediately available and a part is needed for emergency operation or troubleshooting-verification, the alternate part (see Table 5-5) can be tried with at least a 90% probability of success.

#### 5-86 COVERS AND FRONT PANEL

5-87 Top or Bottom Cover. To remove either the top or bottom cover:

a. Turn off unit.

screws at rear of cover.

c. Slide cover toward rear of unit approximately
 3/4 inches and lift out of unit.

5-88 Side Cover. To remove either side cover, remove four, 1/4-inch, No. 6 flat-head screws and lift cover off.

5-89 Side Castings. To remove either side casting:

a. Remove top, bottom, and side cover.

b. Remove eight, No. 6 flat-head screws securing side casting to instrument cross members.

c. Lift side casting off.

5-90 Front Panel. To remove the front panel: a. Remove top, bottom, side covers, and left side casting.

b. Loosen the VOLTAGE METER and CURRENT METER knobs with allen wrench and remove knobs.

c. Front panel may now be pulled forward away '

5-91 Foot Assemblies and Tilt Stand. The front and rear foot assemblies and the tilt stand on the bottom of the unit must be removed before the unit is rack mounted (see Paragraph 2-15). To remove these assemblies, proceed as follows:

a. Remove the rear foot assembly on bottom of the unit by pushing the release button in the center of the foot assembly and sliding the assembly OFF as indicated.

b. Remove bottom cover (Paragraph 5-87). The bottom cover is removed to gain access to the A1 board.

### NOTE

The release button on the front foot assembly is located directly beneath the -I ZERO ADJ potentiometer on board AT. By pressing slightly inward on the AT board, sufficient clearance is provided to remove the front-foot assembly.

. 10

c. Remove the front foot assembly as in step (a) except also apply slight inward pressure to the A1 board.
d. Remove one of the side castings (Paragraph 5-89) to allow removal of the tilt stand.

e. Remove tilt stand.

f., Replace bottom cover if the unit is to be rack mounted.

Model	Reference Designation	HP Part No.	Commercial Replacement	Alternative
6826A	Q1-Q4	1854-0421	60128 RCA	
6827A	01-06	1854-0421	60128, RCA	
6826A	A1CR1-CR4; A3CR1-CR7; A2CR1, CR2; CR5; CR9, CR13, CR14, CR18-CR24; A3CR15-CR17, A3CR19-CR21	1901-0050	.1N4148	
6827A	A1CR1-CR4; A2CR1, CR2, CR5, CR9, CR13, CR14, CR18-CR24; A3CR1, CR2; CR5, CR6, CR18-CR21	1901-0050	1N4148	
6826A/6827A	A1CR5-CR8	1901-0327	1N5059	
6826A/6827A	A1CR10-CR17	1901-0328	A14D GE	Mar Marker (1997) Marker (1997)
6826A/6827A	A1CR18; A2CR10	1901-0535		
6826A	A1CR20	1901-0518		and Markets
6827A	A1CR19, CR20	1901-0518		•
6826A/6827A	A101	1853 0041	2N4036	1 (g. 99) - 193 - 53 1
6826A/6827A	A102, A201, 02, 03, 04, 06; A301, 04, 06	1854-0071		2N4141
6826A/6827A	A103	1854-0244	~ 2N1711A	
6826A/6827A	A1Q4; A2Q5	1853-0099		: 2N2907
6826A/6827A	A1VR1, VR3; A2VR1-5	1902-1221	1,N825	
6826A	A2CR3, CR4, CR7, CR8, CR11, CR12	1901-0033	1N485	
6827A	A2CR3, CR4, CR7, CR8, CR11, CR12; A3CR3, CR4	1901-0033	··1N485	
6826A/6827A		1820-0223	LM301AH National	
6826A/6827A	[1] A. S.	1902-0064	SZ10939-146 Motorola	
6826A	A3CR14, CR22	1901-0460	1N4157	
6827A	A3CR11, CR12, CR15, CR16, CR22-CR25	1901-0460	1N4157	
6826A	A303, 06, 013, 017, 018, 019	1853-0038	SJ5099 Motorola	
6827A	A303, 07, 09,-010, 013-016	1853-0038	SJ5099 Motorola	
6826A	A302, 07, 010-012, 014-016	1854-0095	40346 RCA	1. 名名古梅花
6827A	A302, 06	1854-0095	40346 RCA	
6826A	A3O8, O9	1853-0037		
6827A	A308	1854-0232	2 SJ1679 Motorola	

## Table 5-5. Semiconductor Replacement Data

Model	Reference Designation	HP Part No. Commercial Replacement Alternative
6827A	A3Q11, Q12, Q17-Q20	1854-0271 MM2258 Motorola
6826A	A3VR1	1902-0660 SZ11213-368 Motorola
6826A	A3VR2	1902-0597 SZJ 1213-3561 Motorola
6827A	A3VR3	1902-0184 N N966
6827A	· A3VR4	1902-0182 SZ10939-272 Motorola
6827A	A3VR5, VR6	1902-0597 SZ11213-356 Motorola
6827A	A3VR7.VR8	// 1902-0660 SZ11213-368 Motorola

5-24

### Table 5-5. Semiconductor Replacement Data (Continued)

#### 5-92 REAR HEAT SINK ASSEMBLY

5-93 Interder to remove the power transistors from the heat sink, the rear panels must first be removed. After the rear panels are removed, the transistors are exposed and can be removed. Notice that if a new power transistor is installed, be sure to apply silicon grease (Dow DC-5, HP 8500-0059) to both sides of the transistor's mica insulator to assure proper heat exchange.

5-94 Rear Panels. To remove the rear panel containing the rear terminal boards and the panel containing the power receptacle, proceed as described below.

5-95 Terminal Board Panel.

a. Remove top cover (Paragraph 5-87).

b. Remove two screws at top of unit (near Service tag).

c. Remove cable W2 from connector J4 on board

A1.

d. Lift the terminal board panel straight up and

out.

#### 5-96 Power Receptacle Panel.

a. Remove bottom cover (Paragraph 5-87).

b. Remove two screws securing corner of panel.

c. Lift panel straight up and out.

5-97 (Heat Sink. To remove the heat sink, proceed as follows:

a. Remove all covers (Paragraph 5-86)

b. Remove terminal board and power receptacle rear panels (see above).

c. Remove four screws securing heat sink to side frames.

d. Remove cable W1 from connector J3 on board A1. The heat sink can now be lifted out.

## 5-98 ADJUSTMENT AND CALIBRATION

5-99 Adjustment and calibration may be required after performance testing, troubleshooting, or repair and replace ment.

#### 5-100 METER ZERO ,

5-101 The meter pointer must rest on the zero calibration mark on the meter scale when the instrument is at normal operating temperature, resting in its normal operating position, and turned off. To zero set the voltmeter and ammeter, proceed as follows:

a. Turn on instrument and allow it to come up to normal operating temperature (about 30 minutes).

b. Turn instrument off. Wait one minute for power supply capacitors to discharge completely.

c. Insert sharp pointed object (pen point or awl) into small indentation near top of round black plastic disc located directly below meter face.

d. Rotate plastic disc clockwise until meter reads zero, then rotate counterclockwise slightly in order to free adjustment screw from meter suspension. Pointer should not move during latter part of adjustment.

5-102 CONSTANT VOLTAGE CALIBRATION

### NOTE

The CURRENT MODE fight should be off during these procedures.

5-103 Output Zero and Offset Adjustments. a. Remove top cover to gain access to potentiometers on boards A1 and A2.

b: Connect DVM to the +S and -S rear terminals.

c. Short BPS/A front panel input terminals (HI IN to LO IN). Output terminals HI OUT (+) and LO OUV (-) are open circuited.

d. Set MODE switch to FXD GAIN AMP position. Turn CURRENT control fully clockwise.

e. Torn on BPS/A and allow a 10-minute warmup. f. While switching the RANGE switch between the a

X1 and X10 positions, adjust A2R60 until the X10 reading is of the same polarity and 10 times the X1 reading within 2.5mV. For example, if X1 reading is +.1mV, adjust A2R60 for +3.5mV or less.

g. Set RANGE switch to X1 and adjust A2R61 for OV ±0.25mV reading on DVM.

h. Set RANGE switch to X10, DVM should read. OV ±2.5mV. If not, repeat steps (f) through (g).

i. Remove short-from HI and LO IN terminals.

5-104 Constant Voltage Programming Accuracy. a. Set MODE switch to POWER SUPPLY position and RANGE switch to X1 position.

b. Short terminals A9 and A10 on rear terminal strip.

c<sup>1</sup> Adjust potentiometers A2R58 (coarse) and A2R59 (fine) for a DVM reading of -5,120V ± .2mV (6826A) or -10.24V ± .4mV (6827A).<sup>4</sup>

d. .Turn BPS/A off. Remove jumper between terminals A8 and A9 and connect a precision 10.24KΩ (±.05%) resistor between terminals A9 and A10.

e. Turn BPS/A on and adjust front panel V ZERO ADJ A1R1 for 0V ± .2mV (6826A) or 0V ± .4mV (6827A). f. Set RANGE switch to X10 position. DVM should read 0V ±2mV (6826A) or 0V ±4mV (6827A). If not, check A2R60 adjustment (step f of Paragraph 5-103). g. Turn BPS/A off. Remove 10.24KΩ resistor

and connect a 20.48K $\Omega$  resistor between terminals A9 and A10.

<sup>™</sup>h, Turn BPS/A on. DVM should read +51.20V ± 25mV (6826A) or 102.40V ± 50mV (6827A).

5-105 DE Voltmeter Calibration.

a. Set VOLTAGE METER switch to the high range DC, 60V (6826A) or 120V (6827A), position. b. Adjust A1R8 for +51.20V (6826A) or +102.40V

(6827A) indication on BPS/A's front panel voltmeter.

c. Connect short across 20,48K $\Omega$  (±0.5%) resistor (A9 to A10). Front panel voltmeter should read -51.2V (6826A) or +102.40V (6827A).

d. Turn BPS/A.off, remove 20.48K resistor, IAstall, jumper between A8 and A9, remove DVM from output terminals, and replace top cover.

# 5-106 CONSTANT CURRENT CALIBRATION

### NOTE.

The CURRENT MODE light should be on dur- . ing these procedures.

5-107 Constant Current Programming Accuracy.

a. Remove top cover to gain access to potentiometers on boards A1 and A2.

b. Remove jumpers from A19 to A20 and from A12 to A13 on rear terminal strip.

c. Short terminals A18 and A13 and A18 to A20 on rear terminal strip.

d. Connect a 1 $\Omega$  .1% precision resistor (R<sub>S</sub>) in series with the appropriate high range load resistor (R<sub>L</sub>), 49 $\Omega$  (6826A) or 199 $\Omega$  (6827A) as shown in Figure 5-16. Connect the DVM across the 1 $\Omega$  resistor.

e. Turn on BPS/A and allow a 30-minute warmup.

f. Set MODE switch to POWER SUPPLY. Set the RANGE switch to X10 and turn the VOLTAGE control

fully clockwise. g: Adjust front panel + 1 ZERO ADJ (A1R2) for a reading of 0.000 ± .3mV on DVM.

h. Turn VOLTAGE control fully counterclockwise. i. Adjust front panel – LZERO ADJ (A1R3) for a

reading of 0.000 ± .3mV on DVM.

#### NOTE:

The A1R2 and A1R3 adjustments may interact. Repeat steps (f) through (i) several times to minimize errors.

j. Turn BPS/A off and remove jumper from A18 to A20. Connect a precision (±0.5%) resistor between A18 and A20: 10.24K $\Omega$  (6826A) or 5.12K $\Omega$  (6827A).

k. Tyrn VOLTAGE control fully counterclockwise and turn on the BPS/A.

.I. Adjust A2R21 for -1.024V ± .25mV (6826A) or -.512V ± .125mV (6827A) as indicated on DVM.

m. Turn BPS/A off and remove the jumper between A18 and A13. Connect a precision (±0.5%) resistor

between A18 and A13: 10.24K (6826A) or 5.12K (6827A). n. Turn VOLTAGE control fully clockwise and

turn on the BPS/A. o. Adjust A2R19 for +1.024V ± .25mV (6826A) or +.512V ± .125mV (6827A) as indicated on DVM. 5-108 DC Ammeter Calibration.

a. Set the CURRENT METER switch to the high range, DC, 1:2A (6826A) or 0.6A (6827A), position.

b. Adjust A1R20 for a front panel ammeter indication of 1.0A (6826A) or 0.5A (6827A).

c. Turn off BPS/A. Remove the 10.24 K resistors, replace jumpers from A20 to A21 and from A18 to A14. Ensure that jumpers are also connected from A12 to A13 and from A19 to A21. Replace top cover.

#### AC METER CALIBRATION 5-109

AC Voltmeter Calibration 5-110

a. Remove top cover to gain access to potentiometers on board A1.

b. Connect test setup as shown in Figure 5-16 with , appropriate high range load resistor ( $B_L$ ), 49 $\Omega$  (6826A) or 1990 [6827A], connected in series with 10 resistor (Rg) across + and - output terminals. Set function generator for a 5 volt 100Hz squarewave output.

FXD GAIN AMP

5-26

X10

c. Set BPS/A front panel/controls as follows: MODE switch: **RANGE** switch:

**VOLTAGE** control:

CURRENT control:

May be left in any position for this procedure. fully clockwise

VOLTAGE METER: high range AC

CURRENT METER: - high range\_AC

U. Turn on BPS/A and allow a 10-minute warmup. e. Connect oscilloscope to +S and --S terminals

and observe waveform for overshoot and ringing. f. Remove oscilloscope and connect DVM to +S, and -S terminals.

g. Adjust the function generator output level for a DVM reading of 35.3 ± 0.5V rms (6826A) or 70.7 ± 1.0V rms (6827A).

rms (6827A) indication on BPS/A front panel voltmeter

5-111 AC Ammeter Calibration

a. Connect DVM across the  $1\Omega$  resistor.

b. Adjust function generator output level for a DVM reading of .707 ± .03V rms (6826A) or .305 ± .015V rms (6827A).

c. Adjust A1R18 for .7A rms (6826A) or .3A rms (6827A) on BPS/A front panel ammeter.



SECTION VI REPLACEABLE PARTS

#### INTRODUCTION 6-1

This section contains information for ordering re-6-2 placement parts. Table 6-4 lists parts in alpha-numeric order by reference designators and provides the following information:

- a. Reference Designators. Refer to Table 6-1.
- b. Description, Refer to Table 6-2 for abreviations.

c. Total Quantity (TQ). Given only the first time the part number is listed except in instruments containing many sub-modular assemblies, in which case the TO appears the first time the part number is listed in each assembly.

- d. Manufacturer's Part Number or Type
- e. Manufacturer's Federal Supply Code Number. Refer to Table 6-3 for manufacturer's name and address.
  - f. Hewlett-Packard Part Number.

g. Recommended Spare Parts Quantity (RS) for complete maintenance of one instrument during one year of isolated service.

h. Parts not identified by a reference designator are listed at the end of Table 6:4 under Mechanical and/or Miscellaneous. The former consists of parts belonging to and grouped by individual assemblies; the latter consists of all parts not immediately associated with an assembly

#### ORDERING INFORMATION 6-3

To order a replacement part, address order or in-64 quiry to your local Hewlett-Packard sales office (see lists at rear of this manual for addresses). Specify the following information for each part: Model, complete serial number, and any Option or special modification (J) numbers of the instrument; Hewlett Packard part number; circuit reference designator; and description. To order a part por listed in Table 6.4, give a complete description of the part, its function, and its location.

Table 6-1. Refer	ence Designators
A = assembly	E = miscellaneous
B' = blower (fan) C = capacitor ( • 14) CB = circuit breaker	F = fuse
CR = diode	۲K Y, च relay

meter

= device, signaling (lamp)

ADS//

### Table 6-1, Reference Designators (Continued)

	The second se
P = plug	V 📜 = Vacuum tube,
김 김 씨는 영국에 왜 가지 않는 것을 알려야 한다. 이렇게 많이	
-Q = transiştor	neon builb, , the
R = résistor	photocell, etc.
S = switch	VR = zener diode
T = transformer	X/ = socket
	Z = integrated cir.
TB = terminal block	
TS = thermal switch	cuit or network!
	1. 전문 : 2. 전 전 : 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2.

## Table 6-2. Description Abbreviations

A = ampere	mod, = modular or
ac = alternating current	modified
assy. = assembly	mtg = mounting
bd = board	n = nano = 10 <sup>.9</sup> ,
bkt = bracket	NC = normally closed
°C = degree Centigrade.	NO = normally open
cd = card	-NP = nickel-plated
coef = coefficient	••Ω = ohm <sup>tr</sup>
comp = composition	obd = order by
CRT = cathode-ray tube	description
CT. /= center-tapped	OD = outside diameter,
dc. / = direct current	, p = pico = 10 <sup>-12</sup>
DPDT= double pole,	P.C: = printed circuit
double throw	pot. = potentiometer
DPST = double pole,	<pre>c p-p = peak-to-peak .</pre>
single throw	ppm = parts per million
elect = electrolytic	pvr_ = peak reverse
encap = encapsulated	voltage
(F) = farad	rect = rectifier +
<sup>O</sup> F = degree Farenheit	rms = root mean square
fxd = fixed	Si = silicon
Ge = germanium	SPDT = šingle pole,
H = Henry	double throw
Hz = Hertz	SPST:= single pole,
IC = integrated circuit	single throw
ID // = inside diameter · //	SS = small signal
incnd = incandescent	T+ = slow-blow
k = kilo = 10°	tan. =#tantulum
$m = milli = 10^{3}$	Ti ,∓,titanium
M ' ≜ mega = 19 <sup>6</sup>	volt v
µ = micro⊁-10 <sup>-6</sup>	var 👾 = variable
met: = metal	www = wirewound
mfr = manufacturer	.,₩. '= Watt

·· CODI	MANUFACTUBER ADDRESS	ĊODE	MANUFACTURER
0062	EBY Sales Co., Inc. 3 Jamaica, N.Y.	07137	Transistor Electronics Corp.
00650			Minneapolis, Minn
0085	しき 動きなした 売してられた。 お検索がありたいがかんしょうがらい みんげき ちょうしょくしむけ	07 138	Westinghouse Electric Corp. Elmira, N.Y.
	S. Çarolina Div. Pickens, S.C.	07263	Fairchild Camera and Instrument
0112	「「「「」」「「」」「「」」「「「」」「「」」」「「」」」「「」」「「」」」「「」」」「「」」」」		Mountain View, CallL
. , 0125		07387	Birtcher Corp., The Los Angeles, Galiant
0128	入るの事件 ジェート・トレート しょうしょうしょう しょうしょう 行行な行う しょうしょう かいしょう しょうしょう	07397	Sylvania Electric Prod. Inc.
$\{ (A, B, S) \}$	. Stawndale, Calif.		Mountainview, Calif.
01298	Charles The Television of the second state of the	07716	IRC Div. of TRW Inc. Burlington; Iowu*
01680		• • • • • • • • • • • • • • • • • • • •	Continental Device Corp.
01930			Hawthorne, Calif.
02107		07933	Raytheon Co, Components Div.
02600		08484	Mountain View; Calif.
02660	しかえ 動き出し アイ・ション・チャイ ション・ション ちょうしん しんしきょう してき アプログライアライ マイス・オート・	08530	Breeze Corporations, Inc. Union, N.J. Beliance Mica Corp. Brooklyn, N.Y.
0273		08530	Sloan Company, The Sun Valley, Calif.
	Receiving Tube Div. Somerville, N.J.	08730	Vemaline Products Co. Inc.
03508			Wyckoff, N.J.
	Syracuse, N.Y.	06806	General Elect. Co. Minature
03797	Eldema Corp. Compton, Calif.		Lamp Dept. Cleveland, Ohjo
03877		08863	Nylomatic Corp. Norrisville, Pa.
	Wakefield, Mass.	08919	RCH Supply Co. Vernon, Calif.
03888		09021	Airco Speer Electronic Components
•	Cedar Knolls, N.J.		Bradford, Pa
04009	Arrow, Hart and Hegeman Electric Co.	09182	Hewlett-Packard Co. New Jersey Div.
•	Hartford, Conn.		Rockaway, N.J.
04072		09213	General Elect. Co. Semiconductor
04213			Prod. Dept. Buffalo, N.Y.
a state in	Mineola, N.Y.	09214	General Elect. Co. Semiconductor
04404	n a bhailte ann an the she ann an the star a the Ohio Children ann an the Latio		Prod. Dept. Auburn, N.Y.
	Palo Alto, Calif.	09353	C.& K Components Inc. Newton, Mass.
04713	국가 문화 방법 방법 이 비행 이 가지 않는 것 같아요. 한 것 같아요.	09922 .	Burndy Corp.
05277	Bhoenix Arizona	11115	Wagner Electric Corp. 🚄
	[일] - 동생동 ^ 2 - 2 - 2 - 2 - 2 - 2 - 2 - 2 - 2 - 2	11236	Tung Sol Div. Bloomfield, N.J.
05347	이 같은 것 같은	11230	CTS of Berne, Inc. Berne, Ind. , Chicago Telephone of Cal. Inc.
05820	· · · · · · · · · · · · · · · · · · ·	1231	So. Pasadena, Calif.
06001	사람들은 사람들은 사람들은 것을 통해 있는 것은 것을 수 있는 것을 가지 않는 것을 하는 것을 수 있는 것을 하는 것을 수 있다.	11802 -	
	Capacitor & Battery Dept. : Irmo, S.C.	11711	IRC Div. of TRW Inc. Boone, N.C. General Instrument Corp. Newark, N.J.
06004		12136	Reiladelphia Handle Co. Camden, N.J.
1.15	Bridgeport, Conn.	12615	U.S. Terminals, Inc. Cincinnati, Ohio
06486		12617	Hamlin Inc. Lake Mills, Wisconsin
	Semiconductor Plant Lynn, Mass.	12697	Clarostat Mfg. CoInc. Dover, N.H.
06540		13103	Clarostat Mfg. Co. Inc. Dover, N.H. Thermalloy Co.
	New Rochelle, N.Y.	14493	Hewlett Packard Co. Loveland, Colo.
06555	してい 目 のうえい とうとう うちがた とうしき だいしょう ちんゆうかい ほうちゅうしょう ちゃく しょうしゅきり ひとうしょう たちなく 書い せい	14655	Cornell-Dubiller Electronics Div.
	Penacook, N.H.		Federal, Pacific Electric Co.
06666	영화 나는 사람은 가장을 가지 않는 것이 같이 있는 것이 같이 나라는 것이 같이 집에서 한 것이 가지? 이 가지?		Newark, N.J.
06751	Semoor Div. Components, Inc.	14936	Ceneral Instrument Corp. Semicon-
135.4965	- Phoenix, Arizona		ductor-Prod. Group
06776	이 같은 사람들은 것은 같은 것은 것을 수 있는 것을 하는 것을 하는 것이 같이 있다. 나는 것은 것은 것을 가지 않는 것을 하는 것을 하는 것을 수 있다. 나는 것을 가지 않는 것을 하는 것을 하는 것을 하는 것을 하는 것을 수 있다. 나는 것을 하는 것을 하는 것을 수 있다. 나는 것을 수 있다. 아니는 것이 같이 않다. 아니는 것을 수 있다. 아니는 것이 않다. 아니는 것이 같이 않다. 아니는 것이 같이 않다. 아니는 것이 않다.	15801 -	Fenwal Elect: Framingham, Mass. Corning Glass Works Raleigh, N.C.

Table 6-3.--Code List of Manufacturers

4

Use Code 28480 assigned to Hewlett-Packard Co., Palo Alto, California

• • 6.2

CODE	MANUFACTURER ÁDDRESS
16758	Delco Radio Div. of General Motors
	Corp. Kokomo, Ind.
17545	Atlantic Semiconductors, Inc. &
	Asbury Park, N.J.
1780	Fairchild Camera and Instrument Corp.
anti 2	Mountain View, Calif.
17870	Daven Div. Thomas A. Edison Industries
	McGraw-Edison Co. Orange, N.J.
18324	Signetics Corp. Sunnyvale, Calif.
_ 19315 -	Bendix Corp. The Navigation and
	- 「いっ」 と言語できる ディー・ディング ひょうけい とうしょう ちょうしょう しょうしょう しょうしょう
19701.	Electra/Midland Corp. Mineral Wells, Texas
1	
21520	Fansteel Metallurgical Corp. No. Chicago, III.
	· Union Carbide Corp. Electronics Div.
22229	Mountain View, Calif.
00700	UID Electropics Corp. Hollywood, Fla.
22753	Pamotor, Inc. Pampa, Texas
23936	General Electric Co. Schenectady, N.Y.
24446	General Electric Co.
24455	Nela Park, Cleveland, Ohio
29655	General Radio Co. West Concord, Mass.
24681	LTV Electrosystems Inc. Memcor/Com-
24001	ponents Operations Huntington, Ind
. 26982	Dynacool Mfg: Co. Inc. Saugerties, N.Y.
27014	National Semiconductor Corp.
1.1-	Santa Clara, Calif.
28480	Hewlett-Packard Co. Palo Alto, Calif.
28520	Heyman Mfg, Co. Kenilworth, N.J.
28875	IMC Magnetics Corp. Rochester, N.H.
31514	SAE Advance Packaging, Inc.
	Santa Ana, Calif.
31827	Budwig Mfg. Co. Ramona, Calif.
33173	G.E. Co. Tube Dept. Owensboro, Ky.
35434	Lectrohm, Inc. Chicago, III.
37942	P.R. Mallory & Co. Indianapolis, Ind.
42190	Muter Co. Chicago, III.
43334	New Departure-Hyatt Bearings Div,
	General Motors Corp.
	Sandusky, Ohio
44655	Ohmite Manufacturing Co. Skokie, III.
46384	Penn Engr. and Mfg: Corp.
	Doylestown, Pa.
47904	Polaroid Corp. Cambridge, Mass.
49956	Raytheon Co. Lexington, Mass.
55026	Simpson Electric Co. Div. of American
	Gage and Machine Co. Chicago, III.
66289	- Sprague Electric Co.
	- North Adams, Mass.
58474	Superior Electric Co. Bristol, Conn.
58849	Syntron Div. of FMC Corp. Homer City, Pa,
a dia mandri dalam National dia kaominina	Homer City, Fa,

ADDRESS MANUFACTURER CODE Philadelphia, Pa. Thomas and Botts Co. 59730<sup>°</sup> Union Carbide Corp. New York, N.Y. 61637 Ward Leonard Electric Co. 63743 Mt. Vernon, N.Y. Union City, N.J. Amperite Co. Inc. 70563 Beemer Engrg Co. 70901 Fort Washington, Pa. Chicago, III. 70903 Belden Corp. Willoughby, Ohio Bud Radio; Inc. 71218 Cambridge Thermionic Corp. 71279 Cambridge, Mass. Bussmann Mfg. Div.of McGraw & -71400 St. Louis, Mo. Edison Co. 🗧 👘 Elkhart, Ind. CTS Corp. 71450 I.T.T. Cannon Electric Inc. 71468 Los Angeles, Calif. Globe-Union Inc. 71590 Milwaukee, Wis. General Cable Corp. Cornish 71700 Wire Co. Div. Williamstown, Mass. Providence, R.I. Coto Coil Co. Inc. 71707 Chicago Miniature Lamp Works 71744 Chicago, III. Cinch Mfg: Co. and Howard 71785 Chicago, III. B. Jones Div. Midland, Mich. Dow.Corning Corp. 71984 Electro Motive Mfg. Co. Inc. 72136 Willimantic, Conn. Brooklyn, N.Y. Dialight Corp. 72619 General Instrument Corp. Newark, N.J. 72699 Drake Mfg. Co. Harwood Heights, III. 72765 Elastic Stop Nut Div. of 72962 Union, N.J. Amerace Esna Corp. **Eric Technological Products** 72982 Erie, Pa. Hart Mig. Co SHartford; Conn. 73096 Beckman Instruments 73138 Fullerton, Calif. Ashland, Mass. Fenwal, Inc. 73168 Hughes Aircraft Co. Electron 73293 Torrance, Calif. Dynamics Div. Amperex Electronic 73445 Hicksville, N.Y. Bradley Semiconductor, Corp. 73506 New Haven, Conn. Hartford, Conn. Carling Electric, Inc. 73559 Federal Screw Products, Inc. 73734 Chicago, III. Heinemann Electric Co. Trenton, N.J. 74193 Hubbell Harvey Inc. Bridgeport, Conn. 74545 Amphenol Corp. Amphenol RF Div. 74868 Danbury, Conn. Waseca, Minn. 74970 E.F. Johnson'Co.

Table 6-3: Code List of Manufacturers

CODE	MANUFACTURER ADDRESS	CODE	MANUFACTURER ADDRESS
75042	IRC Div. of TRW, Inc. Philadelphia, Pa.	82866	Research Products Corp. Madison, Wisc.
75183	"Howard B. Jones Div. of Cinch	82877	Rotron Inc. Woodstock, N.Y.
	Mig, Corp. New York, N.Y.	82893	Vector Electronic Co Glendale, Calif
75376	Kurz and Kasch, Inc. Dayton; Ohio	83058	Carr Fastener Co. Cambridge, Mass.
75382	Kilka Electric Corp. Mt. Vernon, N.Y.	83186	Victory Engineering Springfield, N.J.
.75915	Littlefuse, Inc. Des Plaines, Ill.4	83298	Bendix Corp, Eatontown, N.J.
76381	Mingesota Mining and Mfg. Co.	. 83330	Herman H. Smith, Inc. Brooklyn, N.Y.
	St. Paul, Minn.	., 83385	Central Screw Co. Chicago, III.
76385	Minor Rubber Co. Inc. Bloomfield, N.J.	83501	Gavitt Wire and Cable Brook field, Mass.
76487	James Millen Mfg. Co. Inc. Malden; Mass.	83508	Grant Pulley and Hardware Co.
76493	TW. Miller Co.		West Nyack, N.Y.
76530	City of Industry, Calif.	83594	Burroughs Corp., Plainfield, N.J.
76854	Oak Mfg. Co., Div, of Oak Electre/	83835	U.S. Radium Corp. Morristown, N.J.
	Netics Corp: Crystal Lake, III.	83877	Yardeny Laboratories New York, N.Y.
77068	Bendix Corp., Electrodynamics Div,	. 84171	Arco Electronics, Inc. , Great Neck, N.Y.
	No. Hollywood, Calif.	84411	TRW Capacitor Div, Ogàllala, Neb.
77122	Palnut Co. Mountainside, N.J.	86684	RCA Corp. Harrison, N.J.
77147	Patton-MacGuyer Co. Providence, R.I.	86838	Rummel Fibre Co. Newark, N.J.
77221	Phaostron Instrument and Electronic Co.	87034	<ul> <li>Marco &amp; Oak Industries Anaheim, Calif.</li> </ul>
	• South Pasadena, Calif.	87216	Philco Corp. Lansdale, Pa.
77252	Philadelphia Steel and Wire Corp.	87585	Stockwell Rubber Co. Philadelphia, Pa.
	Philadelphia, Pa.	87929	Tower-Olschan Corp. Bridgeport, Conn.
77342	American Machine and Foundry Co.	88140	Cutler Hammer Inc. Lincoln, III.
	Princeton, Ind.	88245 :	Litton Precision Products Inc. USECO
77630	TRW Electronic Components Div.		Van Nuys, Calif.
	Camden, N.J.	90634	Gulton Industries Inc. Metuchen N.J.
77764	Resistance Products Co. • Harrisburg, Pa.	90763	United-Car. Inc. Chicago, III.
78189	Illinois Tool Works Inc. Elgin, III.	91345	Miller Dial and Nameplate Co.
78452	Everlook Chicago, Inc. Chicago, Ill.		El Monte, Calif.
78488	Stackpole Carbon Co. St. Marys, Pa.	914.18	Radio Materials Co. Chicago, III.,
• 78526	Stahwyck Winding Div. San Fernanda	91506	Augat, Inc. Attleboro, Mass.
	Electric Mfg. Co. Inc. Newburgh, N.Y.	91637	Dale Electronics; Inc. Columbus, Neb.
78553	Tinnerman Products, Inc. Cleveland; Ohio	91662	Elco Corp. Willow Grove, Pa
78584	Stewart Stamping Corp. Yonkers, N.Y.	91929	Honeywell Inc.
79136	Waldes Kohinoor, Inc L.I.C., N.Y.	92825	• Whitso, Inc. Schiller Pk., III.
.79307	Whitehead Metals Inc. New York, N.Y.	93332	Sylvania Electric Prod. Woburn, Mass.
79727	Continental-Wirt Electronics Corp.	93410	Essex, Wire Corp. Mansfield, Ohio
	Philadelphia, Pa:	94144-	Raytheon Co. Quincy, Mass.
79963	Zierick Mfg. Co. Mt. Kisco, N.Y.	94154	Wagner, Electric Corp. Livingston, N.J.
80031	Mepco Morristown, N.J.	94222	Southco Inc. Lester, Pa
80294	Bourns, Inc. Riverside, Calif.	95263	Leeeraft Mig, Co. Inc. L.I.C., N.Y.
81042	Howard Industries Racine, Wisc.	95354	Methode Mfg. Co. Rolling Meadows, III.
81073	Grayhill, Inc.	95712	Bendix Corp, Franklin, Ind.
81483	International Rectifier El Segundo, Calif.	95987	Weckesser Co. Inc. Chicago, Wi
81751	Columbus Electronics Yonkers, N:Y.	96791	Amphenor Corp. Janesville, Wis
82099	Goodyear Sundries & Mechanical Co. Inc.	97464	Industrial Retaining Ring Co.
$\{ P_{i} \} \in \{ 0, 1 \}$	New York, N.Y.		Irvington, N.J.
82142	Airco Speer Electronic Components	97702	IMC Magnetics Corp. Westbury, N.Y.
1. 4. S. A.	Du Bois, Pa. 🕴	98291	Sealectrö Corp. Mamaroneck, N.Y:
82219	Sylvania Electric Products Inc.	98410 -	ETC Inc. Cleveland, Ohio
Sharpon -	Emporium, Pa	•08978	International Electronic Research Cord.
*82389 82647 -	Switchcraft, Inc. Chicago, III. Metals and Controls Inc. Attleboro, Mass.	99934	Burbank, Calif. Renbrandt, Inc. Boston, Mass
			THE REPORT OF A DESCRIPTION OF A

Table 6-3. Code List of Manufacturers

Use Code 71785 assigned to Cinch Mfg. Co., Chicago, Ill.

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REF DESIG.	DESCRIPTION	TQ	MER. PART NO.	MFR. CODE	HP PART NO.	RS
A1,	Interconnect and Power Supply Board					
CI	Not Assigned			56289	0180-0094	1
C2	fxd, elect. 100µF 25Vdc	1	30D107G025DD2-DSM	56289	0180-0332	1
C3,4	fxd, elect. 325µF 35Vde	2	- D34656-DEE	56289	0150-0052	2
C5-C7	fxd, cer05µF 400Vdc	8	33C17A3-CDH	No. 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0180-1885	1
<b>C8</b>	fxd, elect, 200µF 175Vdc	2.	68D10223	56289	0150-0052	, °,
C9	fxd, cer. 05µF 400Vdc		33C17A3-CDH	56289	0100-0098	
.C10		2		reapo	0180-2193	
6826A	fxd, elect. 3000µF 85Vdc		36D302G085AC2A-DQB	56289	0180-1808	
6827A	fxd, elect. 430µF 200Vdc		32D6008	56289		
C11-C14	fxd, cer. 0.5µF 400Vdc		33C17A3-CDH	. 56289	0150.0052	
C15					0100 0102	 
6826A	fxd, elect. 3000µF 85Vdc	•	36D302G085AC2A-DOB	56289	0180-2193	
6827A	fxd, elect: 430µF 200Vdc	i 1	32D6008	56289	0180-1808	1.3
C16	fxd, elect. 200µF 175Vdc	1	68D10223	56289	0180-1885	
(11) Apple 10 (11)	fxd, cer47µF 25Vdc	2	5C11B7-CML	56289	0160-0174	
C17, C18	Diode, Si. 200mA 75V	4	1N4148	28480	1901-0050	14
CR14	Diode, Si. 200prv 1A	8	1N5095	28480	1901-0327	
CR5-8						
CR9	Not Assigned	8	A14D	03508	1901-0328	3 B S S -
CR10-17	Diode, Si, 400V 1A	1		28480	1901-0535	
CR18	Diode, Hot Carrier				1	
CR19				化验验学		
6826A	Not Used (Jumper)			28480	1901-0518	
6827A	Diode, Hot Carrier			28480	1901-0518	
CR20	Diode, Hot Carrier		ero 10 20 240	71785	1251-2134	
J1, J2	Connector, Printed Circuit Edge	2		76381	1251-3119	1111
J3, J4	Connector, Multi-contact	2		09023	0490-0745	
K1	Relay; 6Vdc coil voltage -	1	603-6	02735	1853-0041	10 <b>-</b>
01	Power PNP Si.	· . <b>1</b>	, 2N4036,	01295		
02	•SS NPN Si.	1	2N4141	28480		
03	Power NPN Si.	1	2N1711A	56289		- <b>1</b>
Q4	SS PNP Si.	1* <b>1</b>	2N2907	- P	2100-175	
R1,2,3	var, ww. 100, 5%, 1W	3	CT-106-4	84048	2100-175	1
R4		<u>,</u> 2			0686-563	:
6826A	4xd, comp. 56K ±5%, ½W		EB5635	01121		
.6827A	fxd, comp. 51K ±5%, ½W	u u	EB5135	01121	0686-513	1
R5, 6		2			0000 000	
6826A	fxd, metal oxide 5.1K ±5%, 2W		RG42	11502		1 1 1
6827A	fxd, metal oxide, 33K ±5%, 2W	n ann A	RG42	11502	0764-004	<b>P</b>
R7 .	fxd; comp. 56K ±5%, ½W		EB5635	01121		
6826A	fxd, comp.,51K ±5% ½W		1 EB5135	01121		
6827A	var, ww 5K ±5%, 1W		2 / CT-100-4	84048	2100-074	1
. R8		ALC ()	2			
* R9	C.J. CT. 0 00K +19/ 1/9W		CEA T-0	07716	0757-028	8
6826A	fxd, film; 9.09K ±1%, 1/8W		CEA T-0	07710	698-508	7
6827A	fxd, film, 6.2K ±1%, 1/8W	0 35				
} <b>R10</b> }}			CEA T-O	07716	0757-044	1
6826A	fxd, film, 8.25K ±1%, 1/8W		CEA T-0	07710		
. 6827A	fxd, film, 12K ±1%, 1/8W	비난음	소 [1] 양 전문수가 비행을 감독한 관계 관계 관계		a na estas en a	

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REF. DESIG.	DESCRIPTION	το.	MFR. PART NO.	MFR. CODE	HP PÅRT NO.	Ĩ
		2				
A1B11 '		· · · · · ·	CEA T-0	07716	0757-0453	
6826 <u>A</u>	fxd, film, 30.1K ±1%, 1/8W •		CEA T-0	• 07716	0698-3157	
6827A	fxd, film, 19.6K ±1%, 1/8W		CEATU .		0000 0.01	
R12				07716	0698-3572	
6826A	fxd, film, 60.4K ±1%, 1/8W		CEATO	07716	0698-3265	
6827A //	fxd, film, 118K ±1%, 1/8W		CEA T-0	,84048	2100-0741	
R13	var, ww, 5K ±5%; 1W		CT-100-4	,04040	2100 0.11	
R14		II		07716	0757 0440	
6826A	fxd, film, 7.5K ±1%, 1/8W		CEA T-O		0698-5087	
6827A	fxd, film, 6.2K ±1%, 1/8W		CEA T-0	07716	0088/3087	
R15					0757 0274	
6826A	fxd, film, 1.21K ±1%, 1/8W		CEA T-0	07716	[1] A. L. M.	1
6827A	fxd, film, 1.1K ±1%, 1/8W		CEAT 0	07716	0757-0424	
R16		321				
6826A	1xd, film, 30.1K ±1%, 1/8W		CEA T-O	07716	0757-0453	
6827A	fxd, film, 42.2K ±1%, 1/8W		CEATO	07716	0698-3450	1
R17		2		하는 사람이		ļ.
6826A	fxd, film, 1.69K ±1%, 1/8W	$SEC = \{i_i\}_{i \in I}$	CEA T-0	07716	0698 4428	. 1
6827A	fxd, film, 16.2K ±1%, 1/8W		CEA T-0	07716	0767-0447	
R18	var, ww.200 ±5%, 1W	2	CT-100-4	84048	2100 1771	
R19		$C_{i} = \{i,j\}$				
	fxd, film, 1:78K ±1%, 1/8W	通道 (1)	CEATO	• : 07716		
6826A	fxd, film, 16.2K ±1%, 1/8W		CEATO	07716		
6827A	var, ww. 200 ±5%, 1W		CT-100-4	. 84048	2100-1771	
R20	1 var, www, 200 ± 570, 111		266년 268년 - 482	1		<u>.</u>
R21	fxd, film, 2,21K ±1%, 1/8W		CEA T-0	. 07716	+0757-0430	) [
6826A	$120, 1100, 2,210 \pm 100, 1000$		CEA T-0	07716	0698-3512	2
6827A	( fxd, film, 1.18K ±1%, 1/8W				194	2
R22			CEA T-O -	07716	0698'3435	5
6826A	fxd, film; 38:3 ±1%, 1/8W					
6827A	Not Used (Jumper)	$\frac{1}{2}$ $\frac{1}{2}$ $\frac{1}{2}$ $\frac{1}{2}$ $\frac{1}{2}$				3
<b>F</b> 23				07716	0757-0410	3
6826A	fxd, film, 511 ±1%, 1/8W		CEA T-O	07716	a ana sa	· ·
6827A	fxd, film, 178 ±1%, 1/8₩		CEA T-0	4		
R24		<u>,   1</u>		07716	0698-508	7
6826A	fxd, film, 6.2K.±1%, 1/8W		CEA T-O			
6827A	+ fxd, film, 3,48K ±1%, 1/8W	23 ( <b>P</b> 1	. CEA T-0	.01121		
R25	fxd, comp. 4.3K ±5%, ½W	<b></b>	EB-4325			
R26	fxd, comp. 7.5K ±5%, ½W	2	EB-7525	0112	金田 化二苯基乙基甲基乙	
R27	fxd, comp. 750 ±5%, ½W	i (	EB-7515	0112		
R28	fxd, comp. 1K ±5%, ½W	्र <b>ा</b> ।	EB-1025	0112	S 1 12 1 1 4 4 5	
R29	fxd, film, 3.92K ±1%, 1/8W	1.	CEATO	- 0771	그렇게 가 귀찮게 가 가지 않는 것	6 A 1
R30	fxd, film, 6.81K ±1%, 1/8W	- <b>i</b> .	CEA T·O	. 07710		
R31	fxd, comp. 7.5K ±5%, %W		E& 7525	0112		
R32	fxd, film, 1.3K ±1% ½Ŵ		CCA T-0	0771	3 0757-073	2
NI R33	Not Assigned					<u> </u>
" R34	fxd, film, 5.49K ±1%, 1/8W	11	CEA T-O	0771	6, 0698-338	2
R35	Not Assigned	> 12	<u>推进运行</u> 化分散管理			
ション・ショー おおもい	fxd, film, 9.09K1±1%,-1/8W		CEATO	0771	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
<b>R36</b> 1	fxd, comp. 620 ±5%, ½W		EB-6215	0112	1 0686-621	51

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Table 6-4. Replaceable Parts

REF.	<b>DESCRIPTION</b>	τQ	MFR: PART NO.	MFR. CODE	HP PART NO.	R
A1R38		-1			0011 1001	
6826A	fxd, ww, 750 ±5%, 5W		243E	56289	0811-1861	14
6827A	Not Used (Jumper)					
R39		1			0044 1001	
NA STATES AND A STATE	fxd, ww. 750 ±5%, 5W		. 243E	56289	0811-1861	
6826A	fxd, comp. 36K ±5%, 5W.		EB-3635	01121	0686-3635	
6827A	Not Assigned	a Second				
R40	G fxd, film, 10K ±1%, 1/8W	1	CEA T-0	07716	0757-0442	
a R41	fxd, ww, 10.24K ±.05%, ½W		132F	20940	0811-2958	
<b>R42</b>	1X0, WW, 10.241 1.05 //, /20	-1	EB-1325	01121	0686-1325	
R43	fxd, comp. 1.3K ±5%, ½W	4	CEA T-0	07716	0698-3430	•
<b>914,45</b>	fxd, film, 21.5 ±1%, 178W			E.		
R46	Not Assigned	2				
R51,52	A DY 140 1/014		MF4C T-0	19701	0757-0460	<b> </b> ·
6826A	fxd, film, 61.9K ±1%, 1/8W		CEA T-O	07716	0757-0441	
6827A	fxd, film, 8.25K ±1%, 1/8W		CEA T-0	07716	0698-5092	
R53	fxd, film, 160K ±1%, 1/8W		CEATO	07716	0757-0316	
R54	fxd, film, 42.2 ±1%, 1/8W			02606	0837-0023	
R55	Thermistor, 64 ±10%		LB16J1	07716	0757-0279	1.1
R56	fxd, film, 3.16K ±1%, 1/8W	1	CEA T-0			
R57	Not Assigned					•
R58		:  1		07716	0757-0283	
6826A	fxd, film, 2K ±1%, 1/8W		CEA T-0		0757-0438	- I
6827A	fxd, film, 5.11K ±1%, 1/8W		CEA T-0	07716	0737-0430	
R59	Not Assigned	100			0011/1722	
R60	fxd, ww, 1 ±5%, 3W	1	242E 1R05	56289	0811-1732	
	Not Assigned			4 A	0000 0470	
R61-65	fxd, film, 6K ±1%, 1/8W	4	CEA T-0	07716	0698-3476	
R66-69	fxd, film, 21.5 ±1%, 1/8W		CEA T-0	07716	0698-3430	1 1
R70, 71	Switch, rotary, 3 sections	2		28480	3100-1941	U. 1
S1, 2		2	1N825	28480	1902-1221	
VR1	Diode, zener 6.2V	1.7				
VR2	Not Assigned	1	1N825	28480	1902-1221	
VR3	Diode, zener 6.2V					_
A2	Voltage and Current Control Plug-In					
	Board		RDM15E300J3S	00853	0160 0181	÷.
Ċ1	fxd, mica, 30pF ±5%, 300V	5	HDW110C000000			
C2,3	Not Assigned		RDM15E300J3S	00853	0160-0181	
C4,5	fxd, mica, 30pF ±5%, 300V					л. Х
<b>C6</b>		<b>!</b>		.56289	0160-2477	1
6826A	fxd, cer015µF-1KV		C023B102M1537S27-CDH		the second second	
6827A	fxd, cer01µF 1KV		6023A102J103MS38-CDH	00209		
C7		្រា		ODOFO	0160-3068	2
6826A	fxd, mica, 1500pF ±5% 300V	1	RDM19F152J3S	00853	8	
	fxd, mica, 1000pF ±5% 100V		RDM15E102J1C	00853		
6827A .	fxd, mica, 30pF ±5% 300V		RDM15E300J3S	00853		- Y
C8	fxd, mylar, .001µF ±10% 200V	<u>े ि</u> 1	292P10292-PTS	56289		
,C9	fxd, tant. 2.2µF 20Vdc	<u> 1</u> 1	150D225X0020A2-DYS	56289	0180-015	Э. Д
C10		2				<u>.</u>
🦉 C11, 12			292P22392-PTS	56289		1.14
6826A	fxd, mylar, .022µF ±10% 200V		292P68292-PTS	56289	0160-015	<b>9</b>
6827A	fxd, mylar, 0068µF ±10% 200V			1. 1. 1. 1. 1. 1.		- 1

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Table 6-4. Replaceable Parts

REF. "DESIG.	<b>DESCRIPTION</b>	то	MFR. PART NO.	MFR. CODE	HP, PART NO.	
A2C13	fxd, elect: 100µF, 6ydc	1	30D107G006CC2-DSM	56289.	0180-1734	1
C14	fxd, mica, 30pF ±5%, 300V		RDM15E300J3S	.00853	0160-0181	
CR1, 2	Diode, Si. 200mA 75V	· 13	1N4148	28480	1901-0050	7
CR3, 4	Diode, Si. 250mW 200prv	6	1N485	28480	1901-0033	5
CR5	Diode, Si. 200mA 75V		1N4148	28480	1901-0050	
CR6	Not Assigned					
CR7, 8	Diode, Si. 250mW 200prv		1N485	28480	1901,0033	
CR9	Diode, Si: 200mA 75V		1N4148	28480	1901-0050	
CR10	Diode, Hot Carrier	1		28480	1901-0535	1
CR11,12 -	Diode, Si. 250mW 200pw	્યતી કે મહાત્ર મહાત	1N485	28480	1901-0033	
CR13,14	Diode, Si. 200mA 75V		1N4148	28480	1901-0050	
CR15-17	Not Assigned					
CR18-24	Diode, Si. 200mA, 75V		1N4148	28480	1901-0050	
<b>K1</b>	Reed Relay.	<u>(</u> 1		28480	0490-1013	d t
	Reed Relay	2 1		28480	0490-0399	2
l 1	Indicator, 1 microhenry	1		28480	9100-2198	1 "
Q1-4 (	SS NPN SI.	5	2N4141 //	01295	1854-0071	5
<b>`</b> Q5	SS PNP Si.	(0, 0, 1)	2N2907,	56289	1853-0099	1
Q6	SS NPN SI.		2N4141	01295	1854:0071	(2)
R1,2	fxd, film, 1K ±1%, 1/8W	. 6	CEA T-0	07716	0757-0280	6
R3	fxd, ww, 714±1%, ¼W	( ) ( <b>1</b> )	R303B	01686	0811-1935	
R4	fxd, film, 1K ±1%, 1/8W		CEA T-0	07716	0757-0280	
R5	fxd, film, 6K ±1%, 1/8W •	5	CEA T-0	07716	0698-3476	1
.R6, 7	fxd, ww. 10.24K ±.05%, ½W	2	132F	20940	0811-2958	212
R8	fxd, film, 6K±1%, 1/8W		CEA T-0	07716	0698-3476 *	
R9	Not Used (Jumper)					
R10	fxd, film, 6K,±1%; 1/8W		CEA T-0	07716	0698-3476	r da si Davisi
R11,12	fxd, film, 100 ±1%, 1/8W	4	CEA T-0	07716	0757-0401	1
R13.						
6826A	fxd, film, 4.75K ±1%, 1/8W	- <b>1</b> -	CEA T-0	07716	0757-0437	1
6827A	fxd, film, 6.81K ±1%, 1/8W	2	CEA T-0	07716,		1
• R14				4		*
6826A	fxd, film, 5.11K ±1%, 1/8W	4	CEA T-0	07716	0757-0438	1
6827A	fxd, film, 6.2K ±1%, 1/8W	12	CEA T-0	07716	0698-5087	1.
- R15						
6826A	fxd, film, 511 ±1%, 1/8W	1	CEA T-O	07716	0757-0416	1
6827A	fxd, film, 619 ±1%, 1/8W	1	CEA T-0	07716	0757-0418	1
,R16.		•			4	
6826A	fxd, ww, 10.24K ±.05%, ½W		132F	20940	0811-2958	
6827A	fxd, ww, 20.48K ±.05%, ½W	1	132F	20940	0811-2959	1
R17, 18	fxd, film, 1.18K ±1%, 1/8W	2	CEA T-0	07716	0698-3512	1
R19	var, ww. 10K ±5%, 1W	2	CT-106-4	11502	21'00-0989	1
R20-	fxd, film, 57.6K ±1%, ¼W	2	CCARDO	07716	0757-0114	1
R21	Var, ww 10K ±6%, 1W		CT+106-4	11502	2100-0989	
R21	fxd, film, 57.6K ±1%, ¼W		CCA T-0	07716	0757-0114	
- R23.		4				
6826A	Not Used (Jumper)					
6827A	fxd, film, 1Meg ±1%, ½W	4	CCA T-0	07716	0757-0344	1
/ R24	fxd, comp, 3:9 ±5%, %W			01121	0698-5139	
You Man Charles			EB39G5	A CHINE I -		

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Table, 6-4. Replaceable Parts

REF. DESIG.	DESCRIPTION	та	MFR. PART NO.	MFR. CODE	HP PART NO.	RS
A2R25						1
6826A	fxd, film, 221K ±1%, 1/8W	an e	CEA T-0	07716	0757-0473	r
6827A	fxd, film, 422K ±1%, 1/8W		CEA T-0	07716	0698-3460	
R26						
6826A	Not Used (Jumper)					
6827A	fxd, film, 1Meg ±1%, %W#		CCA T-0	07716	0757-3444	
R27	fxd, ww. 1 ±.5%, 8W	1	<b></b>	01686	0811-2133	
R28,29				07746	0757-0344	1
6826A	. fxd, film, 1Meg ±1%, %W	2	CCA T-O	07716	0757-0344	
. 6827A	fxd, film, 1Meg-±1%, ¼W		CCA T-0	07716	0/3/0344	
R30,31	Not Assigned			07716	0757-0288	2
R32,33	fxd, film, 9.09K ±1%, 1/8W	2	CEA T-0	07716	0698-5088	1
R34	fxd, film, 12K ±1%, 1/8W	(1) <b>1</b>	CEA T-0	077.16	0757-0283	1
R35	fxd, film, 2K ±1%, 1/8W	3	CEA T-O.	07716	0757-0441	1
R36	fxd, film, 8.25K ±1%, 1/8W	2	CEA T-0	07716	0698-3430	1
<b>R</b> 37,38	fxd, film, 21.5 ±1%, 1/8W	2	CEA T-0	07716	0757-0441	
R39	fxd, film, 8.25K ±1%, 1/8W		CEA T-0	01121	0686-8205	1
R40	fxd, comp. 82 ±5%, ½W		EB-8205	07716	0757-0438	
R41	2 fxd, film, 5.11K ±1%, 1/8W		CEA T-0			16
R42			400E	20940	0811-3200	1
6826A	fxd, ww, 92.16K ±.05%, ½W		132F	20940	0811-3201	1
6827A	fxd, ww, 184.32K ±.05%, ½W		132F :			1
R43,44	Not Assigned	2	CEA T-0	£7716	0757-1093	1
R45	fxd; film, 3K ±1%; 1/8W	4	CEA T-0	07716	0757-0440	1 k - 1
R46	fxd, film, 7.5K ±1%, 1/8W	<b> </b>   <b> </b>	CEA T-0	07716	0698-5087	- I
·R47	fxd, film, 6.2K ±1%, 1/8W		CEA T-0	07716	0757-0440	
R48	fxd, film, 3K ±1%, 1/8W	·	CEA T-0	07716	0757-0446	- 10 L
R49	fxd, film, 15K ±1%, 1/8W		EB-1025	01121	0686-1025	
R50	fxd, comp. 1K ±5%, ½W	, <b>1</b> - 17	CEA T-0	07716	0698-3476	
R51	fxd, fijm, 6K ±1%, 1/8W		CEA T-0	07716	0757-0401	
R52, 53	fxd, film, 100 ±1%, 1/8W		CEA T-0	07716	0698-3476	
R54	fxd, film, 6K ±1%, 1/8W		CEA T-0	07716	0757-0452	
R55	fxd, film, 27.4K ±1%; 1/8W		CEA T-0	07716		
R56	fxd, film, 2K ±1%, 1/8W		CEA T-0	07716		
R57	fxd, film, 7.5K ±1%, 1/8W	i	* CT-106-4	84048		<b>,</b>
R58	var, ww, 1K ±5%, 1W			84048	2100-1752	2
R59	var, ww. 10 ±5%, 1W	1		84048		;
R60,61	var, ww, 100 ±5%, 1W				e star i si ya	
	Not Assigned		CEA T-0	7 07716	0757-0440	)
R63	fxd, film, 7.5K ±1%, 1/8W		CEA T-0	07716	1. D. C. M	1
R64	fxd, film, 5.11K ±1%, 1/8W		CEA T-0	07716	0757-0440	ן (
R65	fxd, film, 7.5K ±1%, 1/8W		CEA T-0	07716		3° 🕌
R66	fxd, film, 5.11K ±1%, 1/8W	日本美				1
R67-70	Not Assigned		CEA T-0	077.16		
R71,72	fxd, film, 1K ±1%, 1/8W fxd, film, 6.81K ±1%, 1/8W		L CEA T-O	07716	0757-0439	
R73	$f_{X,0}$ , film, 0.0 IN $\pm 1.70$ , 1/0W		CEA T-0	07716	0757-0280	)   }
R74	fxd, film, 1K ±1%, 1/8W fxd, film, 5.49K ±1%, 1/8W	2 B.A	CEA T-0	07716		
R75	fxd, film, 24.3K ±1%, 1/8W	2	◎】 「「」 → 「 → 」 → 」 → 」 ( ) ( <b>) ひて</b> んか ( <b>)</b> →	07716	6 0757-045	1
R76 🌾		18 18 5			as <u>biy biyong</u> ana	<u> </u>

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REF. DESIG.	DESCRIPTION	то	MFR. PARTNO:	MFR. CODE	HP PART NO.
A2R77	fxd, film; 2.43K ±1%, 1/8W	•	CEA T-0.	07716	0757-0431
.R78	fxd, film, 24.3K ±1%, 1/8W		CEA T-0	07716	0757-0451
R79	fxd, film; 2K ±1%, 1/8W		CEA T-0	07716	0757-0283
St	Slide Switch, 0.5A, 125Vat/dc	1	GF126-0020	79727	3101-1311-
VR1-5	Diode, zener 6.2V	5	1N825	28480	1902-1221
VR1-5 VR6	Diode, zener 7.50V 400mW	2	SZ10939-146	04713	1902-0064
VR7	Not Assigned	4	3210333140	04713	1302-0004
VRV	Diode, zener 7.50V 400mW		SZ10939-146	04713	1902-0064
U1-5	IC, Linear Amplifier	5	LM301AH	27014-	1820=0223
A3 ( ) ( + ) ( ) (	6826A Power Amplifier Plug-In Board		energia de la compositiva de la construcción de la construcción de la construcción de la construcción de la con El construcción de la construcción d		
C1	fxd, elect. 20µF 15Vdc	1 <b>1</b>	30D206G016BB2-DSM	56289	0180-0300
C2	fxd, efect. 1µF 35Vdc	2 <b>1</b> 2 3	150D105X9035A2	-56289	0180-0291
C8-8	Not Assigned				
C0-0 C9	_ fxd, mica, 150pF 300Vdc	: 1315년 11 19월 <b>1</b> 9일	RDM15F151J3C	00853	0140-0196
C10	fxd, mica, 1900F 300Vdc		RDM15F331J5S	00853	0140-0190
그는 그 아이들은 글 것이 봐야?					0160-2012
C11	fxd, mylar, .001µF 200Vde		192P10292	56289	
C12,13	fxd, mylar .047µF 200Vdc	2	292P47352-PTS	56289	0160-0138
C14	fxd, cer: ,02µF 500Vdc	· 1 .	C023B501J203ZS25	56289	0160-0468
C15	fxd, cer. 5000pF 1KV	<b>1</b>	C023B102G502ZS31-CDH	56289	0160-0899
CR1-7	Diode, Si. 200mA 75V	13	1N4148	28480	1901-0050
-CR8-13	Not Assigned				
CR14	Stabistor, Si. 10prv 400mW	2	1N4157	28480	1901-0460
CR15-17	Diode, Si. 200mA 75V		1N4148	28480	1901-0050
CR18	Not Assigned	in tr			
CR19-21	Diode, Si. 200mA 75V		1N4148	28480	1901-0050
CR22 : "	Stabistor, Si. 10prv 400mW		1N4157 0	28480	1901 0460
· <b>Q1</b>	SS NPN Si.	3	2N4141	28480	1854-0071
02	SS NPN SI.	8	40346	86684	1854-0095
<b>Q3</b>	SS PNP SI.	6	-SJ5099	04713	1853 0038*
Q4,5	SS NPN SI.		2N4141	28480	1854-0071
Q6	SS PNP SI	$b \in \mathbb{R}^{n}$	SJ5099	04713	1853-0038
Q7	SS NPN Si.		40346	86684	
Q8,9	SS PNP SI.	2		28480	1853-0037
Q10-12	SS NPN SI.		40346	86684	1854,0095
"013	SS PNP'SI.		SJ5099	04713	1853-0038
Q14-16	SS NPN SI.	7	40346	86684	1854-0095
017-19	SS PNP SI.		90540 SJ5099	04713	1853-0038
			The second s		Sector 1. Sector 1. Sector 1.
R1,2	fxd, comp; 15K ±5%, %W	2	EB-1535	01121	0686-1535
R3	fxd, comp, 510 ±5%, ½W	1	EB-5115	01121	0686-5115
- <b>R4</b>	Not Assigned				orno odor
R5	fxd, comp, 3K ±5%, ½₩-	1	EB-3025	01121	0686-3025
R6	fxd, cömp, 1K ±5%, ½W	2	EB-1025	01121	0686-1025
R7	fxd, comp, 1,2K,±5%, ½W	2	EB-1225	01121	0686-1225
R8 •	fxd, comp, 8.2K ±5%, ½W	5	EB-8225"	01121	0686-8225
R9 🕴	fxd, comp, 750 ±5%, ½W	2	EB-7515	01121	0686-7515
R10	, fxd, comp, 6.2K ±5%, %W	1	EB-6225	01121	0686-6225
R11,12	fxd, comp, 8.2K ±5%, %W		EB-8225	01.121	0686 8225
R13	fxd, comp, 1.2K ±5%, %W	対応部門	EB-1225	01121	0686-1225

Table 6-4. Replaceable Parts

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REF. DESIG. /	DESCRIPTION	тα	MFR: PART NO.	MFR. CODE	HP PART NO.	RS-
A3R14	fxd, comp. 8.2K ±5%, %W	•	EB-8225	01121	0686,8225	
R15	/fxd, comp. 5.1K ±5%, %W	3	EB-5125	01.121	0686-5125	3
R16.	fxd, comp. 8,2K ±5%, ½W * .	<b>1</b>	EB-8225	01121	0686-8225	1
B17~	fxd, comp, 750 ±5%, ½W,		EB-7215	. 01121	0686-7515	
R18	fxd, comp. 1K ±5%, %W		EB-1025	01121	0686-1025	en Plei
,R19, 20	Not, Assigned					
-R21	fxd, comp, 5.1K ±5%, ½W		• EB-5125	01121	0686-5125	
R22 -	fxd, comp, 1.5K ±5%, %W		EB-1525	.01121	0686-1525	
R23	fxd, comp, 5.1K ±5%, %W		EB-5125	01121	0686-5125	
R24	fxd, comp, 5.6K ±5%, ½W	1	EB-5625	01121	0686-5625	<u>ା</u> 1 ୍
R25	∙fxd, comp, 100±5%, ¼₩ -	1	'EB-1025	01121	0686-1015	1
R26	fxd, comp, 4.3K ±5%, XW.	1	EB-4325	01121	0686-4325	<b>1</b>
R27:	fxd, comp, 1.6K ±5%, ½W	1	EB-1625	01121		1.
R28	fxd, comp, 18K ±5%, %W	: <b> </b>	EB-1835	01121	0686-1835+	
R29	fxd, comp. 390 ±5%, ½W	1	EB-3915	01121	0686-3915	1
R30	fxd, comp, 200 ±5%, %W	1	EB-2015	01121	0686-2015	
R31	fxd,.comp, 10K±5%, %W	2	EB-1035	01121	0686-1035	. <b>* 1</b> 42
R32	fxd, comp, 30 ±5%, ½W	3	EB-3005	01121	0686-3005	1
R33	fxd, comp, 10K±5%, ½W	• •	EB-1035	01121	0686-1035	
R34	√fxd, comp, 30 ±5%, ½₩*		EB-3005	01121	0686-3005	
R35	fxd, comp, 360 ±5%, ½W	3	EB-3615	01121	0686-3615	11
R36	fxd, comp, 330 ±5%, ½W	1	EB-3315	01121	0686-3315	1.
R37-42	fxd; comp, 39 ±5%, ½W	6	EB-3905	01121	0686-3905	1
R43,44	fxd; comp, 360 ±5%, ½W		EB-3615	01121	0686-3615	<b>•</b>
R45-48	fxd, ww, 3 ±5%, 3W	4	242E	56289	0811-1224	1
R49,50	Not Assigned					
R51	fxd, ww; 1.25 ±1%, 4W	2 1	NS-2-18	91637	0811-2556	1
	Not Assigned	12				13.00
R52	, j1 fxd, comp, 30 ±5%, ½₩		EB-3005	01121	0686-3005	
R53	fxd, comp, 47 ±5%, ½W	1	EB-4705	01121	0686-4705	1
R54	fxd, comp, 10 ±5%, ½W	11	EB-1005	01121	0686-1005	1
, R55	Not Assigned					
R56-65	fxd, comp, 160K ±5%, ½W	1	EB-1645	01121	0686-1645	1
R66	Diode, zener 61,9Vdc		9711213-368	04713	1902-0660	1
. VB1	Diode, zener 56.2Vdc	1.	SZ11213-356	04713	1902-0597	<b>i</b>
VR2	DIODE, Zeiter DO'S And		<b>1</b> 0	E Provincia		
A3	6827A Power Amplifier Plug-In Board			1	0180-0300	1
C1	fxd, elect, 20µF.15Vdc	<b></b>		56289	0180-0300	
• C2	fxd, elect. 1µF 35Vdc	1		56289		
• C3 -	fxd, mylar, .047µF 200Vdc	1	292P47352-PTS	56289	0160-0138	. 1
C4-12	Not Assigned				0160 0284	
4 C13 💷	fxd, mylar, .01µF 400Vdc	, <b>1</b>	663UW	84411	0160-0381	1     1
C14	fxd, mylar, .01µF 200Vdc	2	192P10392	56289	0160-0161	
• C15	fxd, elect. 5µF 150Vdc	្រា	40D505F150DC4	56 89	0180-1841	
C16	fxd, mica, 150pF 300Vdc	1	RDM15F151J3C	00853	0140-0196	
C17-	fxd, mylar, .022µF 200Vdc	1,	지수는 것은 것이 있는 것이 가지 않는 것이 있는 것이 있는 것이 있는 것이 없다. 것이 있는 것이 없는 것이 없 않 않이 않	56289	0160-0162	
C18	fxd, mylar, 01µF 400Vdc		192P10392	56289	0160-0161	
CR1.2	n Diode, Si. 200mA 75V	8	1N4148 *	28480		
CR3:4	Diode, Si. 250mW 200prv	. 2	. 1N485B	28480	1901-0033	2

Table 6-4. Replaceable Parts

Table 6-4. Replaceable Parts

REF. DESIG.	DESCRIPTION	<b>то</b> :	MFR. PART NO.	MFR. CODE	HP PART NO.	F
A3CR5,6	Diode, SI. 200mA 75V		1N4148	28480	1901-0500	
- CR7-10	Not Assigned					
CR11,12	Stabistor, Si: 10prv 400mW	8	1N4157	28480	1901-0460	
CR13, 14	Not Assigned					
CR15, 16	Stabistor, Si. 10prv 400mW	1	1N4157 •	28480	1901-0460	1
CR17	Not Assigned					
CR18-21	Cliode, Si. 200mA 75V	1.1	1N4148	28480	1901-0500	þ
CR22-25	Stabistor, Sil 10prv 400mW		1N4157	28480	1901-0460	
ά αi 🐂 🔸	SS NPN SI	3	2N4141	28480	1854-0071	41 1
02	SS NPN SI	2	40346	86684	1854-0095 .	
<b>Q3</b>	SS PNP Si.	8	SJ5099	04213	1853-0038	
Ú4,5	SS NPN SI.		2N4141	28480	1854-0071	
Q6.	SS NPN Si.		40346	86684	1854-0095	
07. 107	SS PNP SI.		SJ5099	04713	1853-0038	•
OB 1.	SS NPN SI.	10.50 0 <b>1.1</b> 5	SJ1679	04713	1854-0232	
Q9,10	SS PNP Si		SJ5099	04713	-1853-0038	
	SS NPN SI.	1997 - B.		04713	1854-0271	
01), 12		6	MM2258	<ul> <li>A second sec second second sec</li></ul>	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
O1316	SS PNP Si.		SJ5099	04713.	1853,0038	
Q17-20	SS'NPN Si.		MM2258	04713	18540271	
R1,2	fxd, comp, 15K ±5%, %W	2	EB-1535	.01121	0686-1535	144
R3 -	fxd, comp, 510 ±5%, ½W	1	EB-5115	01121	0686-5115	
8 <b>R4</b> (1997)	Not Assigned			6 . Well - F		
R5	fxd, comp, 3K ±5%, ½W	•1	EB-3025	01121	0686-3025	
R6-20	Not Assigned		가지, 지지, 아이가 아이지, 지지, 이가 아이가 아이가 있다. 이가 아이가 아이가 있다. 이 것은 것은 아이가			
R21	fxd, comp, 5.1K #5%; ½W	. 3	EB-5125	01121	0686-5125	
R22	fxd, comp, 1.5K ±5%, %W	1	EB-1525		.0686-1525	
R23	fxtl, comp, 5.1K ±5%, ½W		EB-5125	01121	0686-5125	•
R24	fxd, comp, 5.6K ±5%, ½W	1 •	EB-5625	01121	0686-5625	
R25	, fxd, comp, 100 ±5%, ½W	2	EB-1015	01121*	0686 1015	
R26	fxd, comp, 4:3K ±5%, %W	1	EB-4325	01121	The second s	R
R27	fxd, comp, 10K±5%, ½Wo	1	EB-1035	01121	0686-1035	
R28	• fxd, comp, 820 ±5%, ½W	1	EB-8215	01121	0686-8215	
R29	fxg, comp, 5.1K ±5%)%W	1	EB-5125	01121	0686-5125	1
R30	fxd, comp, 1.2K ±5%, ½W	1	EB-1225	01121	0686-1225	
. R31	fxd, comp, 910 ±5%, %W	1	EB-9115	01121	0686-9115	
R32	"fxd, metal gxide, 36K ±5%, 2W		RG-42	11502	0698-3651	
R33	fxd, comp, 2K ±5%, 1W	1	GB-2025	01121	0689-2025	
R34	fxd, comp, 300 ±5%, ½W		EB-3015	01121	0686-3015	
R35	fxd, comp, 180 ±5%, ½W,	3	EB-1815	01121	0686-1815	
An a thread of the sector of the	fxd, comp, 750 ±5%, 24%.	<b>J</b>	EB-7515	01121	0686-7515	
R36			EB-1015	01121	0686-1015	1 2
R37	fxd, comp, 100 ±5%, ¼W					• •
R38	fxd, comp, 1K ±5%, %W	1	EB41025	01121	0686-1025	1.1
R39	fxd, comp, 240 ±5%, ½W		-EB-2415	01121	0686-2415	
R40	fxd, comp; 39 ±5%, ½W	1	ÆB 2905	01121	0686-3905	
R41	fxd, comp 360,±5%, ½W	1	8B-3615	01121	0686-3615	
R42	fxd, metal oxide, 33K, 2W	2	Type C42S	16299	0764-0046	
R43	fxd, metal oxide, 47K, 2W	5	Type C42S	16299	0764-0031	
R44 .	fxd, comp, 160K ±5%, ½W	2	EB-1645	01121	0686 1645	
.R45	fxd, metal oxide, 47K, 2W	1.1.1	Type C42S •	16299	0764-0031	1.

	Table 6-4. Repl	aceable Parts	in series Series Marine Constant			REF.	Table 6-4. I			MFR.	ΗP	Ţ
REP	DESCRIPTION	MFR. PART NO.	MFR. CODE	HP PART NO,	RS	DESIG.	DESCRIPTION	ΤQ	MFR. PART NO	CODE	PART NO.	
DESIG, A3R46 R47 R48 R49 R50	fxd, metal oxide, 22K, 2W, 3 fxd, comp, 200 ±5%, 4W, fxd, comp. 100 ±5%, 7W, fxd, comp. 100 ±5%, 7W, fxd, metal oxide, 47K, 2W, fxd, comp, 82 ±5%, 7W, 0 2	Type C42S EB-2015 EB-1815 Type C42S EB-8205	16299 01121 01121 16299 01121	0764-0045 0686-2015 0686-1815 0764-0031 0686-8205 0686-2015	13 13 13 14 3	A5B1 R2 S1 S2	var, ww. dual ganged 15K-15K (CURRENT Control) var, ww. 25K (VOLTAGE Control) Switch, Toggle SPDT 5A (LINE Switch). Switch, Rotary, 3 Section (RANGE/MODE Selection Switch)			-28480 28480	2100-3271 2100-3272 3101 <sup>1</sup> 1605 3100-1942 <sup>1</sup>	
R51 R52, R59 R54 R55 R56, 57 R58	fxd, comp, 200 ±5%, ¼W         fxd, comp, 82 ±5%, ¼W         fxd, comp, 180 ±5%, ¼W         fxd, ww, 2.7.±5%; 2W         fxd, metal oxide, 22K, 2W         fxd, metal oxide, 47K, 2W         fxd, metal oxide, 33K, 2W	EB-2015 EB-8205 EB-1815 Type BWH Type C425 Type C425 Type C425	-01121 '01121 -	0686-8205 0686-1815 0811-1671		A6 A1-4 Q5;6 6826A 6827A	Heat Sink Assembly – Electrical Power NPN Sr.	4	60128 60128	86684 86684		42
R59 R60 R61 R62 R63 R64 R65	fxd, comp, 82K ±5%, ½W fxd, comp, 200 ±5%, ½W fxd, comp, 27 ±5%, ½W fxd, mata/ oxide/ 22K, 2W fxd, comp, 200 ±5%, ½W fxd, comp, 200 ±5%, ½W fxd, comp, 27 ±5%, ½W	EB-8235 EB-2015 EB-2705 Type C42S EB-2015 Type BWH EB-2705	01121 01121 16299 01121 07716 01121 01121	0686-2015 0686-2705 0764-0045 0686-2015		R1-3 6826A 6827A W2 -TB1-3 W1	Not Used fxd, ww, 2,7 ±5%, 2W Ribbon Cable Assembly Terminal Block Ribbon Cable Assembly Chassis Electrical	1	Type BWH	28480	0360-1766	
R66 R67-69 R70 VR12 VR3 VR4 	fxd, comp, 160K ±5%, %W Not Assigned fxd, comp. 4.7 ±5%, %W Not Assigned Diode, zener 16.2V 400mW Diode, zener 20.5V 400mW Diode, zener 56.2V 1W Diode, zener 56.2V 1W	EB-1645 EB-47G5 1N966 SZ10939-272 SZ11213-356 SZ11213-368	28480 04713 04713 04713	0698-000 1902-018 1902-018 1902-059 1902-056	1 1 4 1 2 1 7 2 0 2	C1,2 C3 6826A 6827A T1 6826A 6827A	fxd, mylar 1µF 220Vac fxd, cer., 1µF 500V Not Used Transformer, Power Transformer, Power	2, 1 代	439P1059220 41C9#B5-CDH	56289 28480	0160-0269 0160-0269 06826-80091 06827-80091	1 1
VR6,7 VR8 A4 F1	Diode, zener 56.2V 1W Power Module lincludes slide, switch and fuse) Fuse! 2A 250V SIO Blo	SZ11213-356		1902-059 5060-118 2110-030	9		A1 Interconnect and Power Supply Board – Mechanical Heat Dissipator (Q1, Q3) A3 Power Amplifier Board – Mechanical	2	NF-207 <sup>47</sup>	05820	1205-0033	
A5 DS1 6826A 6827A DS2	Front Panel – Electrical, Indicator Lamp (LINE) Indicator Lamp (LINE) Indicator, Light Emitting Diode (CUR RENT-MODE) -		28480 28480 28480		17		Heat Dissipator 6926A (Q6, Q9-Q11) 6827Å (Q13:Q20) Heat Dissipator 6826A (Q14-Q19)	4 8 6	NF-207 NF-207 2227-8	05820 05820 13103	1205-0033	
M1 6826A /8827A M2	Voltmeter, Dual Range DC or AC (±6; ±60Vdc or 4, 40V rms) Voltmeter, Dual Range DC or AC (±12, ±120Vdc or 8, 80V rms)			) 1120-137 ) 1.120-133 0 1.120-133	72		Hear Sink Assembly — Mechanical A Bushing Insulator 6826A (Q1-Q4) 6827A (Q1-Q6) Insulator, Mica 6826A (Q1-Q4)	4			0340-0795	
6826A 6827A	Ammeter, Dual Range DC or AC (±0.12, ±1,2A or 0.08, 0.8A rms) Ammeter, Dual Range DC of AC (±0.06, ±0.6A or 0.04, 0.4A rms)			0 1120-13			6827A (Õ1-Q6) Chassis Rear Heat Sink	6 1 1			-5000-9369 5020-8401	

# Table 6-4. Replaceable Parts

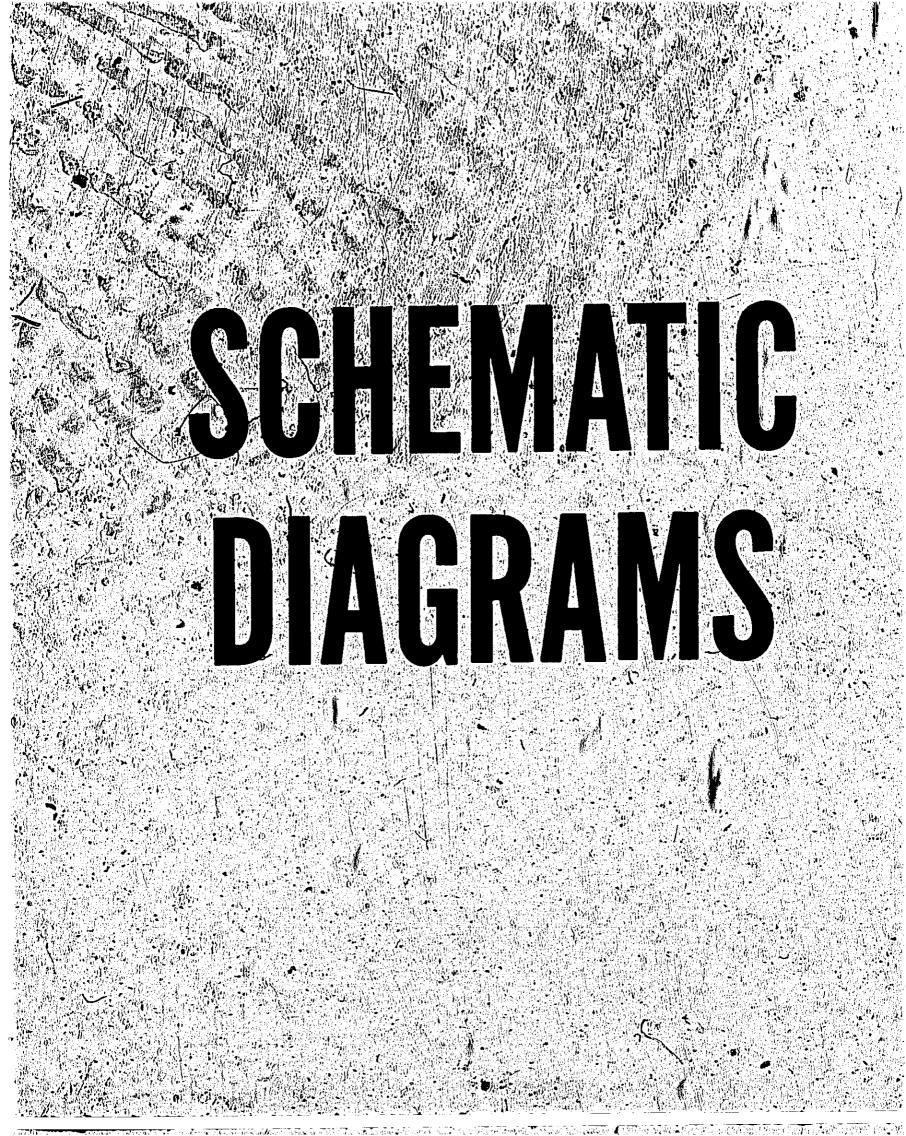
			<u>15</u> 91	
	DESIG.	DESCRIPTION	TO.	MFR. PART N
1		Heat Sink Assembly (Continued)	3	於聖台語語
		Barrier Strip Board Assembly		的主义。但是
ľ		Heat Sink Board Assembly	3 1	
		6826A		
		6827A	<b>)</b> • (	自的分别通信
-		Front Panel - Mechanical	a tağır. Karalış	
		Bezel, Meters	3 731.1 1	
		Control Panel 6826A •	4 2001) 1997 - Den	
		6827A	部的	
		Output Panel	્યાન	
		Foot Assembly	#1 <u>}</u>	
		Insulator, Binding Post. (Black)	) <b>2</b>	
		Insulator, Binding Post (Red)		
		Control, VOLTAGE METER		
		switch, CURRENT METER, switch)		
		Knob (MODE select)	<b>.</b> 1	
1		Knob (RANGE select)	3 <b>1</b> .	
		Collar, LED (CURRENT MODE/Indi-		
		cator)		
		Lamp Holder, Clear		
Ì		Binding Post (Red)	4	
		Binding Post/(Black)	t	
		Tilt Stand	14114 14101	And the second second and the second s
-		Chassis – Mechanical		
		Cover, Top. -Cover, Battom	· •	
		Cover, Side	27	
1		Chassis	1	
		Rear Panel, Bottom	<b> -</b> _1	
ì		Foot Assembly	]	
		Jumper, Barrier Strip	9	
		Standoff 8-32 x .875 Cover Barrier Strip	11	
		6 x 1.1 Frame Assembly	2	
		MAN AND A CARD AND A		
		Miscellaneous		/MDX-1A
	Second second	Fuse 1A 250V Slo-Blo		
		Power Card		
9		Floater Pad, Carton		
s' Y				
1		Option/007:		
		10-Turn VOLTAGE Control		
		var, ww 20K ±5%, 2W, lidear, 10-turn knob		

6813.6633

6 15

2848(	71400 28480 28480 28480	28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480	28480 28480 28480 28480 28480 28480 28480 28480 28480 28480	28480 28480	MFR, CODE	
2100 1867	8120-1348 9211-1196	5000-9368 5000-9332 5000-9345 5000-9345 5060-0728 0360-0523 0380-0849 5000-9356	5040-0234 5040-0305 1510-0094	5060 0728 0340 0733	HP PART NO.	
					RS	

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## SECTION VII CIRCUIT DIAGRAMS

# 7-1 INTRODUCTION

7.2 . This section contains the circuit diagrams necessary for the operation and maintenance of BPS/A Models 6826A and 6827A.

# 7-3 SIMPLIFIED SCHEMATIC DIAGRAM

7.4 This diagram, Figure 7.1, shows the relationship between the instrument assemblies and ties the schematic diagram sheets together.

# 7-5 COMPONENT LOCATION ILLUSTRATIONS

7.6 The component location diagrams show the physical location of parts mounted on each assembly. They are included on the schematic diagrams where they apply or on the rear of the provious schematic. Thus, the schematic diagram is unfolded to the right and component location.

## diagram is unfolded to the left

## 7-7 SCHEMATIC DIAGRAMS

7-8 Separate schematic glagrams are provided for Model 6826A and 6827A as follows:

Figure 7-2 (Sheet ). Model 6826A Output Power Amplifier Circuits, Schematic Diagram

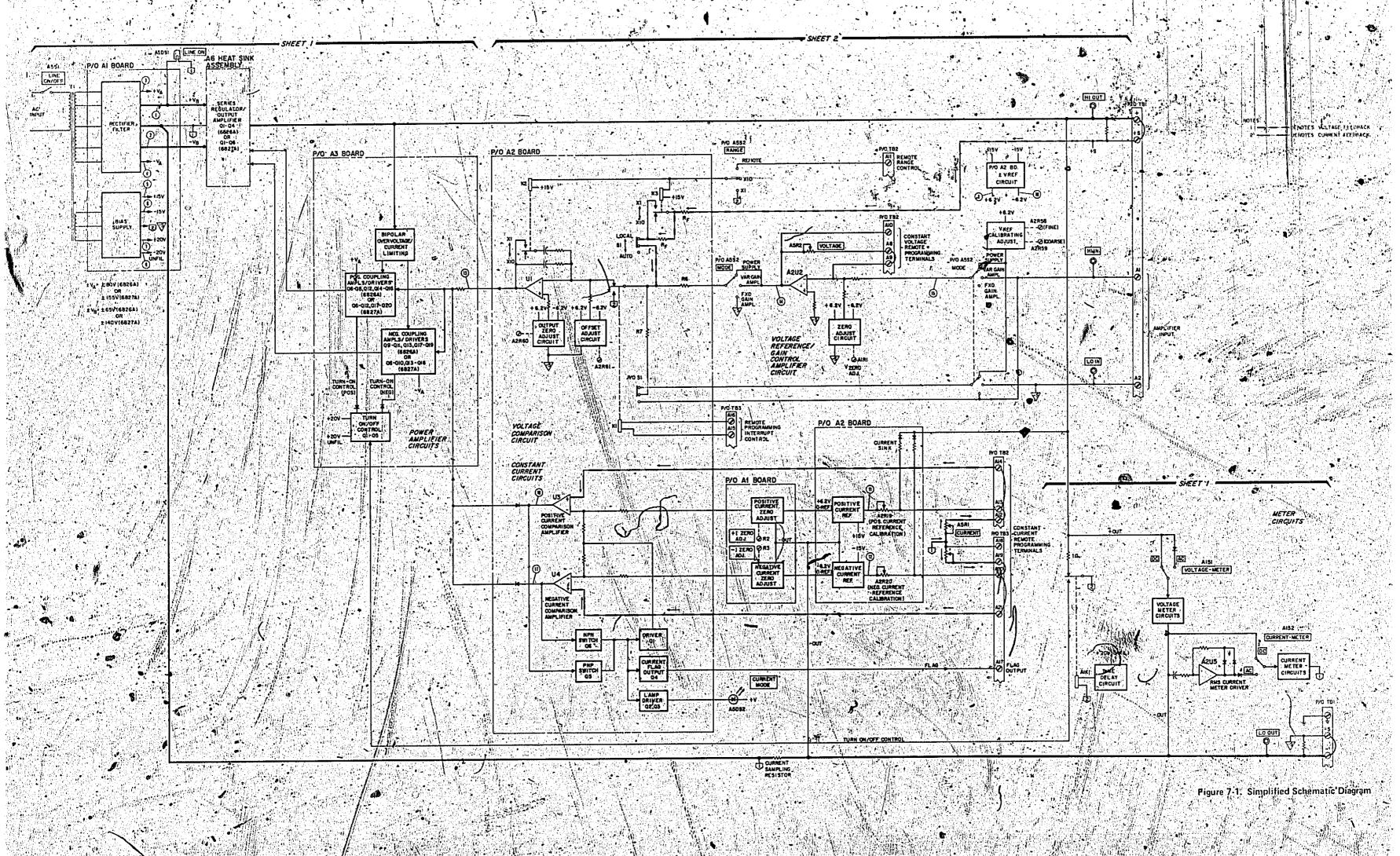
Figure 7.2 (Stick 21. Model 6826A Voltage and Current Control Circuits Schematic Diagram

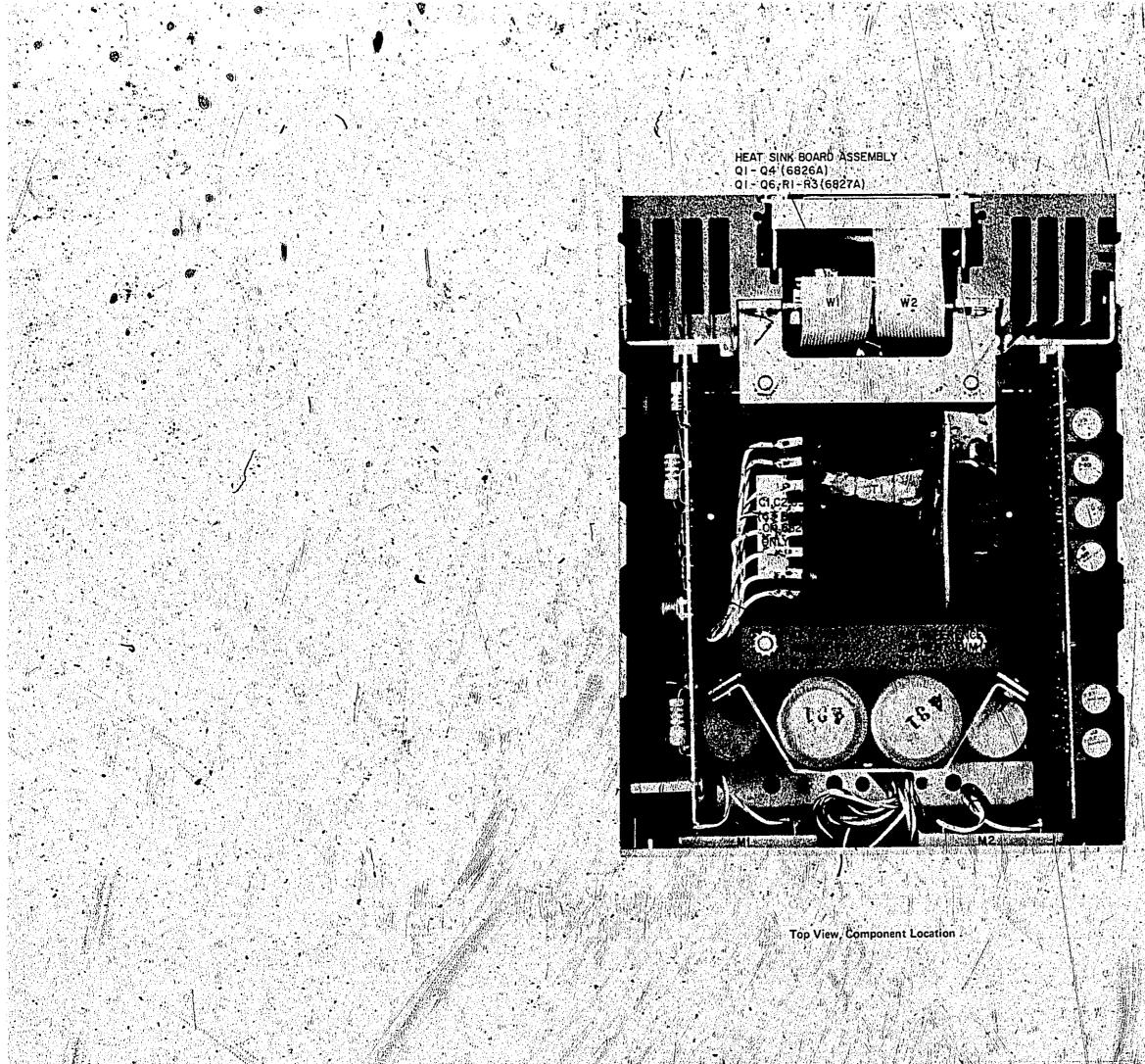
Figure 7:3 (Sheet 1), Model 6927A Output Power Amplifier Circuits, Schematic Diagram

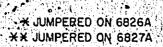
Figure 7.3 (Sheet 2). Model 6827A Voltage and Current Control Circuits, Schematic Diagram

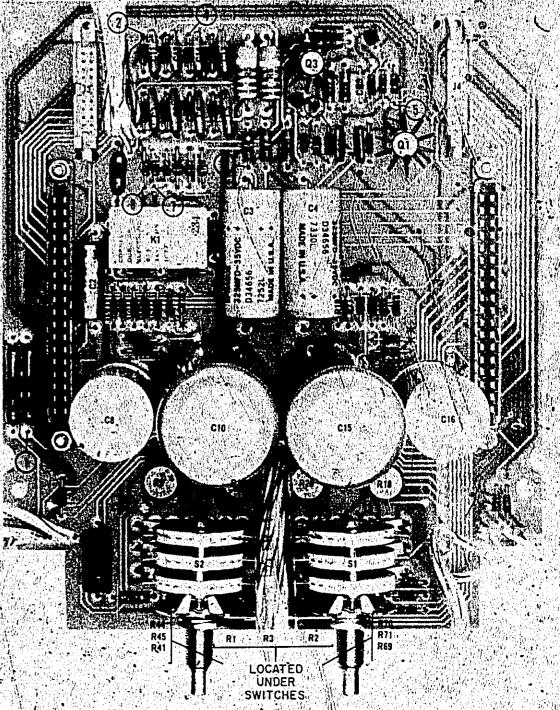
7.9 Test points (encircled numbers) appear on the schr atics. These points coincide with the test prints on the component location diagrams and are referred to in the text.

14

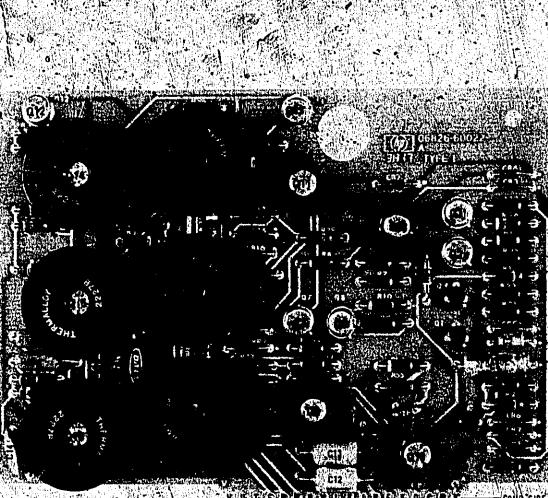








A1 Power Supply and Interconnect Board, Component Location



345

A3 Amplifier Board (6826A only), Component Location

NOTES: ALL RESISTORS ARE IN OHNS, 1/2W, 5% UNLESS OTHERWISE INDICATED ALL 1/8W AND 1/4W RESISTORS ARE 1% UNLESS OTHERWISE INDICATED 3. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE INDICATED 4. \_\_\_\_\_ DENOTES FRONT PANEL MARKING.

DENOTES VOLTAGE FEEDBACK

6 \_\_\_\_\_\_ DENOTES CURRENT FEEDBACK

nc n - - -TO-3

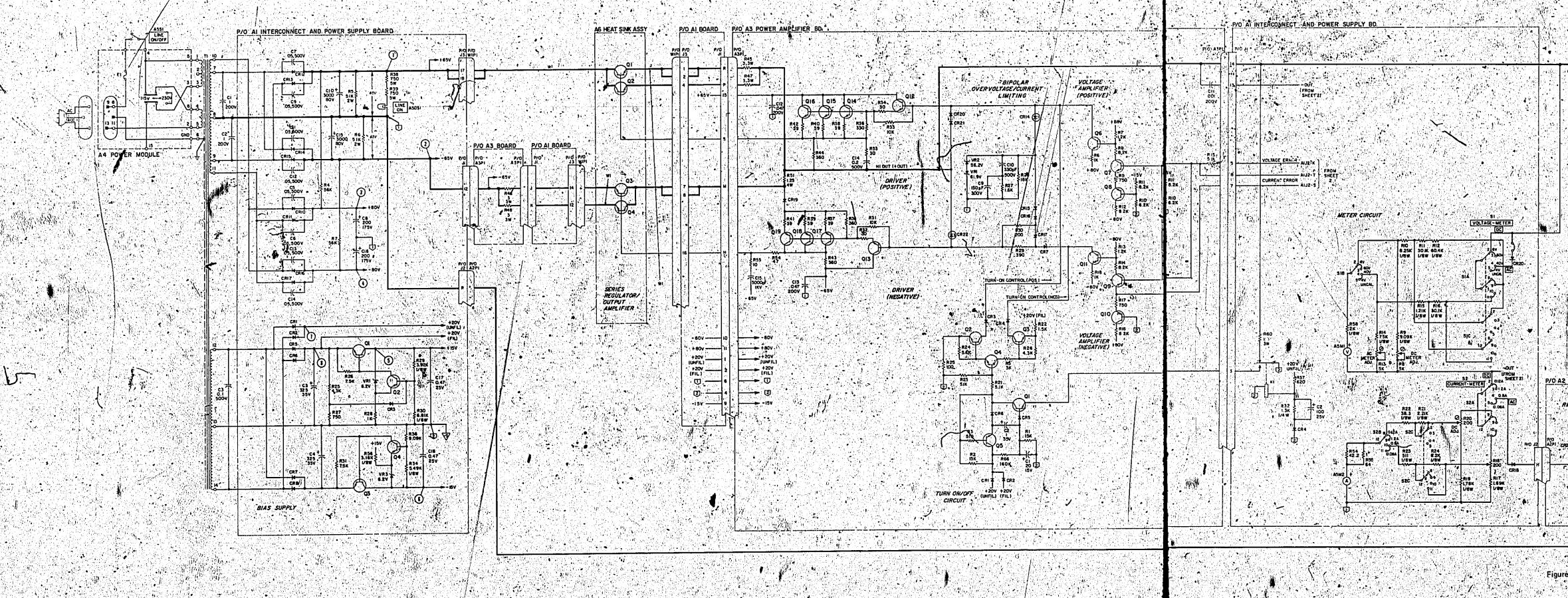
(ALL BOTTOM VIEWS)

B. PIN LOCATIONS FOR INTEGRATED CIRCUITS A2UI A2US ARE AS FOLLOWS

(BOTTOM VIEW)

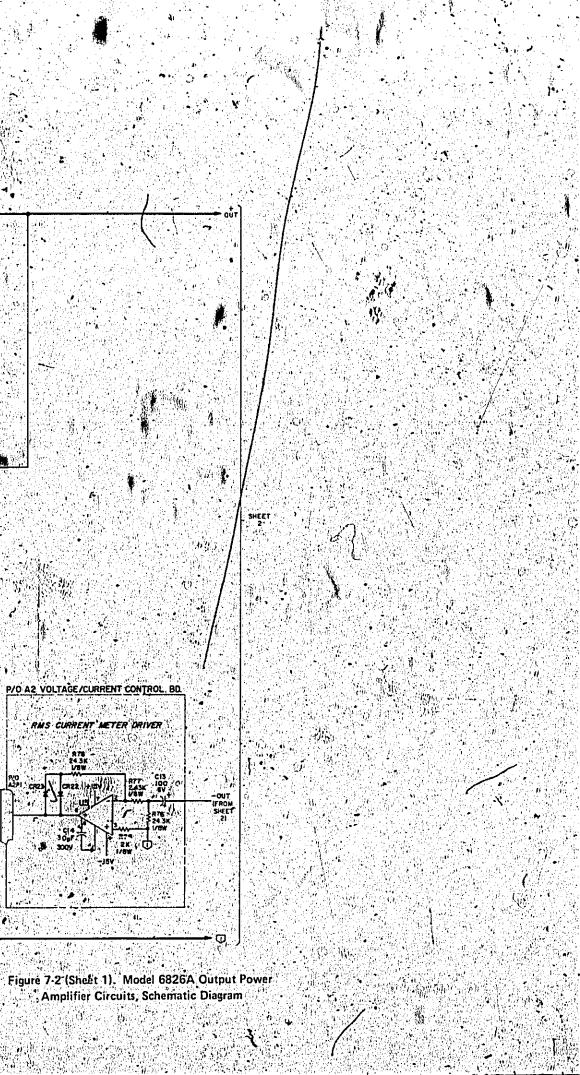
9. THE LOCAL/AUTO SWITCH, LOCATED INSIDE THE UNIT ON BOARD A2, IS ACCESSIBLE BY REMOVING THE RIGHT SIDE COVER OR TOP COVER, FOR NORMAL OPERATION, THE SWITCH MUST BE LEFT, IN THE LOCAL POSITION (PUSHED TO THE RIGHT OR TO REAR OF UNIT) THE AUTO POSITION (PUSHED TO LEFT OR FRONT OF UNIT) IS USED ONLY DURING AUTO-SERIES, OR AUTO-PARALLEL OPERATION OF TWO OR MORE UNITS.

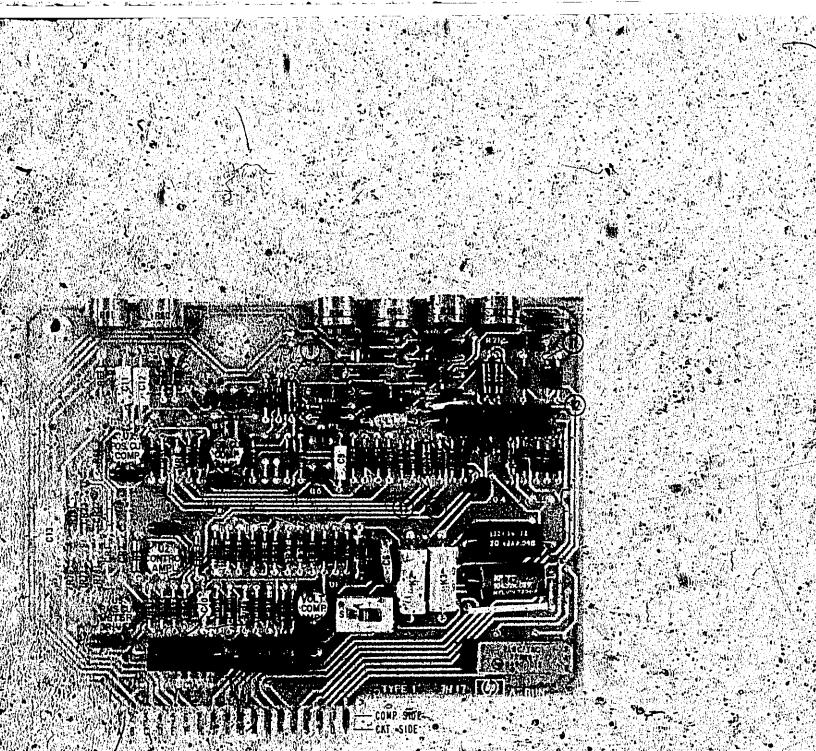
IO JUMPER AZWI IS REDUCED FOR OPEN COLLECTOR OPERATION. NOTE THAT THIS CIRCUIT. IS REFERENCED TO [3] COMMON WHICH IS COMPECTED TO THE -S. TERMINAL



Constant States

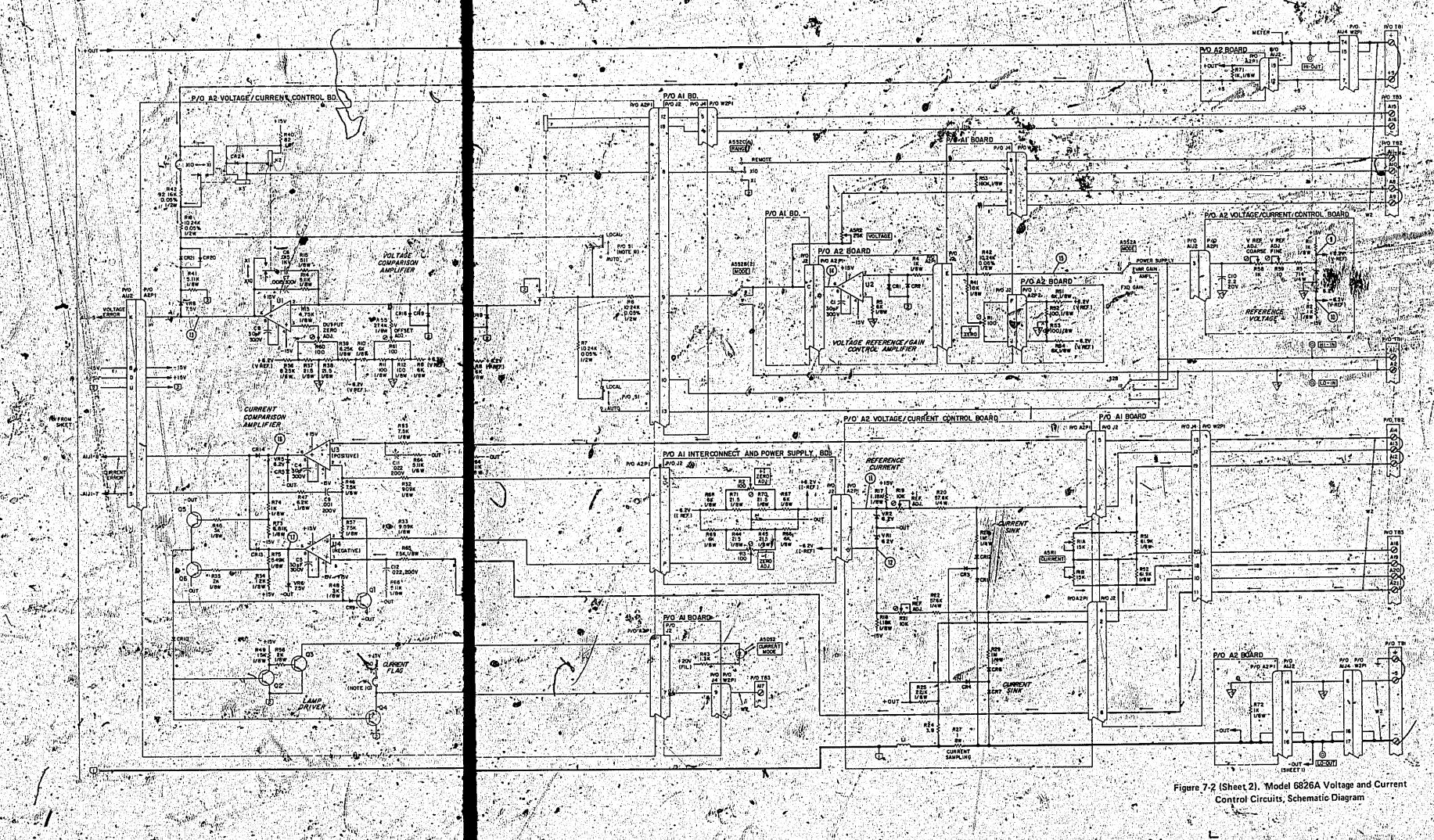
127 200



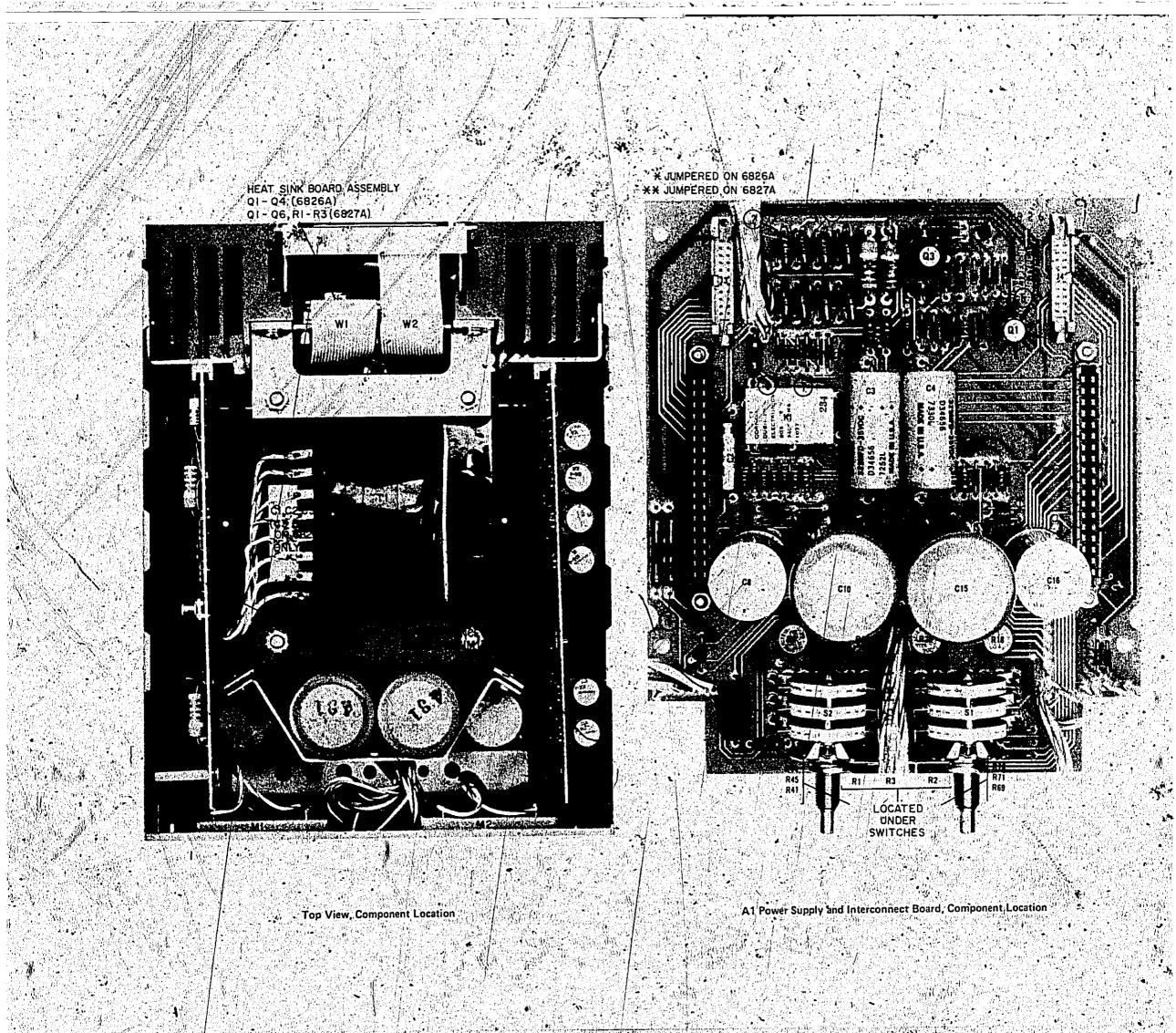


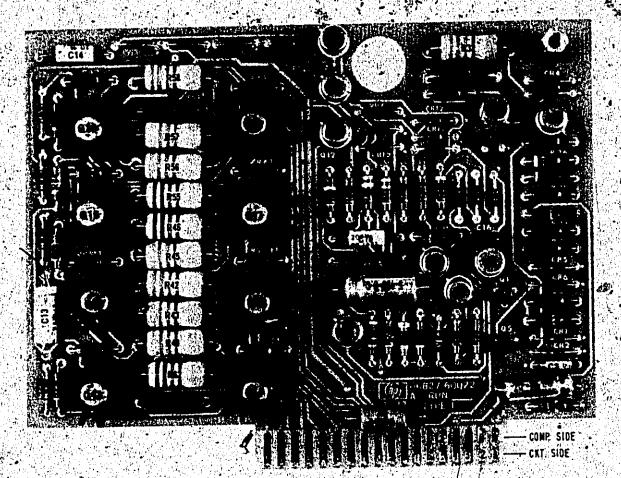
UMPERED ON 6826A

A2 Voltage and Current Control Board, Component Location



A CONT





A3 Amplifier Board (6827Aconly), Component Location

## NOTES L. ALL RESISTORS ARE IN OHMS, 1/2W, 5% UNLESS OTHERWISE INDICATED.

- 2 ALL 1/8W AND 1/4W RESISTORS ARE'1%, UNLESS OTHERWISE INDICATED
- 3. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE NOICATED.
- 4 DENOTES FRONT PANEL MARKING. • 5.
- 6.
- DENOTES CURRENT FEEDBACK 7 PIN LOCATIONS FOR TRANSISTORS ARE AS FOLLOWS

ŧΕ **oc** 3 •8 ζ το-3 n

## (ALL BOTTOM VIEWS)

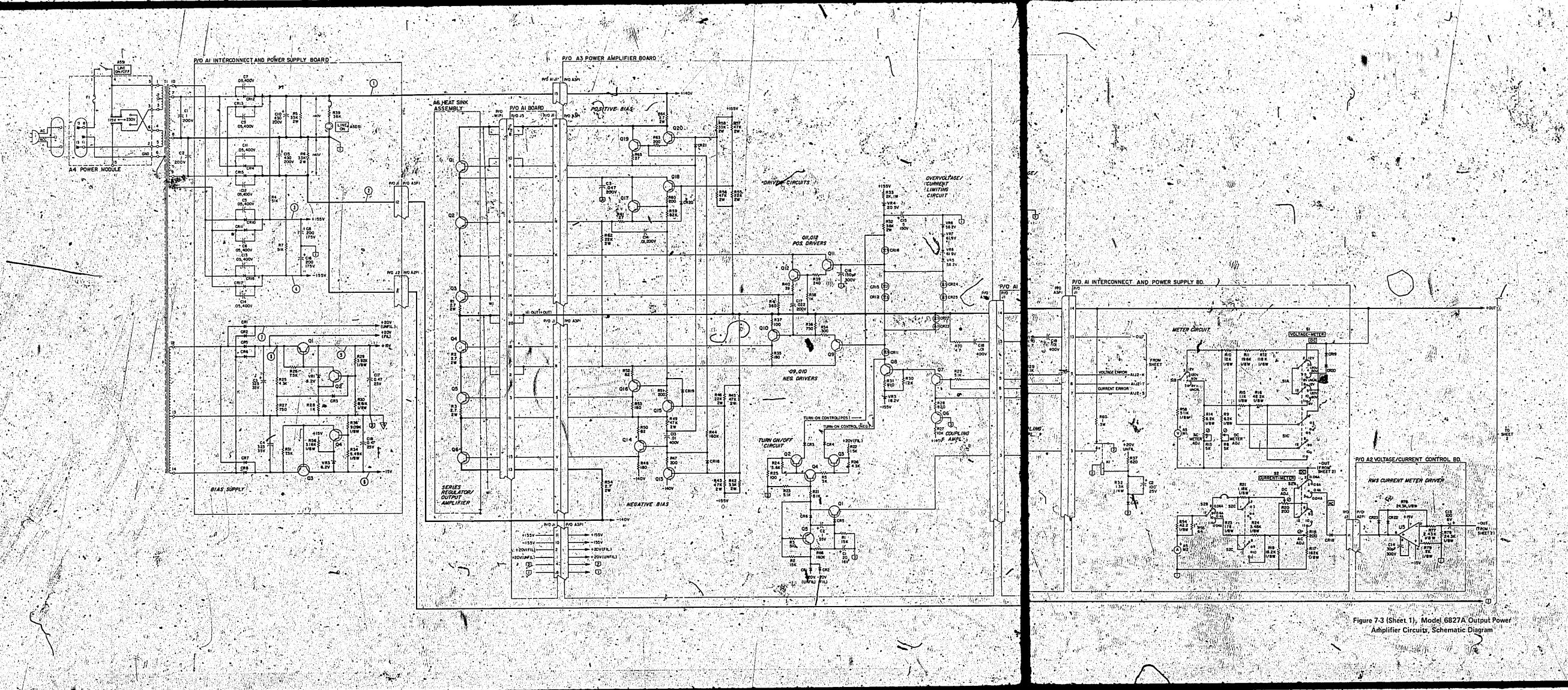
B. PIN LOCATIONS FOR INTEGRATED CIRCUITS A2UI-A2US ARE AS FOLLOWS

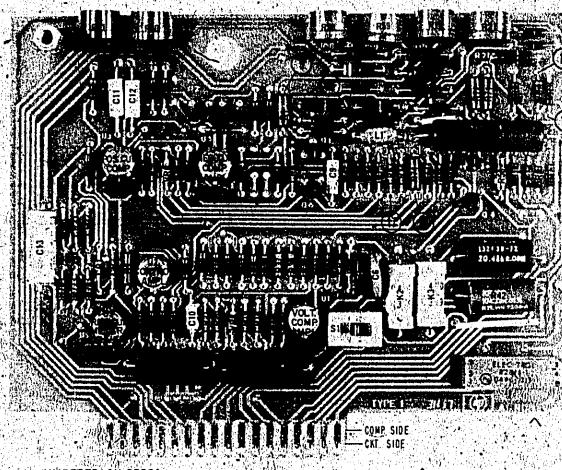
### (BOTADM VIEW)

9. THE LOCAL/AUTO SWITCH, LOCATED INSIDE THE UNIT ON BOARD A2. IS ACCESSIBLE BY REMOVING THE RIGHT SIDE COVER OR TOP COVER, FOR NORMAL OPERATION, THE SWITCH MUST BE LEFT IN THE LOCAL POSITION (PUSHED TO THE RIGHT OR TO REAR OF UNIT) THE AUTO POSITION (PUSHED TO LEFT OR FRONT OF UNIT) IS USED ONLY DURING AUTO-SERIES OR AUTO-PARALLEL OPERATION GF TWO OR MORE UNITS.

IO JUMPER AZWINS REMOVED FOR OFFN COLLECTOR OPERATION. NOTE THAT THIS CIRCUIT.

-i)



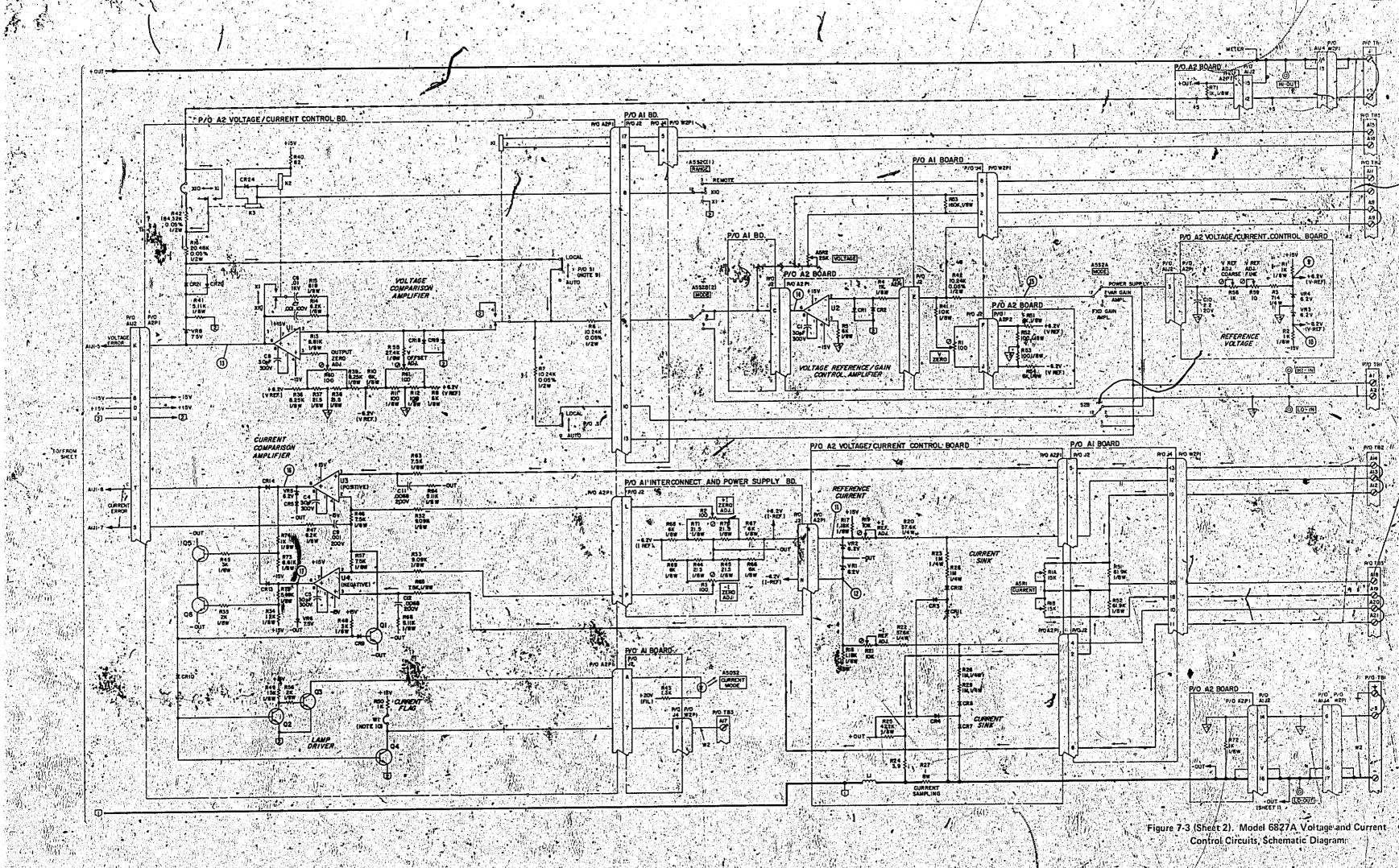


\* JUMPERED, ON 6826A

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A2 Voltage and Current Control Board, Component Location

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# OPERATING AND SERVICE, MANUAL

BIPOLAR POWER SUPPLY/AMPLIFIER

MODELS 6826A AND 6827A

HEWLETT D PACKARD

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# -HEWLETT D PACKARD

# CERTIFICATION

The Hewlett-Packard Company certifies that this instrument was thoroughly tested and inspected and found to meet its published specifications when it was shipped from the factory. The Hewlett Packard Company further certifies that its calibration measurements are traceable to the U.S. National Bureau of Standards to the extent allowed by the Bureau's calibration facility.

# WARRANTY AND ASSISTANCE

All Hewlett-Packard products are warfanted against defects in materials and workmanship. This warranty applies for one/year from date of delivery, or in the case of certain major components listed in the opernting manual, for the specified period. We will repair of replace products which prove to be defective during the warranty period provided they are returned to Hewlert-Packard. No other, warranty is expressed or implied. We are not liable for consequential damages.

For any assistance conject your nearest Hewlert-Packard Sales and Service Office. Addresses are provided at the back of this manual.



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# BIPOLAR POWER SUPPLY/AMPLIFIER MODELS 6826A AND 6827A

OPERATING AND SERVICE MANUAL FOR SERIALS 1317A-00101 AND ABOVE

> \*For Serials Above 1317A-00101 a change page may be included.

> > HEWLETT-PACKARD

HP Part No. 5950-1702

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Printed: January, 1974

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## SECTION I GENERAL INFORMATION

# DESCRIPTION

This instruction manual contains operating and service instructions for two Bipolar Power Supply/Amplifiers (Models Wi26A and 6827A). The Bipolar Power Supply/ Amplified BPS/A) is a general purpose instrument useful in any taboratory emaged in the research and development of (electronic systems, circuitry for components. The BPS/A) can be operated as a power supply or as an amplifier. Terminals on the rear terminal strip permit adverse to various in ternal control points to further expand the operational cap subjitties of the unit. The resulting flexibility lends the BPS/A to an almost unlimited number of applications. Some of ( these applications are outlined in Section 111 of this manual. The following paragraphs describe some of the features of the BPS/A as a power supply and as an amplifier.

## POWER SUPPLY FEATURES

1-3

The unit can be made to function as a regulated dc 1.4 power supply by setting the front panel MODE switch to the POWER SUPPLY position. The supply can furnish either a Constant Voltage output or Constant Current output. The dc output is bi-polar and is continuously adjustable from its maximum rated positive value to an equal negative continuously through zero. A crossover feature automatically changes the supply from constant voltage to constant current operation at a preset or programmed voltage/current. point. The front panel CURRENT MODE indicator lights for constant current operation. Both the supply and the load are protected against overvoltage and overcurrent conditions by internal circuits. Dual output voltage ranges are provided for better resolution. The front panel RANGE switch allows selection of the high (X10) or low (X1) output range.

1.5 The output voltage can be programmed locally using the front panel VOL TAGE control, or remotely, by means of a resistance connected to the appropriate rear terminals. The output current can be programmed locally using the front panel CURRENT control, or remotely, by means of a resistance or voltage source connected to the appropriate rear terminals. The BPS/A can be programmed (controlled) at a very high rate of speed (less than 500 sec for output voltage change over the entire voltage span). Board and remote programming connections are described in Section 111. The output voltage and current ranges are as follows: Model 6826A: -5V to +5V at 0 to 1.0A (low range) -50V to +50V at 0 to 1.0A (high range) Model 6827A: -10V to +10V at 0 to 0.5A (low range)  $v^{4}$ -100V to +100V at 0 to 0.5A (high range)

1-6 The BPS/A car sink, as well as source current, permitting it to serve as a variable load device. The BPS/A can sink up to 50% of the rated output current.

# 7 AMPLIFIER FEATURES

The unit can be made to function as a variable gain of a fixed gain amplifier by setting the MODE switch to the VAR GAIN AMP or FXD GAIN AMP position. When oper, ating as an amplifier, the BPS/A can amplify externally / applieduc or dc signals. Variable gain can be controlled a locally (VOLTAGE control) or remotely and is accurate to bits. The variable or fixed gain provided is as follows: Model 6826A: Variable Gainy, 0-2 (low range), 0-20

1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	N 1 1 N	a high range)
N. C.	Fixed Gin - v	(low range) 10%
1.1.0		(hujh range)
del 6827A:	Variable Gain -	0-4 (low range), 0-40
1 <b>7</b> -11-14		(high range)
( +	Fixed Gain -,	2X (low range), 20X
		(high range)

1-9 The variable gain amplifier is non-inverting and has a frequency response from dc to 15kHz. The fixed gain amplifier is inverting and has a frequency response from dc to 40kHz (6826A) or from dc to 30kHz (6827A). Total harmonic distortion is 0.1% (maximum).

# 1-10 METERS

Mo

1.11 A voltmeter and an ammeter on the front panel monitor the ac of dc output voltage and current respective-fr ly. Associated front panel VOLTAGE METER and CUR-RENT METER switches allow the meters to monitor either an ac or dc output and also provide dual range monitoring capability for better resolution. The dc meter accuracy is ±3% of full scale and the ac meter accuracy is ±5% of full scale.

## 1-12 SPECIFICATIONS

1-13 Detailed specifications for the two models are

Táble 1-1. Specifications, Models 6826A and 6827A

Ĉ.,

Specifications apply to all mo	dels unless otherwise specified.
GENERAL SPECIFICATIONS	DC Output (Continued);
A PARTICIPACITY AND A PART	Model 6827A:
hput Power:	X1 Range: -10V to +10V, 0 to 0.5A
Model. 68266 . 104/129/208-254 Vac (Switchable).	X10 Range: -100V to +100V, 0 to 0.5A
48-63Hz, 1.0A, 130W	
Model 682724 104-127/208-254 Vaci(switchable),	Load Effect (Load Regulation):
48-63Hz, 1.2A, 150W	Voltage load effect is given for a load current change
	equal to the current rating of the supply. Current load
Meters:	effect is given for a load voltage charles equal to the
Individual voltage and current meters. DC accuracy is	voltage rating of the supply
3% of full scale. AC accuracy is 5% full scale with sinu-	, Model '6826A:
solulal, 100Hz input.	Voltege (X1 Range): 0.01% + .5mV
	Voltage (X10 Range): 0.01% + 1mV
Meter Ranges (DC):	Current: .01% + 250µA
Mod 1 5826A: 1.6V ±60V	
10.12A, ±1.2A	
Model 6827A: ±12V, ±120V '	Load Effect (Load Regulation) Continued:
±0:06A, ±0.6A	Model 6827A:
Mana Barlillian	Voltage (X1 Range): .01% + .3mV
Meter Ranges (AC):	Voltage (X10 Range): .01% + 1mV
Model 6826A. 4V (uncal), 40V rms	Current: .01% + 250µA
Model 68070	
Model 6827A: 8V (uncal), 80V rms	Source Effect / ins Devided 1
The object of the second secon	Source Effect (Line Regulation):
Temperature Ratings:	For a change in line voltage between 104 and 127Vac/ 208 and 254Vac at any output voltage and current
Operating: 0, to 55°C.	within rating. (
Storage: -40, to +75°C	Model 6826A;
$M_{\rm eff} = \frac{1}{2} \sum_{i=1}^{n} \frac{1}{N_{\rm eff}} = \frac{1}{N_{\rm eff}} \sum_{i=1}^{n} \frac{1}$	Voltage (X1 Range): .01% + .5mV
Cooling:	Voltage (X10 Range): .01% + 5mV
Convection cooling is employed. The supplies have no	(Current: .01% + 250µA
moving parts.	Model 6827A:
	Voltage (X1 Range): .01% + 1mV
Dimensions:	Voltage (X10 Range):01% + 10mV
See outline diagram, Figure 2-1.	Current: .01% + 250µA
Weight:	
18 lbs. (8.2 kg.) net, 21 lbs. (9.5 kg.) shipping,	PARD (Ripple and Noise):
	Rms/p-p (20Hz to 20MHz) at any line voltage and
	under any load condition within rating.
POWER SUPPLY SPECIFICATIONS	<u>Model 6826A:</u>
	Voltage IX1, Range): 2mV rms/10mV p.p
DC Output:	Voltage (X10 Range): 6mV rms/35mV p-p
Voltage and current spans indicate range over which	Current: .8mA rins/5mA p.p. Model 6827A:
output may be varied. Model 6826A:	
×1 Range: −5V to +5V.0 to 1.0A	Voltage (X1' Range): 2.5mV rms/15mV pp Voltage (X10 Range): 10mV rms/50mV p-p
X10,Range: -50V to +50V, 0 to 1.0A	Current: .4mA rms/5mA p-p
	and the second se Second second s

*POWER SUPPLY SPECIFICATIONS (Continued)	Model 6826A:
	Voltage (X1 Range): 10mV
And Alexander	Voltage (X10 Range): 100mV
emperature Coefficient	Current: 3mA
Quiput change per degree Centigrade change in am-	Model 6827A:
bielst following 30 minutes warm-up.	Voltáge (X1 Range): 20mV
Mg/(v)/6826A	Voltage (X10 Range): 200mV
//W/tage (X1 Range): .01%+ .35mV	Current: 1.5mA
Voltage (X10 Range): .01% + 3mV	
Current: .02% + 50µA	Output Impedance (Typical to 50kHz);
Mødel 6827A.	Approximated by a resistance in series with an induc-
Voltage (X1 Range): .01% + .7mV	tance (constant voltage operation).
Voltage (X10 Range): .01% + 6mV	Mpdel 6826A: 1mΩ & 1.5pH
Current: 02% + 80µA	Model 6827A: \ 2mΩ & 4μH++++
	DC Output Isolation:
with (Conhillion).	Supply may be floated at up to 300V above ground.
rift (Stability): Change in output (dc to 20Hz) over 8 hour interval	Ahhhi way no undred at all to ano a more Bradian
under constant line, load, and ambient following 30	Remote Resistance Programming:
	Model 6826A (Resistance Coefficient):
minutes warm-up	Note: 062074 (Resistance Coefficient). Voltage (X1 Range): $2000\Omega/V \pm .1\%$
Model 6826A:	Voltage (X10 Range): $200\Omega/V \pm .1\%$
Voltage (X1 Range): .03% + 1mV (Pot wiper jump.	
effect may add 5mV)	Current: $10\Omega/mA^{\pm}$ .1%
Voltage (X10 Range): :03%,+10mV (Pot wiper	Model 6827A (Resistance Coefficient):
[umo effect may add 50mV)	Voltage (X1 Range): 1000Ω/V.±.1%
Current: 1% + 200µA (Pot wiper jump effect may	Voltage (X10 Range): $100\Omega/V \pm .1\%$
add 1.5mA)	Current: $10\Omega/mA \pm .1\%$
<u>Model 6827A</u>	
Voltage (X1 Range);;; 03% + 2mV (Pot wiper jump	Remote Programming Speed:
effect may add 5mV)	50µsec are required to change between 1% and 99% of
Voltage (X10 Range): 00% #20mV (Pot wiper	the maximum + and - voltage limits.
viump effect may add 100mV)	
Current: .1% + 2000A (Rotwiper jump effect may	Remote Programming Temperature Coefficient:
aud ImA)	Output change per degree Centigfade change in am-
	bient using an external control resistor (RF) at output
oad Effect Transient Recovery (Load Transient	voltage (VO) or current (IO). % T.C. RF is the tem-
Recovery	perature coefficient of the control resistance RF.
Time requiring for output voltage recovery to within	Model 6826A:
the specified level of the nominal output voltage	Voltage (X1 Range): .25mV + .007% (VO) +
following a change in output current equal to the	% T.C. R <sub>F</sub> (VO+5)
current rating al the supply.	Voltage (X10 Range): 2.2mV + .007% (Vo) +
Model 6826A:	% T.C. R <sub>F</sub> (Vo + 50)
100µsec is required for output voltage recovery	Current: .016% (IO) + 33µA + % T.C. RF (IO)
within 50mV pr nominal output voltage.	[1] 21년 2월
Model 6827A: 100µsec is required for output voltage recovery	Model 6827A:
	Voltage (X1 Range): .5mV + .007% (Vo) +
within 100mV of nominal output voltage.	% T.C. RF (VO + 10)
	1. Voltage (X10 Range): . 4mV, + .007% (Vo).+
Resolution: Typical output voltage or current change that can be	% T.C. R <sub>F</sub> '(VO + 100)
CONTRACT OF TOTAL AND OF COMPANY COMPANY THE COMPANY OF THE	Current: :016% (IO) /+ 33µA + % T.C. RF (IO)

13

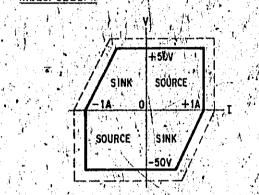
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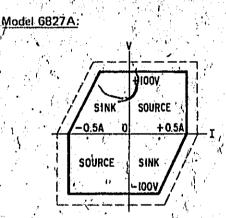
POWER SUPPLY SPECIFICATIONS (Continued)

Sink Current Compliance:

Maximum current that the supply can sink when connected to an active load. Model 6826A:



Sink current is limited to a value ranging linearly from 1A @ 0V to .5A @ 50V, Externally applied voltages to output terminals in excess of 60V could damage the instrument.



Sink current is limited to a value ranging linearly from .5A @ 0V to .25A @ 100V. Externally applied voltages to output terminals in . excess of 125V could damage the instrument.

POWER AMPLIFIER SPECIFICATIONS

Output: Model 6826A:

- Voltage (X1 Range): 10V p·p Voltage (X10 Range): 100V p·p
  - Current: 1A peak
- Model 6827A:
  - Voltage (X1 Range): 20V p.p.
  - Voltage (X10 Range): 200V p-p Current: 5A peak

Voltage Gain (High/Low Range): Model 6826A: Fixed Amplifier (Inverting): 10X (high range)/ 1X(low range) Variable Gain (Non-Inverting): 0-20 (high range)/ 0-2 (low range) Model 6827A: Pixed Amplifier (Inverting): 20X (high range)/ 2X (low range) Variable Gain (Non-Inverting): 0-40 (high range)/ 0.4 (low range) Frequency Response (+1, -3dB at full output): Modil 6826A:- 1 2 Fixed Gain: dc - 40kHz Variable Gain: dc - 15kHz Model 6827A: Fixed Gains dc- 30kHz Variable Gain: dc - 15kHz Distortion: Total harmonic distortion is . 1% (maximum) at 100 Hz and full output.

Input Impedance: 10K\$2 (Typical).

Fixed Gain Accuracy (at 100Hz): <u>Model 6B26A</u>: Low Range (X1): .1% + .5mV High Range (X10): .1% + 5mV

Model 6827A: Low Range (X1): .1% + 1mV High Range (X10): .1% + 10mV

Remote Resistance Programming Variable Gain  $(A_V)$ :  $A_V = \frac{KR_F}{10.24 \times 10^3 \Omega}$ , where K is the constant indicated and R<sub>F</sub> is the external control resistance.

Model 6826A:

- Av at high range {X 10} : <u>10R</u>+ 10.24 × 10<sup>3</sup>

Model 6827A:

Av at low range (X1):  $\frac{2R_{F}}{10,24 \times 10^{3}}$ Av at high range (X10):  $\frac{20R_{F}}{10.24 \times 10^{3}}$ 

## Variable Accuracy:

Accuracy in high range at 100Hz using an external control resistance (RF) at output voltage (VO). % RF is the accuracy of the control resistance RF. <u>Model 6826A:</u> (.05% + %RF) VO + 5mV . <u>Model 6827A:</u> (.05% + %RF) VO + 10mV

Remote Voltage Control Coefficient: Fixed gain amplifier mode, voltage coefficient:

# 1-14 OPTIONS

1-15 Options are customer-requested factory modifications of a standard instrument. The option described below applies to Models 6826A and 6827A. Option No. Description

Option No. 007

Ten-turn Output Voltage Control. Replaces standard single-turn voltage control to allow greater resolution in setting the output voltage of supply.

## 1-16 ACCESSORIES

1.17 The accessories listed in the following chart may ordered with the instrument or separately from your local Hewlett-Rackard sales office (refer to list at rear of manual for addresses).

HP Part No. Description

5060-8762 Dual Rack Adapter: Kit for rack mounting: one or two supplies in standard 19-inch rack.

uhits in standard 19-inch rack.

5060-8760

11057A 🕓

Blank Panel: Filler panel to block unused half of rack when mounting only one supply.

Carrying handle easily attached for portability and handling convenience.

Combining Case for mounting one or two

Cooling kit for, above combining case, 115

1052A

5060-0789

5060-0796 Cooling kit for above combining case, 230

Model 6826A:

Voltage (X1 Range): 1 volt/volt ± ,1% Voltage (X10 Range): 10 volts/volt ± .1% Model 6827A;

Voltage (X1 Range): 2 volts/volt ± .1%

Voltage (X10 Range): 20 volts/volt ± .1% Constant Gurrent, voltage coefficient (the following applies to variable gain amplifier, fixed gain amplifier, and power supply modes of operation): Models 6826A and 6827A: 1 ampere/volt ± .5%

# 1-18 INSTRUMENT IDENTIFICATION

1-19 Hewlett-Packard power supplies are identified by a three-part serial number. The first part is the power supply model number. The second part is the serial number prefix, consisting of a number-letter combination denoting the date of a significant design/change and the country of manufacture. The first two digits indicate the year (12 = 1972, 13 = 1973, 20 = 1980, etc); the second two digits indicate the week (01 through 52); and the letter "A", "G", "J", or "E" designates the U.S.A., West Germany, Japan; or the United Kingdom, respectively, as the country of manufacture. The third part is the power supply serial number; a different 5-digit sequential number is assigned to each power supply, starting with 00101.

1-20 If the serial number prefix on your unit does not agree with the prefix on the title page of this manual, change sheets supplied with the manual or manual backdating changes in Appendix A define the differences between your instrument and the instrument described by this manual.

# 1-21 ORDERING ADDITIONAL MANUALS

1-22 One manual is shipped with each instrument. Additional manuals may be purchased from your local Hewlett-Packard field office (see list at rear of this manual for addresses). Specify the model number, serial number prefix, and HR part number shown on the title page.

- MENINZ

Vac. 50-60Hz.

### 

### 2-1 INITIAL INSPECTION

2-2 Before shipment, this instrument was inspected and found to be free of mechanical and electrical defects. As soon as the instrument is received, proceed as instructed in the following paragraphs.

### 2-3 MECHANICAL CHECK

2.4 If external damage to the shipping carton is evident, ask the carrier's agent to be present when the instrument is unpacked. Check the instrument for external damage such as broken controls or connectors, and dents or scratches on the panel surfaces. If the instrument is damaged, file a claim with the carrier's agent and notify your local Hewlett-Packard Sales and Service Office as soon as possible (see list at rear of this manual for addresses).

### 2-5 ELECTRICAL CHECK

2-6 Check the electrical performance of the instrument, as soon as possible after receipt. Section V of this manual contains performance check procedures which will verify instrument operation within the specifications stated in Table 1-1. This check is also suitable, for incoming quality control inspection. Refer to the inside front cover of the manual for the Certification and Warranty statements.

### 2-7 REPACKAGING FOR SHIPMENT

2-8 To insure safe shipment of the instrument, it is recommended that the package designed for the instrument be used. The original packaging material is reusable. If it is not available, contact your local Hewlett-Packard field office to obtain the materials. This office will also furnish the address of the nearest service office to which the instrument can be shipped. Be sure to attach a tag to the instrument specifying the owner, model number, full serial number, and service required, or a brief description of the trouble.

### 2-9 INSTALLATION DATA

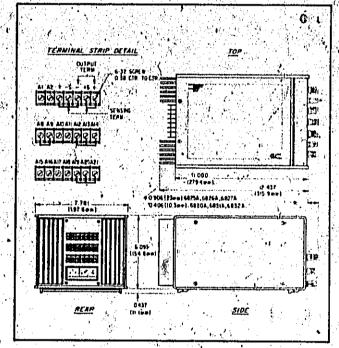
2.10. The instrument is shipped ready for bench operation. It is necessary only to connect the instrument to a  $\frac{1}{2}$  source of power and it is ready for operation.

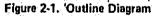
### 2-11 LOCATION

2-12 This instrument is convection copied. Sufficient space should be allotted so that a free flow of cooling air can reach the top and rear of the instrument when it is in operation. It should be used in an area where the ambient, temperature temains between 0°C and +55°C.

### 2-13 OUTLINE DIAGRAM

2-14 Figure 2-1 illustrates the outline shape and dimensions of Models 6826A and 6827A.





### 2-15 RACK MOUNTING

2-1

2-16 The Model 6826A and 6827A BPS/A's may be rack mounted using either the dual rack adapter kit of the combining case (with appropriate cooling kit) described in Paragraph 1-16. The necessary installation instructions are provided with the accessories Refer to Paragraph 5-91 before proceeding with the rack mounting installation instructions.

### 2-17 INPUT POWER REQUIREMENTS

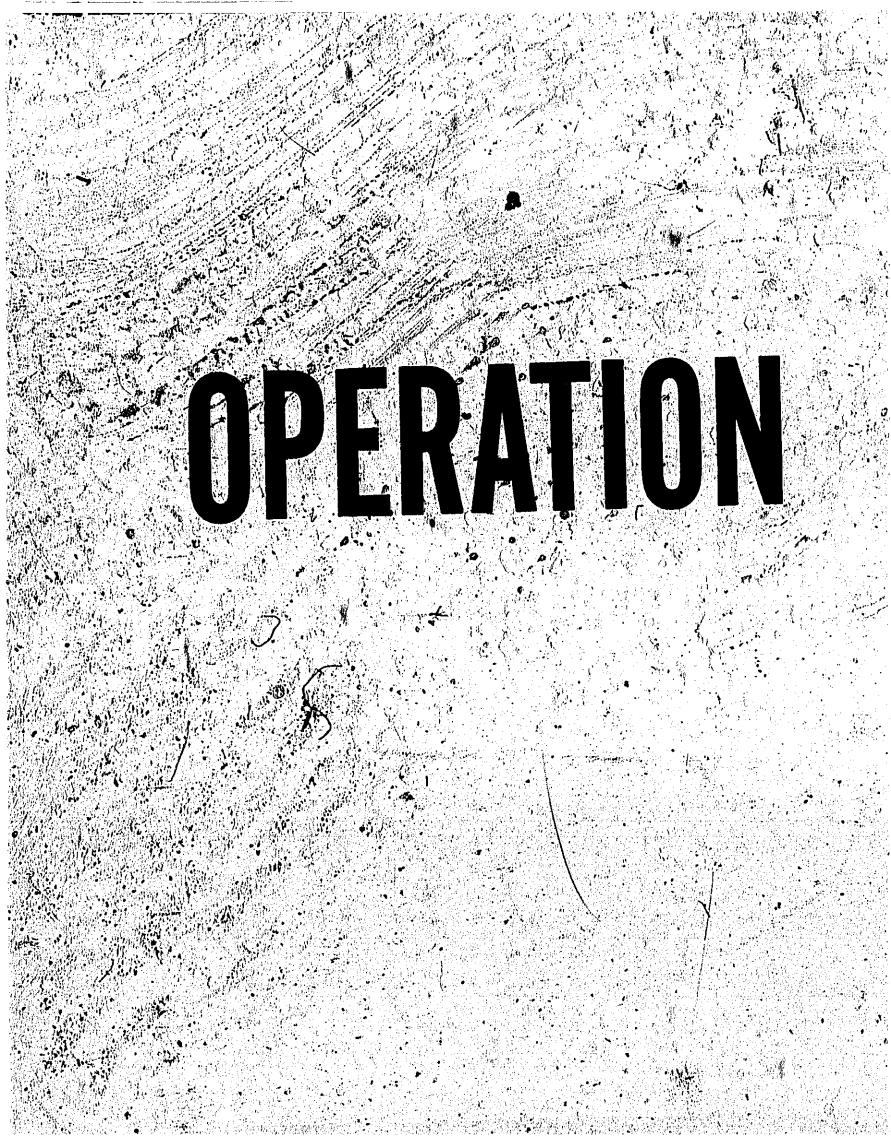
2.18 Models 6826A and 6827A may be operated continuously from either a nominal 120 volt or 240 volt, 48-63 Hz power source. A two-position selector switch (, ) hocated within the a-c power module on the rear panel selects the power source. Before connecting the instrument to the power source, check that the selector switch setting matches the hominal line voltage of the source. If required, move the switch to the other position. Note that the power cable must be removed, the plastic door on the power module inust be removed, the plastic door on the power module ward and the fuse must be removed in order to gain access to the selector switch.

2-19 When the instrument leaves the factory, the proper tuse is installed for 115 volt operation. An envelope containing a fuse for 230 volt operation is attached to the instrument. Make sure that the correct fuse is installed if the position of the slide switch is changed (2A for 115 volt op eration, and 1A for 230 volt operation).

### 2-20 POWER CABLE

2-21. To protect operating personnel, the National Electrical Manufacturers' Association (NEMA) recommends that the instrument panel and cabinet be grounded. This instrument is equipped with a three conductor power cable. The third conductor is the ground conductor and when the cable is plugged into an appropriate receptacle, the instrument is grounded. The offset pin on the power cable's three prong connector is the ground connection.

2-22 To preserve the protection feature when operating the instrument from a two contact outlet, use a three-prong to two-prong adapter and connect the green lead on the adapter to ground.



### SECTION III OPERATING INSTRUCTIONS

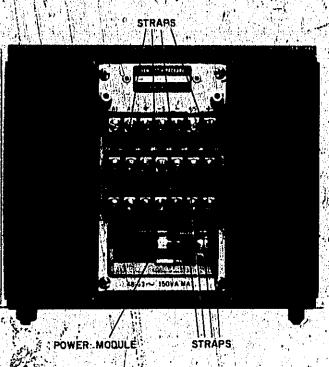


Figure 3-1: Bipolar Power Supply Amplifier, Models 6826A and 6827A, Rear View

## 3-1 INTRODUCTION

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3-2 This section describes the operating controls and indicators, the turn on checkout sequence, and operating modes of Bipolar Power Supply/Amplifier (BPS/A) models 6826A and 6827A. Local and remote programming operations are also described.

### 3-3 REAR TERMINALS AND AC INPUT

The Bipolar Power Supply/Amplifier (BPS/A) is shipped with the rear terminals strapped for local programming (using front panel controls) as shown in Figure 3-1. Remote programming strapping requirements are described in subsequent paragraphs. The power module contains fuse F1/(2A for 115Vac or 1A for 230Vac) and a slide switch for connecting 1.15Vac or 230Vac input power to the instrument. To turn on the BPS/A, set the LINE switch (item 1), Figure 3-2) to ON. The LINE ON indicator . (2) should light. 'Fuse F1 protects the main power supply. At initial turn on, an internal circuit protects any loads connected to the BPS/A from turn on transients by shorping the output terminals and disabling the BPS/A's power output circuits. This circuit operates similarly at turn off to protect any loads from turn-off transients.

## 3.5 OPERATING CONTROLS AND

### 3-6 MODE SWITCH

3.7 The MODE switch ③ allows the BPS/A to operate as a power supply, variable gain amplifier, or a fixed gain amplifier. In the power supply operation, the BPS/A provides a variable bipolar dc output voltage dependent upon the RANGE switch ④ and VOLTAGE control ⑤ settings. The dc output voltage ranges are as follows:

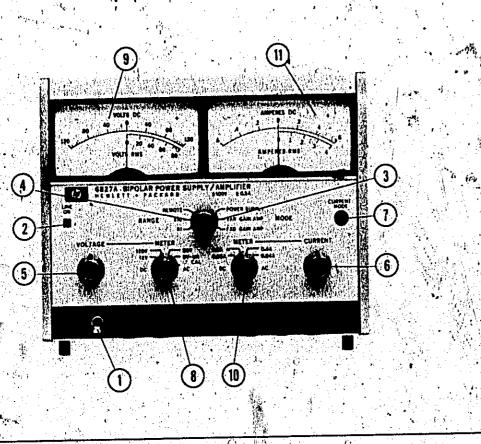


Figure 3-2. Operating Controls and Indicators

MODEL	DC OUTPUT V	VOLTAGE
MODEL	HIGH RANGE	LOW RANGE
6826A 6827A 1	-50V to +50V -100V to +100V	-5V to +5V -10V to +10V

In variable gain amplifier operation, the BPS/A can 3.8 .... amplify or attenuate an external input signal (dc to 15kHz) applied to the HL and LO IN terminals. The gain is variable from 0 to a maximum depending upon the RANGE switch (4) and VOLTAGE control (5) settings. The variable gain ranges are as follows:

	VARIABLE VOL	TAGE GAIN
MODEL	HIGH RANGE	LOW RANGE
6826A 6827A	0-20 0-40	-0-2 0-4

In fixed gain amplifier operation, the BPS/A inverts 3 3.9

and amplifies an external input signal applied to the HI and LOIN terminals. For fixed gain amplifier operation, the 6826A has a fraquency response from DC to 40kHz and the 6827A has a frequency response from DC to 30kHz. The fixed voltage gain provided in the high or low output range is as follows:

			11.
	FIXED VOLT	AGE	IN
MODEL	HIGH RANGE	LOW	RANGE
6826A 6827A	X10 X20		X1 X2

RANGE SWITCH 3 10

3.11. The RANGE switch (4) allows selection of the high (X10) or low (X1) output ranges for power supply, variable gain amplifier, or fixed gain amplifier operation. The REMOTE position, allows the high or low range to be externally selected via the rear terminal strip (see Paragraph 3.45).

3-12 VOLTAGE CONTROL

3-13 The VOLTAGE control (5) controls the output level (power supply operation) or gain (variable gain amplin fier operation) of the BPS/A. In power supply operation, ... the VOLTAGE control varies the output voltage from a maximum negative value (full counterclockwise) through zero (midposition) to a maximum positive value (full clockwise). In variable gain amplifier operation, the gain is variable able from zero to the maximum gain as the VOLTAGE control is varied from full counterclockwise to full clockwise. In fixed gain amplifier operation, the VOLTAGE control does not control circuit operation.

#### CURRENT CONTROL 3.14

3-15 The CURRENT control (6) sets the constant current output of the BPS/A. This control is operable in all three modes of operation (power supply, variable gain ampli-fier, and fixed gain, inplitude, and controls the output cur-rent from 0 to the maximum rated output (1.0A for the 6826A and 0.5A for the 6827A). When the instrument switches from constant voltage to constant current operation, the CURRENT MODE Indicator (7) lights. Selection of constant voltage or constant current operation is described in Paragraphs 3-27 and 3-28.

### 3-16 VOLTAGE METERING

3-17 The VOLTAGE METER switch (8) permits monitoring the DC or AC output voltage on voltmeter (9) The shaded area on the voltmeter face indicates the amount of output voltage that is available in excess of the normal rated output. The voltmeter upper scale reads the bipolar DC voltage from a maximum negative value through OV to a maximum positive value, DC accuracy is ±3% of full scale. The lower scale reads the RMS output voltage from 0 to a maximum level. AC accuracy is ±5% of full scale. The voltmeter ranges selected by the VPLTAGE METER switch are as follows:

	MODEL	VOL	METER RANGES
	MODEL	DC	AC (RMS)
	6826A	사람이 지수는 것 같아요. 이 가지 않는 것 같아요.	60V 0-4V (uncal), 0-40V 120V 0-8V (uncal), 0-80V
-			·特别的人的基本的。

3-18 CURRENT METERING

3-13 The CURRENT METER switch (10) permits monitoring the DC or AC output current on ammeter (1) The sheded area on the ammeter face indicates the amount

of output current that is available in excess of the normal rated output. The immeter upper scale reads the bipolar DC current from a maximum negative value through OA to a maximum positive value. DC accuracy is \$3% of full scale. The lower scale reads the RMS output current from 0 to a maximum level. AC accuracy is 15% pf full scale. The ammeter ranges selected by the CURRENT METER switch are as follows:  $\mathcal{A} = \{1, \dots, n\}$ 

MODEL	AMMETE	RRANGES
- Contraction	DC.	AC (RMS)
6826A 6827A	0 to ±0.12A,0 to ±1.2A 04p ±0.06A,0 to ±0.6A	

TURN-ON CHECKOUT PROCEDURES 3-20

### CAUTION

Rear terminal strip cover must be in place when instrument is in use.

The following turn-on and checkout procedures 3.21 are performed utilizing the front panel controls (Figure 3/2) and the normal rear terminal strapping connections as received from the factory. Also, the Local/Auto switch, located inside the instrument on board A2, is in the Local position (pushed to the right or toward the rear of the instrument) as received from the factory. The AUTO position is used for auto-series and auto-parallel operations,[see \* Paragraphs 3-57 through 3-61). The following procedures: check both power supply and amplifier to ensure that the BPS/A is operational.

POWER SUPPLY CHECKOUT PROCEDURE

Set front panel controls as follows:

MODE switch (3) - POWER SUPPLY

RANGE switch (4) - X1

VOLTAGE control (5) - midposition

CURRENT control (6) - full clockwise

VOLTAGE METER switch (8) - low range DC CURRENT METER switch (10) - high range DC

Set LINE switch (1) to ON and observe that LINE ON indicator (2) lights.

Adjust VOLTAGE control (5) from full counter. C. . clockwise (-) to full clockwise (+) range through OV and note that maximum/output is attained as indicated on meter 9

Set VOLTAGE METER switch (8) to high range d DC and RANGE switch (4) to X10 position. e. Adjust VOLTAGE control (5) clockwise and

counterclockwise through entire bipolar output voltage.

range through 0 and note that maximum output is attained as indicated on meter (9) Adjust output voltage to +50V.

1. To checkout the vonstant current circuit, first turn of BPS/A. Short circuit the front panel terminals (HI OUT to LOCOUT).

h Turn on supply and observe that GURRENT MODE indicator () lights and meter () indicates 0 volts. h Adjust CURRENT control () from full cw to full can and note that minimum corrent is attained as indicated on meter (1)

Furn off supply and remove short from output terminals.

J Turn on supply and adjust VOLTAGE CONTROL for an output of -50V.

k. Turn off supply and reconnect short across the HI and LO OUT terminals.

'r. Turn on supply and note that CURBENT MODE indicator' (7), lights and meter (9) Indicates 0 volts.

m. Adjust CURRENT control (6) from full cw to full ccw and note that minimum current is attained as indicated on meter (11)

n. Turn off BPS/A and remove short from output term inals.

VARIABLE GAIN AMPLIFIER CHECKOUT PROCEDURE o. Set front panel controls as follows:

MODE switch (3) – VAR GAIN AMP RANGE switch (4) – X1 VOLTAGE control (5) – midposition CURRENT control (6) – full clockwise VOLTAGE METER switch (8) – low range AC

CURRENT METER witch (10) - high range AC Connect a 1:75V rms, 100Hz input signal to the

front panel input terminals (HI and LO IN). q. Turn on supply and adjust VOLTAGE control (5) through entire RMS range and note that maximum voltage is attained as indicated on meter (9).

r, Set VOLTAGE METER switch (B) to high range AC, RANGE switch (a) to X10, and adjust VOLTAGE control; (b) through entire RMS range and note that maximum voltage is attained as indicated on meter (g) FIXED GAIN AMPLIFIER CHECKOUT PROCEDURES

s. Set MODE switch ③ to FXD GAIN AMP position and increase input signal to 3.5V rms. 't: VAdjust VOLTAGE control ⑤ through entire.'

RMS range and note that maximum voltage is attained as indicated on meter

### 3-22 OPERATING MODES

p,

### --- CAUTION----

Rear terminal strip cover must be in place when instrument is in use.

The position of the front panel MODE switch de-3.23 termines whether the instrument will be used as a power supply or an amplifier. In addition, the instrument may be controlled locally using the front panel VOLTAGE, and CURRENT controls or remotely via terminals on the rear of the unit. The front panel output terminals, (HI and LOW) OUT) and input terminals (H) and LO IN) are repeated as (+ and -) and (A1 and (A2) respectively on the rear terminal strip. The rear terminal strip includes sensing (+Siand -S) terminals and other terminals for remote control of the BPS/A as shown in Figure \$3. These terminals connect to various control points within the instrument and allow strapping connections to be made which enable the power supply or amplifier to be utilized in many applications. The following paragraphs describe the procedures for utilizing the various operational capabilities of the power supply. A more theoretical description concerning the operational features of this supply is contained in Application Note 90 and in various Tech. Letters. Copies of these can be obtained from your local Hewlett-Packard field office.

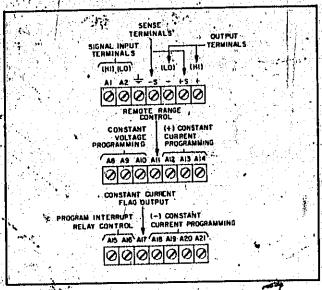


Figure 3-3. Rear Terminal Strip

### 3-24 LOCAL PROGRAMMING

3.25 The BPS/A is shipped with its rear terminal strapping connections arranged for constant voltage/constant current, local programming, local sensing, single unit mode of operation. (This strapping pattern is illustrated in Figure 3.4, Also, the Local/Auto switch on board A2 (see Paragraph 3.54) is in the Local position when the instrument is shipped from the factory. This switch <u>must</u> be in the Local position for single unit mode of operation.

3-26 The operator selects either power supply, variable gain amplifier, or fixed gain amplifier operation (MODE

3.4

switch) and also selects either constant voltage or a constant current output using the front panel VOLTAGE and CUR-RENT controls (for local programming, no strapping changes are required). Constant voltage or constant current operation are selected as described in the following paragraphs, c

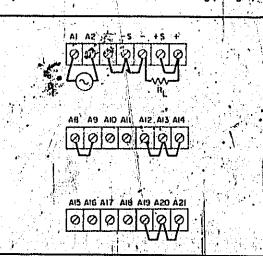


Figure 3-4. Normal Strapping Pattern (LOCAL Programming)

3-27 Constant Voltage. To select a constant voltage output, proceed as follows:

a. Remove load from output terminals turn on supply, and adjust VOLTAGE control for desired output voltage.

b. - Short output terminals and adjust CURRENT control for maximum output current allowable (current limit) as determined by load conditions and voltage range selected in step (a). If a load change takes place and causes the output current to exceed this setting, the power supply will automatically crossover to constant current mode and output current will be constant at the level set by the CURRENT control. The CURRENT MODE indicator will come on and output voltage will drop proportionately to maintain constant current.

3-28 Constant Current. To select a constant current output, proceed as follows:

a. Short output terminals and adjust CURRENT control for desired output current.

b. Open output terminals and adjust VOLTAGE control for maximum output voltage allowable as determined by load conditions and current selected in step (a). If a load change causes the voltage to rise, the power supply will automatically crossover to constant voltage at the voltage 'setting and output current will drop proportionately.

3-29 OPERATION OF SUPPLY BEYOND RATED

3-30 The shaded area on the front panel meters indicate.

the amount of output voltage and current that is available in excess of normal rated output. Although, the BPS/A can be operated in this region without damage, it cannot bu guardinged to meet all of its performance specifications.

### 335 REACTIVE LOAD CONSIDERATIONS

3.32 The life and performance of the instrument cabile be preserved if the following simple precaution is observed if when driving reactive loads. Always of program the VOLT AGE control for zero output before removing a capacitive load or interrupting an inductive load.

### 3-33 CONNECTING LOAD

3-34 Each load should be connected to the power supply output terminals (front or rear) using separate pairs of connecting wires. This will minimize mutual coupling etthets between loads and will retain full advantage of the low putput impedance of the power supply. Each pair of connecting wires should be as short as possible and twisted or shielded to reduce noise pickup. (If a shielded pair is used, connect the shield to ground at the power supply, and leave the other end unconnected.)

3 35. If load considerations require that the output power distribution ferminals be remotely located from the power supply, then the power supply output terminals should be connected to the remote distribution terminals via a pair of twisted or shielded wires and each load should be separately connected to the remote distribution terminals. For this case, remote sensing should be used. (Refer to Paragraph 3-39).

3-36. Always use two leads to connect the load to the supply, regardless of where the setup is grounded: This will eliminate any possibility of output current return paths through the power source ground. The supply can also be operated up to 300V deabove ground if heither output terminal is grounded.

### 3-37 REMOTE SENSING

3.38 Remote sensing is used to maintain good regulation at the load and reduce the degradation of regulation which would occurridue to the voltage drop in the leads between the power supply and the load. Remote sensing is account plished by utilizing the strapping pattern shown in Figure 3.5. The power supply should be turned off betony changing strapping batterns. The leads from the sensing (15), 1 terminals to the load will carry much less current than the load leads and it is not required that these leads be a heavy as the load light. Towever, they must be twisted or shield the to minimize noise backup.

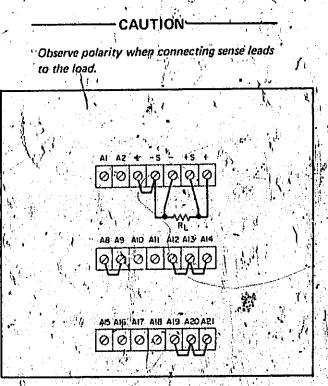


Figure 3-5. Remote Load Sensing

3-39 For reasonable load lead lengths, remote sensing, limits, degradation of the performance of the supply. However, if the load is located a considerable distance from the supply, added precautions must be observed to obtain satisfactory operation. Notice that the voltage drop in the load leads subtracts directly, from the available output voltage.
Because of this, it is recommended that the drop in each load lead not exteed 1.0 volt. If a larger drop must be tolerated, please consult an HP Sales Engineer.

NOTE

Due to the voltage drop in the load leads, it may be necessary to readjust the constant current crossover limit setting in the remote sensing mode.

3-40 REMOTE PROGRAMMING

- CAUTION-

External programming resistors, must be connecyeed to the appropriate rear terminals before power is applied to the instrument.

3.4 here constant voltage and constant current outputs if - ing to the value of the BPS/A canibe programmed (controlled) from a re- wather voltage output motely located device such as HP 6940A Multiprogrammer and are as follows:

3.6

or HP 6941A Multiprogrammer Extenders. Either a resistance or voltage source can be used as the programming device. The wires connecting the programming terminals on the rear of the BPS/Aito the remote programming device should be twisted or shielded to reduce noise pickup.

3.42 Resistance Programming Constant Voltage. A programming resistor (RpV), connected as shown in Figure 3.5, can be used to control the voltage output or gain provided that the MODE switch is in the POWER SUPPLY or the VARIABLE GAIN AMP position. Resistance programming of the constant voltage output is not applicable in the FXD GAIN AMP mode of operation. The VOLTAGE control on the front panel is disconnected (disabled) for the strapping connections shown in Figure 3.6. Tormaintain the stability and temperature coefficient of (the instrument, use programming resistors that have stable low noise and low temperature characteristics (less than 20 ppm/°C.). Also, they should operate at less than 1/30th of their wattage ratfing to minimize short term temperature i effects.

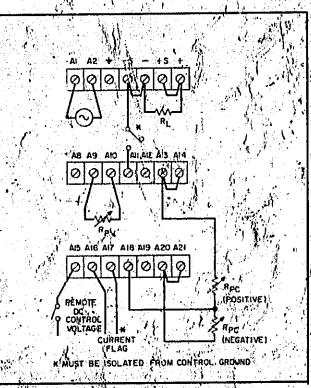


Figure 3-6. Remote Resistance Programming, Constant Voltage/Constant Current

3.43 Power Supply. For power supply operation, the / bipolar output voltage varies linearly from a maximum negative value through zero to a maximum positive value according to the value of the programming resistance Rpv: The voltage output ranges and corresponding values of Rpv are as follows:

ì	<u> </u>	1.1			•
	Bass	682	6A	682	7A
	RPV VALUE	HIGH	LOW . RANGE	HIGH	LOW RANGE
,	ָּס	-54,2V	-5.12V.	-102.4 V.	-10.24V
2	10,24KΩ	ov	ov	ov	ov`
	20.48KΩ	+51.2V	+5.12V	+102.4V	±10,24V

3.44 As noted above, the output voltage should be zero volts with 10:24K connected to the programming terminals. The output may be adjusted to zero by adjusting the V ZERO ADJ potentiometer as described in Paragraph 5-104. The output voltage varies from the maximum negative value to the maximum positive value through 0 at a rate determined by the resistance programming coefficient as follows:

Mobri	PROGRAMMIN	G COEFFICIENT
MODEL	HIGH RANGE	LOW RANGE
6826A	200 ohms/volt ± . 1%	2000 ohms/volt ± .1%
6827A	100 ohms/volt ± .1%	1000 ohms/volt ± .1%

When remote control programming is employed, the FLAG (A17) and REMOTE RANGE (A11) programming connections must be isolated from the computer ground.

3.45 The switch connected between the A11 and -S terminals allows remote selection of the high (X10) or low (X1) range. Note that the front panel RANGE switch must be in the REMOTE position in order to utilize the remote selection feature. The remote dc control voltage connections between terminals A15 and A16 activate an internal relay. When the control voltage is applied, the internal relay is energized momentarily disabling the input driver to the BPS/A error amplifier. This feature is used to prevent transients from affecting the output when the programming input is changed. Terminal A17 provides an indication to the external programming device when the BPS/A is in constant current operation.

3.46 Variable Gain Amplifier. For variable gain amplifier operation, an external input signal (dc to 15kHz), applied to terminals A1 (H1 IN) and A2 (LO IN), is amplified or attenuated. The gain is variable from 0 to a maximum value as the value of RPV varies from 0 to 20.48K ohms. The variable gain at the high and low ranges is as follows:

MODEL	VARIA	BLE GAIN
MODEL	HIGH RANGE	LOWRANGE
6826A	0-20 •	0.2
Ġ827A	545 <b>, 2 20:40</b>	.0.4

### - CAUTÍO

The voltage applied to the input terminals, HI IN (A1) and LO IN (A2), must not exceed 50V (maximum) or the instrument may be damaged.

3-47 Resistance Programming, Constant Current. Programming resistors (RPC), connected as shown in Figure 3-6, can be used to control the constant current output. The front panel CURRENT control is disconnected (disabled) when the remote RPC resistors are connected as indicated ( Resistance programming of the constant current output can be accomplished in all three modes of operation (power supply, variable gain amplifier, and fixed gain amplifier) Individual RPC resistors control positive and negative constant current outputs respectively. The positive or negative output current is variable at a rate determined by the programming coefficient as follows:

MODEL	OUTPUT CURRENT	PROGRAMMING COEFFICIENT
6826A	0 to 1.024A	10 ohms/mA ± 1%
6827A	0 to .512A	10 ohms/mA ± .1%

CAUTION

A load must be maintained at all times during constant current operation. The load can be a 100K  $\Omega$  resistor for the 6826A or a 400K  $\Omega$  'resistor for the 6827A.

3-48 Zero output current for zero programming resistance can be assured through proper adjustment of the front panel +I and -I ZERO ADU potentiometers (see Paragraph 5-107).

3-49 Voltage Programming, Constant Voltage. Voltage programming of the output voltage can be accomplished in the variable gain or fixed gain amplifiel mode of operation. Voltage programming is not applicable in the power supply mode.

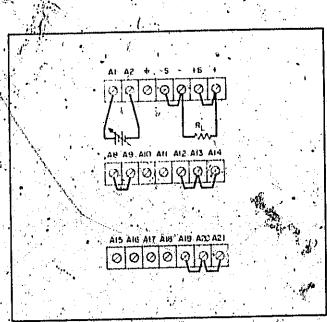


Figure 3.7. Remote Voltage Programming, Constant Voltage

Variable Gain Amplifier. AC signals or a dc level 3.50 (positive or negative) can be amplified or attenuated in the variable gain amplitier mode. Figure 3-7 shows a variable de level (programming voltage) applied to the input terminals A1 (HI IN) and A2 (LO IN). Since the BPS/A is noninverting in the variable gain amplifier mode, a positive input (A1 positive, A2 negative) results in a positive output and a negative input (A1 negative, A2 positive) results in ainegative output. The other connections on Figure 3-7 and shows? for local control using front panel controls, however, remote control using external controls may also be employed. The front panel or remote voltage controls can be used to attenuate or amplify, the input as required. With the front paniel VOLTAGE control or remote programming resistor set for maximum output, the programming coefficient is as follows:

'   c				
MODEL	HIGH RANGE		HIGH RANGE	LOW
6826A 6827A	±50V ±100V	±5V ±10V	20 volts/ volt, 40 volts/ volt	2 volts/ volt 4 volts/

With front panel VOLTAGE control or remote programming resistor set for maximum rated output.

3-51 Fixed Gain Amplifler. AC signals up to 40kHz (6826A) or 30kHz (6827A) or a dc level (positive or negative) can be applified in the fixed gain amplifier mode. Figure 3-7 shows a variable dc level (programming voltage) applied to the HI (A1) and LO (A2) input terminals. Since the BPS/A provides an inverted output in the fixed gain amplifier mode, a positive input (A1 positive, A2 negative) results in a negative output and a negative input (A1 negative, A2 positive) results in a positive output. The front panel or remote programming voltage controls are not applicable in this mode. The programming coefficient in the fixed gain amplifier mode is as follows:

		KIMUM TPUT		AMMING
MODEL	HIGH RANGE	LOW- RANGE	HIGH RANGE	LOW RANGE
6826A	±50V	1.15V.	10 volts/ volt	1 volt/ volt
6827A	±100V	±10V	20 volts/ volt	2 volts/ volt

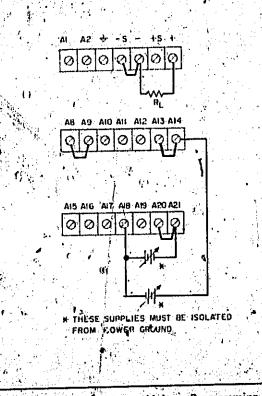


Figure 3-8. Remote Voltage Programming, Constant Current

3-52 Voltage Programming, Constant Current. Voltage programming of the output current can be accomplished in all three operating modes (power supply, variable gain amplifier, and fixed gain amplifier). Positive and negative dc

3.8

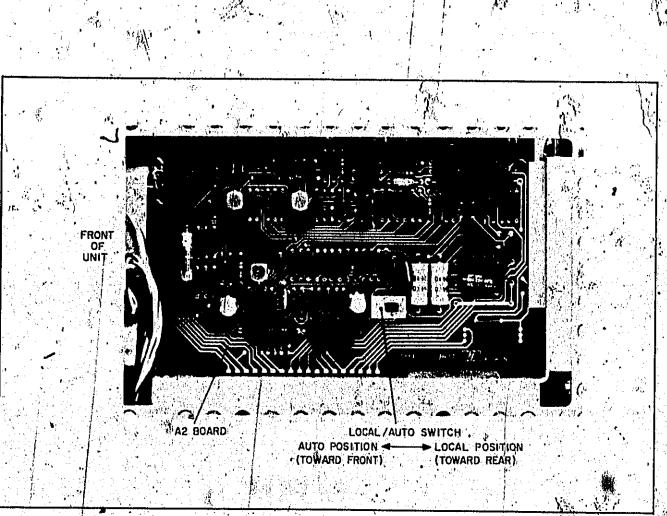


Figure 3-9. Local/Auto Switch

programming voltages are connected to terminals A14 and A21 Respectively as shown in Figure 3-8. The positive or negative output current will vary linearly with changes in! the programming voltages. The output current varies at a rate determined by the programming coefficient. For models 6826A and 6827A, the programming coefficient is 1 amp/ 1 volt. The maximum rated output current for the 6826A is 1A, therefore, the maximum programming voltage for 6826A is 1 volt. The maximum rated output current for the 6827A is 0.5A, therefore, the maximum programming voltage for 6827A is 0.5V:

### 3-53 SERIES AND PARALLEL CONNECTIONS

3-54 The following paragraphs describe the connections required for combining BPS/A's for series and parallel operations. These connections are employed whenever it is required to extend the voltage/gain or current capability beyond one supply. For series operation, the total output voltage/gain is the sum of the voltages/gains of the individual supplies. For parallel operation, the total output current is the sum of the output current from the individual supplies. For series or parallel operation, the BPS/A's must be operated in the same mode (power supply, variable gain amplifier, or fixed gain amplifier). Also, each supply must have its Auto/Local switch A2S1 (see Figure 3-9) in the Local position (pushed toward the rear of the instrument). Note that the external signal applied to the A1 and A2 terminals is internally disconnected when the BPS/A's are in the power supply mode.

3-55 Series Connections. Two or more supplies may be connected in series to obtain a higher voltage/gain than is available from a single supply. Figure 3-10 illustrates the series connections for three supplies. Each of the supplies must be adjusted in order to obtain the desired output/ voltage gain.

**3.56** Parallel Connections. Parallel operation of BPS/A is possible because of the constant voltage/constant current crossover feature. Two or more power supplies can be connected in parallel to obtain a total output current greater than that available from one power supply. The total output current is the sum of the output currents of the individual power supplies. The load must be selected so that the current limit of one supply is exceeded allowing it to operate in the constant current mode. The output CURRENT controls of each power supply can be separately set. The output voltage controls of one power supply should be set to the desired output voltage; the other power supply should be set for a

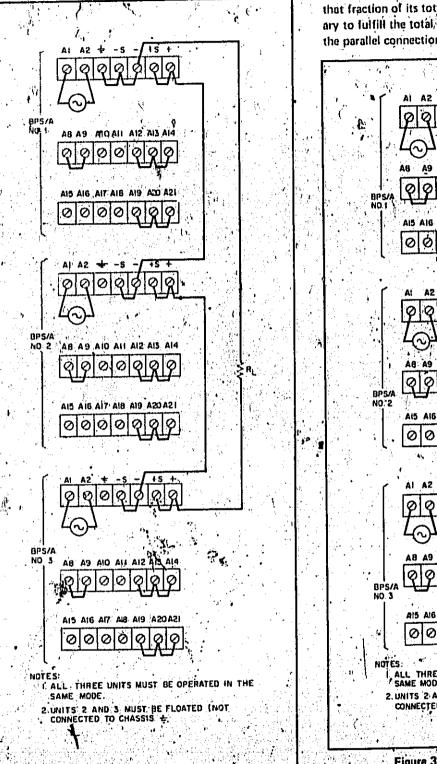


Figure 3-10. Series Connections

slightly, larger output voltage. The supply set to the lower output voltage will act as a constant voltage source; the supply set to the higher output will act as a constant current source, dropping its output voltage until it equals that of the other supply. The constant voltage source will deliver only that fraction of its total rated output current which is necess ary to fulfill the total current demand. Figure 3-11 illustrates the parallel connections for three units.

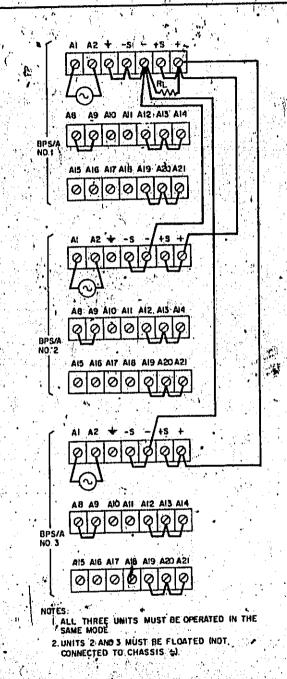


Figure 3-11. Parallel Connections

### 3-57 AUTO-SERIES AND AUTO-PARALLEL CONNECTIONS

3-58 The following paragraphs describe the connections required for combining BPS/A's in auto series and auto-

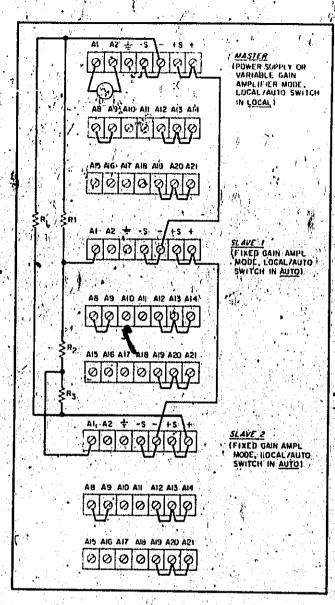


Figure 3-12. Auto-Series Connections, Three Units

parallel. These connections are employed whenever it is required to extend the voltage/gain or current capability beyond one supply. For auto-series operation, the output voltage of each slave supply varies in accordance with that of the master supply. For auto-parallel operation, complete control of the output current from one master is allowed. Diagrams are included for the strapping connections required between master and slaves for both auto-series and auto-parallel operations. In either case, the master must be in the power supply or variable gain amplifier mode and the slaves must be in the fixed gain amplifier mode. Also, for auto-series or parallel operation, the master supply's Local/ Auto switch A2S1 (see Figure 3-9) must be in the Local position and each slave supply must have its Local/Auto switch in the Auto position. The diagrams show the master strapped for local programming and with an external signal applied to the amplifier input terminals. However, the same auto-series or auto-parallel connections could be used with the master strapped for remote programming. Also, with the master supply in the power supply mode, the external signal applied to the A1 and A2 terminals is internally disconnected.

Auto-Series Operation, Two or more BPS/A's can 3 59 - be connected in an auto-series arrangement to obtain a higher output voltage than that available from a single supply. Figure 3-12 illustrates the auto-series connections for three supplies. When this arrangement is used, the output voltage of each slave supply varies in accordance with that of the master supply; thus, the total output voltage of the combination is determined by the master supply's front panel. VOLTAGE control (or remote programming input). The front panel CURRENT controls (or remote programming inputs) of all three units are operative and the current limit is equal to the lowest setting. The slave-units plust belloated off ground. Instruments can be operated floating up to 300 volts off ground whether operated singlely or in series. This limits model 6B26A (±50V @ 1.0A) to six units in series and model 6827A (±100V @ 0:5A) to three units in series

3.60 For instantaneous equal voltage sharing, resistors R1, R2, or R3 must be equal. Since any variation in R1, R2; or R3 will result in a change in the voltage divider ratio and hence the output of the slave supply, it is important that these resistors be stable, low temperature coefficient (20 ppm/°C or better). Also, they should have power rating of at least 10X, their actual power dissipation. The resistors should be selected at the normal operating voltage levels so that the current through them is about 1 to 2mA.

3-61 Auto-Parallel Operation. Two or more BPS/A's can be connected in auto-parallel arrangement to obtain an output current greater than that available from a single supply. Figure 3-13 illustrates the auto-parallel connections for three supplies to allow increased output current in constant voltage operation. When this arrangement is used, current sharing under all load conditions is permitted under control (front panel CURRENT control or remote programming) of the master supply. Because the CURRENT controls (or remote programming) of each slave are operative, they should be set to a maximum to prevent the slave reverting to constant current operation; this could occur if the master output current setting exceeded) the slave's. For equal current sharing, the leads from RM to the load and to the (-) terminals should be approximately equal in length. To maintain instrument acquracy and stability, RM should be a stable, low temperature coefficient resistor of sufficient rating to prevent any apprediable self-heating (typically  $1\Omega$ ) 8W, ±20 ppm/°C, ±1%).

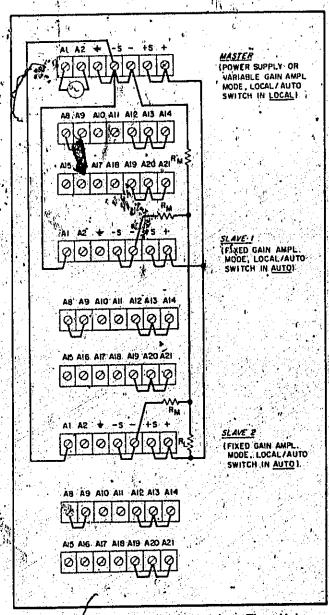


Figure 3-13. Auto-Parallel Connections, Three Units

## 3-62 BIPOLAR OVERVOLTAGE AND OVERCURRENT

3.63 Bipolar overvoltage and overcurrent limit circuits prevent excessive BPS/A voltage or current outputs. The voltage limiting circuit prevents the output voltage from exceeding approximately ±55 volts (6826A) or ±110 volts. (6827A). The current limiting circuit limits the transferit output current to a value approximately two times the maximum rated output of 1.0A (6826A) or 0.5A (6827A).

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### 3-64 REVERSE VOLTAGE AND CURRENT LOADING

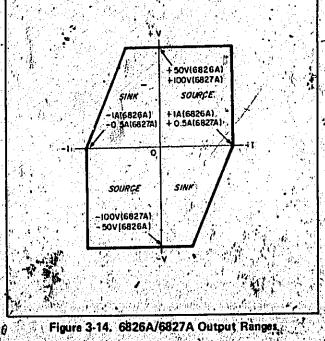
3-65: Current limit circults also protect the BPS/A from active loads that force energy in or out of the BPS/A (sink condition). This can appear as current flow into the HI OUT- (+) terminal when the terminal is positive, or current flow out of the terminal when it is negative. Figure 3-14 shows the normal operating locus of the BPS/A. As shown, the 6826A BPS/A will limit the sink current to a value, ranging linearly from 1A at 0V to 0.5A at 50V and the 6827A BPS/A will limit sink current to a value ranging linearly from 0.5A at 0V to 0.25A at 100V.

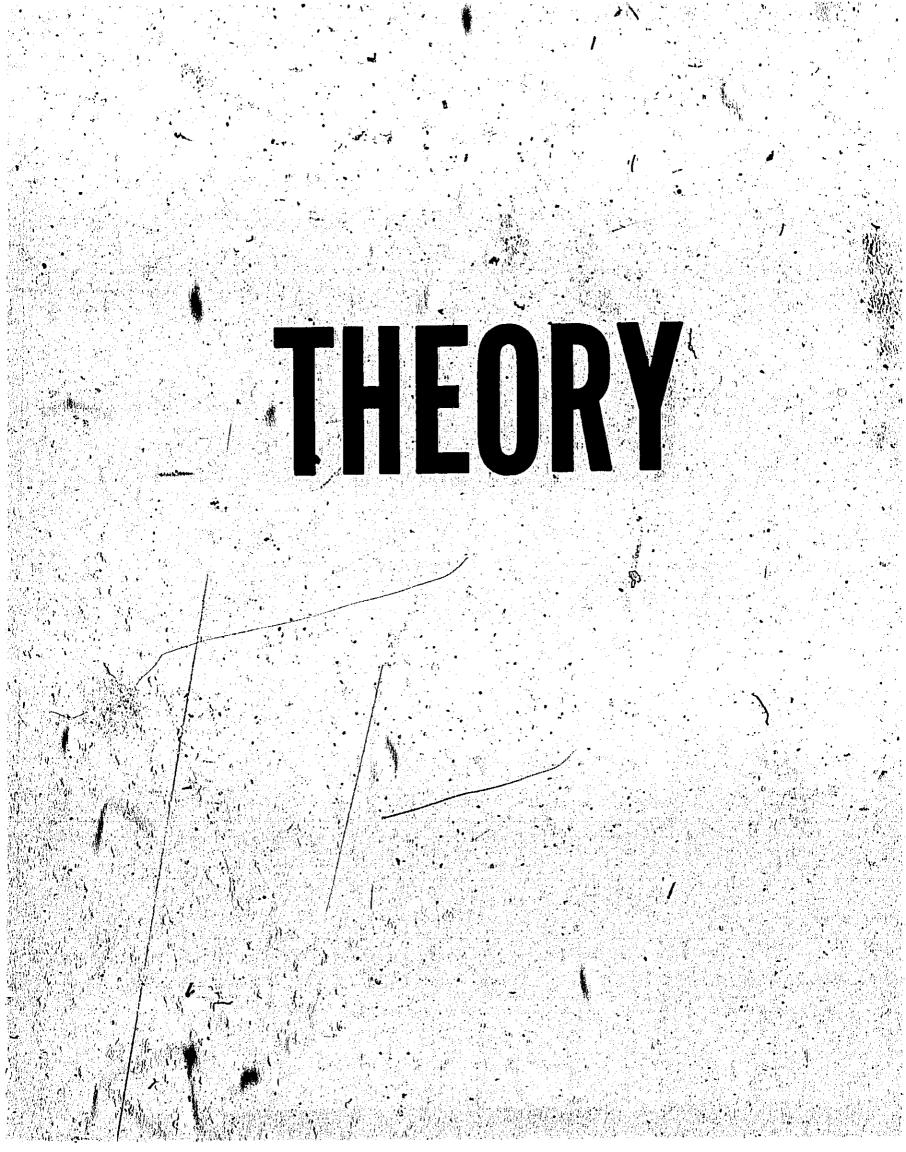
3.66 An active load can easily be accommodated by the BPS/A as long as the following precautions are adhered to: a. The active load must not be applied unless the BPS/A is in its active state.

b. Program to zero output before disconnecting load.

### - CAUTION -

Externally applied voltage to output terminals in excess of 60V (6826A) or 125V (6827A) could damage the instrument.





## SECTION IV

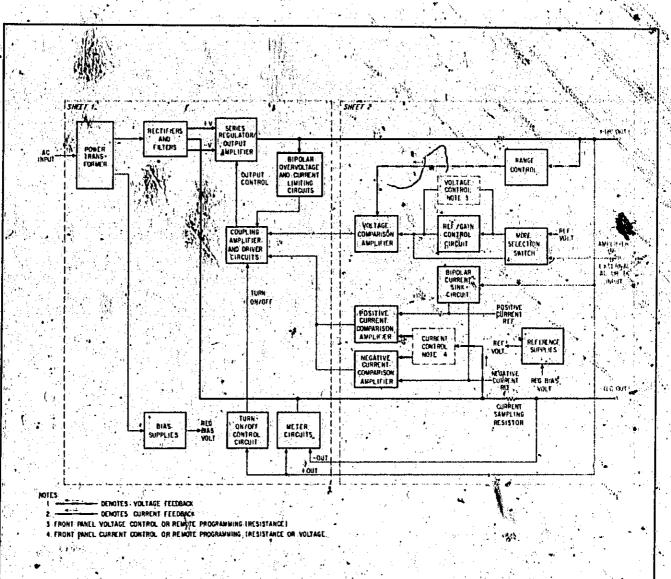


Figure 4-1. BPS/A Block Diagram

## 4-1 COVERALL DESCRIPTION

### 4-2 GENERAL

4-3 The following paragraphs provide an overall description of Bipolar Power Supply/Amplifier, Models 6826A and 6827A. The BPS/A can be operated as a power supply or a power amplifier. As a power supply, the BPS/A provides a precise low noise, low drift bipolar output voltage. The output voltage can be varied from positive to negative continuously through zero using the front panel VOLTAGE control or a remote programming control. A crossover feature automatically changes the supply mode from constant voltage to constant current. Constant voltage (CV)/constant current (CC) operation is described in Paragraph 4-15. The BPS/A is also capable of sinking current; that is, current from an active load can flow back into the BPS/A when the output terminal is positive or current can flow out of the output terminal when the voltage is negative. The BPS/A can sink current up to 50% of the rated current output. The BPS/A can also function as a variable gain or fixed gain amplifier to amplify externally applied dc and ac signals. The variable gain can be controlled locally (front panel VOLTAGE control) or remotely and is accurate to within 0.1%. The variable gain amplifier is non-inverting and has a frequency response from dc to 15kHz. Total harmonic distortion is less than 0.1%. The fixed gain amplifier is inverting and has a frequency response from dc to 35kHz.

### BLOCK DIAGRAM DESCRIPTION

4-4

4.5 Figure 4-1 is a basic block diagram of the BPS/A showing the major circuit blocks, together with the principle input/output signals of each block. The sheet numbers correlate the blocks shown on this diagram with the schematic sheets at the rear of the manual. The following description pertains to BPS/A Models 6826A and 6827A.

The ac line voltage is applied to the power transfor-4.6 mer and, after being altered in level, is rectified and filtered. Theresulting raw dc of both polarities is fed to the series regulator/output amplifier, which varies its conduction (positive or negative) in response to feedback signals to provide the proper output voltage or current. During power supply operation, this circuit functions as a series regulator to provide the proper output voltage. During amplifier operation it acts as an output amplifier to provide the proper gain for externally applied ac or dc signals. The MODE switch allows selection of the power supply mode or amplifier mode (fixed or variable gain). The series regulator/output amplifier is part of a feedback loop consisting of the amplifier and driver circuits, and the voltage and current comparison amplifier circuits.

4.7 The amplifier and driver circuits receive an error signal from the voltage or current comparison amplifiers in order to control the conduction of the series regulator/ output amplifier transistors. A positive or negative going error signal is amplified by the appropriate amplifier and driver transistors (positive or negative) and then fed back to control the appropriate series regulator/output amplifier transistors.

4-8 During constant voltage operation, the voltage comparison amplifier compares a portion of the output voltage (feedback) with a reference voltage. In the power supply or variable gain amplifier mode, the reference voltage is received from the reference/gain control circuit. In the fixed gain amplifier mode, the reference voltage is an externally applied ac or dc signal. If the feedback and reference voltages are not equal, the voltage comparison amplifier produces an amplified error signal which is further amplified by the low level amplifier and driver circuits and then fed to the series regulator/output amplifier to control the output. In this manner, the voltage comparison amplifier maintains a constant output voltage and also generates the signal necessary to set the output level according to the reference voltage or the externally applied ac or dc signal. Note that the output voltage feedback signal is applied to the voltage comparison amplifier via a range control circuit. This circuit provides the proper scaling of the output in the high and low output ranges.

In the power supply mode, the voltage comparison 4.9 amplifier and output amplifier (amplifiers, drivers, and series regulator) blocks can be viewed as a power operational amplifier whose inputs consist of the feedback signal and a control signal from the reference/gain control circuit block. The control signal is derived from an internal dc reference voltage which is applied to the reference/gain control circuit via the MODE selection switch. As the result of a summing action, a bipolar output can be obtained whose magnitude and polarity depend only upon the setting of the VOLTAGE control (or remote programming resistance) connected across the reference/gain control circuit (refer to Paragraph 4-43 for a detailed description of this circuit). In the variable gain amplifier mode, an external dc or ac signal is applied to the reference/gain control circuit via the MODE switch. For variable gain amplifier operation, the magnitude of the output depends upon the setting of the VOLTAGE control (or remote programming resistance) and the polarity of the output is the same polarity as the input signal. In the fixed gain amplifier mode, an external actor dc signal is applied to the voltage comparison amplifier via the MODE switch (the reference/gain control circuit is bypassed). For fixed gain amplifier operation, the output signal is inverted. The range control circuit in the voltage feedback path allows high or low range scaling of the output in all three modes of operation. The range control circuit may be controlled locally (front panel RANGE control) or remotely (rear terminal strip). The range control circuit is described in detail in Paragraph 4-47.

4-10 The current comparison amplifiers control the switching of BPS/A operation between constant voltage and constant current (see, Paragraph 4-15) and provide a constant current output when the BPS/A is operating as a constant current source. During constant current operation, positive and negative current comparison amplifiers detect any difference between the voltage drop across the current sampling resistor and a fixed stable reference. The voltage across the sampling resistor is applied to the amplifiers through the front panel CURRENT control or remote cursent programming control. Any change in load current whether by variation of the CURRENT control resistance (or remote current programming input) or by changes in the current through the current sampling resistor causes an error voltage proportional to the current to be applied to the amplifier and driver circuit.

Consequently, the series regulator/output amplifier conduction will be altered thereby pistoring the load current to some initial value. Either the positive or the negative current comparison amplifier can be in control depending upon the polarity of the current.

4-11 The bipolar overvoltage and current limiting circuits monitor the output voltage and current. The voltage limiting circuit prevents the output voltage from exceeding approximately 10% of the maximum rated output voltage. The current limiting circuit limits the output current to a value approximately two times the nominal rated output in order to protect the instrument during the transition from constant voltage to constant current operation.

4-12 The turn-on/off circuit protects the load from power turn-on and turn-off transients by shorting the BPS/A output and disabling the amplifier and driver circuits during turn-on and turn-off.

4-13 The bias supply converts the ac input to regulated dc voltages which are used throughout the instrument for biasing purposes. Also, the reference voltages used in the voltage and current comparison circuits are derived from the bias voltage. In addition, the bias supply provides the voltage to operate the turn-on/off circuit.

4-14 Meter circuits are provided for monitoring the BPS/A output voltage and current (ac and dc). Compensation circuits are included for meter loading effects.

4-15 CONSTANT VOLTAGE/CONSTANT CURRENT OPERATION

4-16 In order to maintain a constant voltage output, the voltage comparison amplifier tends to achieve zero output impedance by altering the output current whenever the load resistance changes. In order to maintain a constant output current, the current comparison amplifiers attempt to achieve infinite output impedance by changing the output Voltage in response to any load resistance variations. Thus, it should be noted that the voltage and current comparison amplifiers cannot operate simultaneously. For any given value of load resistance, the BPS/A must act either as a constant voltage or a constant current supply. Transfer between operation is accomplished automatically by switchable decoupling circuits at a value of load resistance equal to the ratio of the output voltage control (VOLUAGE control or remote voltage programming control) setting and the current control (CURRENT control or remote current programming control) setting. Figure 4-2 shows the output characteristics of a constant voltage/constant current power supply when operated within the bipolar output voltage and current ranges. With no load attached ( $R_{\parallel} = \infty$ ),  $I_{\Omega(\mid T} = 0$ , and EOUT = ES, the front panel voltage or remote programming control setting. When a load resistance is applied to the output terminals of the power supply, the output current increases, while the output voltage remains constant; point D thus represents a typical constant voltage operating point. Further decreases in load resistance are accompanied by further increases in load resistance is when a statistic age until the output current reaches IS, a value equal to the front panel current or remote programming control setting. At this point the supply automatically changes its mode of operation and becomes a constant current source; still further decreases in the value of load resistance are accompanled by a drop in the supply output voltage with no accompanying change in the output current value. With a short circuit across the output load terminals, IOUT = IS and EOUT = 0.

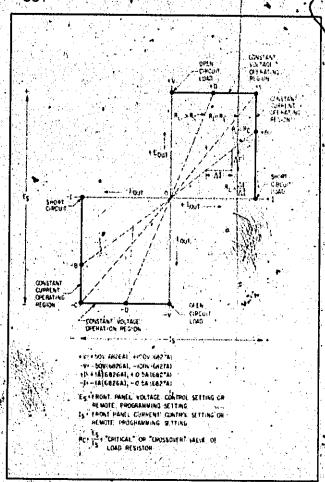


Figure 4-2. CV/CC Operating Locus

4.17 Thus, at voltage and current settings within the bipolar ranges, the "crossover" value of load resistance can be defined as  $R_C = E_S/I_S$ . Adjustment of the voltage and current settings permits this "crossover" resistance  $R_C$  to be set to any desired value within the rating of the instrument. If the magnitude of  $R_L$  is greater than  $R_C$ , the supply is in constant voltage operation.

### 4-18 DETAILED CIRCUIT DESCRIPTIONS

### 4-19 GENERAL

The following paragraphs provide detailed circuit 4.20 descriptions of BPS/A Models 6826A and 6827A. Paragraphs 4-21 through 4-56 cover Model 6826A and Paragraphs 4-57 through 4-78 cover the 6827A. The descriptions are based on simplified schematic of Figure 7-1, and the detailed schematics of Figure 7-2 (6826A) and Figure 7-3 (6827A). The simplified schematic pertains to both models and illustrates, in simplified form, the circulity depicted on Figures 7-2 and 7-3. The sheet numbers on Figure 7-1 correlate the simplified circuits with the circuits on the detailed schematics. The simplified schematic is provided for ease of understanding and should be referred to in conjunction with the appropriate detailed schematic. 'Each detailed schematic consists of two sheets; sheet 1 illustrates the output power amplifier and input power circuits and sheet 2 illustrates the voltage and current control circuits. To avoid redundancy, similar circuits are described once and the differences between the models are noted.

### 4-21 MODEL 6826A OUTPUT POWER AMPLIFIER CIRCUITS (Figure 7-2, Sheet 1)

4-22. AC Input. AC input power(is applied to)the chassis mounted power transformer T1 via the power module on the rear of the unit and the LINE ON switch S1 on the front panel. The power module contains fuse F1 (2A for 1)5Vac or 1A for 230Vac input power) and a slide switch for connecting 115 or 230Vac to the primary of the power transformer. The power transformer secondary provides the proper magnitude ao inputs to the rectifier-filter and to the bias supply.

4-23 Rectifier-Filter. The rectifier-filter circuits contained on the interconnect and power supply board A1, provide the main dc power outputs. These circuits consist of rectifier diodes arranged in full-wave center-tapped rectifier configurations with associated filter capacitors and bleeder resistors to provide  $\pm 65$  and  $\pm 80$  volt raw dc outputs. The front panel LINE ON indicator DS1 is connected across the  $\pm 65$  volt output to indicate when the BPS/A is turned on. The  $\pm 65$  volt outputs are the main input lines to the series regulator/output amplifier. The  $\pm 80$  volt outputs are the bias supply voltages for the amplifier and driver circuits on board A3.

4-24 Bias Supply. The bias supply circuit provides stable ±15 volt outputs which are used throughout the instrument for biasing purposes and to develop the reference voltages. The bias supply also provides 20 volt (filtered and unfiltered) auxiliary outputs. Two series regulator type circuits maintain the ±15 volt outputs constant. Since the circuits are identical, only the +15 volt circuit is discussed. Transistor A102 is a voltage comparison circuit that compares the +15 volt output with a fixed reference voltage. The +15 volt output is applied to the base circuit of A102 through resistors A1R29 and A1R30, whereas the reference voltage is furnished in the emitter circuit by A1VR1. If the +15 volt output changes, voltage comparator A102 produces an error signal which is applied to the base of series regulator A101. The error signal causes A101 to change its conduction so as to correct the output voltage.

4-25 Series Regulator/Output Amplifier. NPN power transistors 0.1 through 04, mounted on the heat sink assembly, are utilized as series regulators during power supply operation and as a single ended push-pull amplifier during amplifier operation.

4-26 During power supply operation, parallel connected transistors Q1, Q2, and Q3, Q4 serve as series control elenents in the positive and negative output lines, respectively. The series regulators are controlled by the positive and negative driver circuits on board A3. When the positive driver circuits are in control, the series regulators Q1 and Q2 are conducting and the series regulators Q3 and Q4 are turned off. For this condition, the supply, furnishes a positive output, The reverse is true when the negative driver circuits are in control, the supply furnishes a positive output.

4-27 Note that NPN power transistors Q1 and Q2 and associated NPN driver transistors A3Q12 and A3Q14 through A3Q16 are connected as cascaded emitter followers which respond to a positive going signal. In order to respond to negative going signals, NPN power transistors Q2 and Q3 are connected with PNP driver transistors A3Q13 and A3Q17 through A3Q19 in a preudo-PNP configuration using local feedback. This configuration allows NPN power transistors to be employed as series control elements for negative outputs.

4-28 : During amplifier operation, the transistors serve as a single-ended, push-pull output amplifier. Although the schematic shows Q1, Q2 and Q3, Q4 drawn as a conventional series regulator, the circuit could be redrawn as a pushpull amplifier without changing any of the connections. The output amplifiers are biased for class AB operation and are connected in a complementary configuration.

4-29 Coupling Amplifier and Driver Circuits. The coupling amplifier and driver circuits on board 'A3 amplify the error signal received from the voltage and current control circuits on board A2. This amplified signal controls the conduction of the series regulator/output amplifier transistors, thus controlling the amplitude and polarity of the BPS/A output. The amplifier and driver circuits consist of positive amplifier and drive) stages (Q6-Q8, Q12, Q14-Q16), and negative amplifier and driver stages (Q9-Q11, Q13, Q17-Q19) on board A3.

4-30 The error signal from the voltage or current control circuits is applied to the positive and negative voltage control amplifier circuits on board A3. For a positive going control signal the positive amplifier conducts more and the negative amplifier less. The reverse is true for a negative going control signal. Since the positive and negative sections of the amplifier and driver are symmetrical, only the positive section is discussed in detail.

4-3 The positive voltage coupling amplifier is comprised of transistor stages Q8, Q7 and Q8. Coupling amplifier stage Q7 serves as a "level changing" transistor coupling the error signal to the output driver circuits. The gain of the coupling amplifier is about 1.6X. Notice that the supply voltages for the input circuits are low level and referenced to (2) common (see Figure 7-1, sheet 2). The other amplifier and driver stages; however, use high-level supply voltages (±66 and ±80V) that are referenced to U common. Transistor Q8, in the emitter circuit of coupling amplifier Q7, serves to minimize unwanted ground current from flowing in the lowoutput sense terminal. The negative going output of coupling amplifier stage Q7 is applied to voltage amplifier Q6. The positive (Q6-Q8) and negative (Q9-Q11) coupling amplifiers provide a combined gain of approximately 36X. 'Each section (positive and negative) provides a gain of approximately 18X. As a result of the voltage amplification, the voltage across R28 biases the positive (NPN) driver transistors (Q12, Q14 through Q16) into conduction provided that a turn-on condition is present (see Paragraph 4-35). The positive driver transistors drive the positive series regulator/output amplifier transistors Q1 and Q2. These transistors are connected in series with the +65V supply voltage and thus control the BPS/A output. Capacitors C9, C10 and resistor R27 form networks which in addition to capacitor C11 connected between the HI and LO output terminals help to shape and stabilize the BPS/A output response. Additional local stabilization is afforded by network (C14, R53) in the positive driver circuits, and network (C15, R55) in the negative driver circuit.

4-32 The negative section of the power amplifier operates in the same manner as that described above except that it is activated by negative going error signals and provides negative BPS/A outputs. The negative section is comprised of negative voltage coupling amplifier stages (Q9-Q11), and negative (PNP) driver transistors (Q13, Q17 through Q19).

4-33 At zero output voltage, both the positive and negative driver sections are conducting a small current through diodes CR14, CR15, and CR16 to provide the voltage drop necessary to forward bias Q12 and Q13 simultaneously. This eliminates "dead spots" when the BPS/A is programmedthrough zero.

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4-34 Bipolar Overvoltage and Current Limiting Circuits, The bipolar overvoltage and current limiting circuits are located on board A3. Zener diodes, VR1 and VR2, connected in the base circuits of Q12 and Q13, prevent the output voltage from exceeding approximately 155 volts. Diodes CR20, CR21, and CR22 form current limiting circuits. These diodes monitor the output current flowing through the series regulator/output amplifier and limit the transient current to a value approximately 2 times the nominal rated output during the transition from the constant voltage to constant current operation.

4-35 Turn On/Off Circuit. The turn on/off circuit is comprised of transistor stages Q1 through Q5 on board A3 and relay K1 on board A1. The purpose of this circuit is to limit turn on/off transients which might affect the load. To accomplish this, the output is clamped at a low level when / the BPS/A is turned on or off.

4-36 Before power is applied to the BPS/A, relay A1K1 is deenergized connecting the HI OUT (+) to LO OUT (--). line via (2) common through resistor R60 (1 $\Omega$ , 3W). Also, with A1K1 deenergized, an open circuit is present at the emitter of A3Q1. When power is applied, relay A1K1 will not become energized for approximately 0.2 seconds due to RC time constant (R32, R37, C2). Thus, the open circuit condition is present at the emitter of A3Q1 at initial turn-on. The +20V (unfiltered) supply voltage, however, causes transistors A3Q4 and A3Q5 to be forward biased. Consequently, transistors A302 and A303 are turned on drawing current away from the bases of driver transistors A3Q12 and A3Q13 respectively; effectively turning thesestages off. After the delay (approximately 0.2 seconds) has elapsed, relay A1K1 becomes energized removing the 2 common path to the HI OUT terminal and connecting. [2] common to the emitter of A3Q1 causing the collector of A3Q1 to drop to about 0.1V. For this condition, the forward bias for transistor A3Q4 is removed causing A3Q4 to turn off which in turn causes transistors A302 and A303 to turn off removing the clamping action at the bases of A3Q12 and A3Q13. Driver transistor A3Q12 or A3Q13 will now conduct depending upon the magnitude and pular ity of the error signal:

4-37 At turn-off, the +20V (unfiltered) supply voltage is removed but relay A1K1 remains energized for approximately .1 seconds due to stored energy. When it becomes deenergized, the 20 common connection from the emitter of A3Q1 is removed and the HI OUT line is connected to the LO OUT line via R60. However, the +20V (filtered) supply voltage is present for some time causing A3Q4 and A3Q5 to be forward biased. This drives transistors A3Q2 and A3Q3 into full conduction drawing current away from the bases of A3Q12 and A3Q13 respectively, effectively. turning these stages off during the decay of stored voltages

Meter Circuits. The meter circuits provide contin-4-38 uous indications of output voltage and current. VOLTAGE-METER M1 is connected across the BPS/A output and can be used to monitor ac or dc output voltage depending upon the position of switch A1S1. With A1S1 in the AC position, diode A1CR20 rectifies the ac output voltage in order to obtain an rms reading. Variable resistors A1R8 (dc adjust) and A1R13 (ac adjust) are used when calibrating the voltmeter. CURRENT-METER M2 is connected across the current sampling resistor A2R27 whose voltage drop is propore tional to the output current. Meter M2 can measure ac or de output current depending upon the position of switch A1S2. With A1S2 in the ac position, current meter driver A2U5 and diode CR18 amplify and rectify the ac input. (applied through C13) in order to obtain an rms reading. Variable resistors A1R20 (dc adjust) and A1R18 (ac adjust) are used when calibrating the ammeter.

4-39 Switches A1S1 and A1S2 are arranged to allow the meter circuits to provide indications of output voltages and currents when the RANGE switch is used to scale the output by 10:1. Each switch provides two ranges, with a 10:1 ratio for each of the dc and ac functions. Resistor R54 is a thermistor which in conjunction with R55 compensates for temperature effects.

### 4-40 MODEL 6826A VOLTAGE AND CURRENT CONTROL CIRCUITS (Figure 7-2, Sheet 2)

4.41 The voltage control circuits consist of the mode selection, voltage reference/gain control, and voltage comparison circuits. The current control circuits consist of positive and negative current comparison circuits. Each of these main circuits and associated components are described in the following paragraphs.

4-42 Mode Selection. The front panel MODE switch (sections A5S2A and A5S2B) allows the selection of the power supply, variable gain amplifier, or fixed gain amplifier operating mode. In the power supply mode, a positive dc reference voltage is converted to a variable bipolar dc output voltage by operational amplifier techniques. In the variable gain amplifier mode, an externally applied dc or ac signal is attenuated or amplified by the voltage reference/gain control circuit for application to the voltage comparison amplifier. In the fixed gain amplifier mode, the voltage reference/gain control circuit is bypassed and an externally applied dc or ac signal is applied directly to the voltage comparison amplifier. Each of the above conditions is described in subsequent paragraphs.

4-43 Voltage Reference/Gain Control Circuit. In the power supply mode, voltage reference/gain control amplifier

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A2U2 provides a signal (0 to -10V) at the junction of A2R6 and A2R7 depending upon the setting of the front panel VOLTAGE control A5R2 (or remote programming input). With rear terminals A8 and A9 shorted and A10 open, local control is allowed through A5R2. Remote control is allowed by connecting a programming resistance between A9 and A10 with A8 open.

4.44 A fixed +5V reference vollage, derived from the +15V regulated bias supply and zener diode A2VR4, is applied to the inverting input (pin 2) of A2U2 through section S2A of the MODE switch. Depending upon the front panel VOLTAGE control (A5R2) setting (or remote programming input), A2U2 provides a 0 to -10V output. This output is summed at the junction of A2R6 and A2R7 with the +5V reference which is applied through section S2B of the MODE switch. This summing action provides a variable bipolar voltage output.

4-45 In the variable gain amplifier mode, the +6V reference is removed and an external signal (dc or ac), applied to the HI IN (A1) and LO IN (A2) terminals, is fed to the inverting input of A2U2. For this mode, the VOLTAGE control A5R2 (or remote programming input) controls the gain of A2U2 from 0 to 2X and summing with the dc references is not performed.

4-46 Diodes A2CR1 and A2CR2 limit the maximum input to the A2U2 amplifier protecting it from excessive voltage excursions. Variable resistors A1R1 (V ZERO on front panel), A2R58 (course adjustment), and A2R59 (fine adjustment) in the reference voltage circuits are used to calibrate zero output voltage and the reference voltages.

Voltage Comparison Amplifier. Voltage compari-4-47 son amplifier A2U1 continuously compares the output voltage with a reference voltage. The inverting input (pin 2) of A2U1 is the summing point which receives a portion of the output voltage (feedback voltage) from the (+S) terminal and the variable reference voltage from A2U2 or from the HI and LO IN terminals (A1 and A2). The non-inverting input (pin 3) of A2U1 receives a fixed dc bias: If a difference exists between these inputs, the comparison amplifier produces an "error" voltage at pin 6 whose amplitude is proportional to the difference. The error signal is then applied to the series regulator/output amplifier via the coupling amplifier and driver circuits. The feedback voltage, is applied to the summing point (pin 2 of A2U1) from the high sense terminal (+S) via a range network consisting of resistors A2R16, A2R42 and relay A2K3. Relay A2K3 changes the range of the power amplifier by changing the feedback resistance by a factor of 10. In the X10 range resistors A2B16 and A2B42 are in the feedback path. In the X1 range resistor A2R42 is shorted out. Relay

A2K2 switches in the proper value equalizing network for each range; A2C7 and A2R14 in the X10 range or these components in parallel with C6 and R15 in the X1 range. Relays A2K2 and A2K3 are controlled by the RANGE switch A5S2C (positions X1, X10, or REMQTE). In the X10 position, the junction of A2K2, K3, and CR4 anode is removed from 2 return which disables the relays to their normally open condition. However, with A5S2C in the X1 position, the return to 2 is completed and the relays are activated from the +15Vdc bias supply. With RANGE switch A5S2C in the REMOTE position, remote selection of the X1 or X10 range is allowed via rear terminal A11.

Changes in the error signal magnitude and polarity 4-48 instantaneously cause the summing point potential to change. This change causes comparison amplifier A2U1 to provide the proper correction voltage to the low level amplifier and driver circuits. The correction voltage levels at the low level amplifier input are from approximately -2.5V to --4.5V and correspond to the output voltage range of +50V to -50V. A correction voltage of approximately -3.5V corresponds to an output voltage of OV. Zener diode A2VR8, diodes A2CR20, and CR21, and resistor A2R41 prevent A2U1 from going deep into saturation. Diodes A2CR18 and A2CR19 limit the maximum input to the comparison amplifier thus protecting it from overvoltage conditions. Variable resistors A2R60 and A2R61, connected to the +6,2V and -6.2V reference voltage circuits through resistors A2R36 to A2R39 and A2R51 through A2R54, are used for output zero and offset adjustments, Relay A2K1 opens the input path to A2U1 when the BPS/A is remotely controlled and the programmed data is changed, thus, preventing data transients from affecting the output voltage. The AUTO/LOCAL switch A2S1 in the feedback loop is normally left in the LOCAL position. The AUTO position is used for auto-series or auto-parallel operation when the summing junction of the error amplifier must be available for external error signal connections from other units.

4.49 Output Voltage/Gain Control Summary. As stated previously, the BPS/A output voltage is developed utilizing operational amplifier techniques. In the power supply mode, the bipolar output characteristic is developed through the summing of the internal fixed reference voltage (VREF) and y a voltage which is dependent only on a single programming control (VOLTAGE control A5R2 or a remote programming resistance). Eo is given by the following equations for the X1 and X10 ranges;

$$E_{O} = +V_{REF} \left( \frac{R_{PV}}{A1R42} \times \frac{R_{F}}{A2R6} \right) - V_{REF} \left( \frac{R_{F}}{A2R7} \right)$$

where;

Rpy = 0 to 20.48KΩ (front panel VOLTAGE control or remote programming resistance) RF = feedback resistance (A2R16, or A2R16 + A2R42) = 10.24KΩ br 102.4KΩ (X1 of X10 range respectively) A1R42 = A2R6 = A2R7 = 10.24KΩ VREF = <del>0</del>.12V

< In the X1 range:

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$$E_{0} = 5.12V \left(\frac{R_{PV}}{10.24K} \times \frac{10.24K}{10.24K}\right) - 5.12V \left(\frac{10.24K}{10.24K}\right)$$

$$E_0 = 5.12V'(\frac{10V}{10.24K}, -1)$$

therefore;  $E_0 = -5.12V$ , if  $R_{PV} = 0$  $E_0 = 0$ , if  $R_{PV} = 10.24K$  $E_0 = +5.12V$ , if  $R_{PV} = 20.48K$ 

In the X10 range:

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$$D = 5.12V \left(\frac{R_{PV}}{10.24K} \times \frac{102.4K}{10.24K}\right) - 5.12V \left(\frac{102.4K}{10.24K}\right)$$
  
$$D = 5.12V \left(\frac{R_{PV}}{10.24K} \times \frac{102.4K}{10.24K}\right)$$

therefore;  $E_O = -51.2V$ , if  $R_{PV} = 0$  $E_O = 0$ , if  $R_{PV} = 10.24K$  $E_O = +51.2V$ , if  $R_{PV} = 20.48K$ 

4-50. In the variable gain amplifier mode, the BPS/A controls the gain of an externally applied dc or ac signal. For this mode, the internal fixed dc reference voltage is disconnected and the reference/gain control circuit attenuates or amplifies the externally applied signal from 0 to 2X depending upon the setting of the VOETAGE control A5R2 (or remote programming resistance). The feedback resistor(s) provide a gain of 1 in the X1 range and a gain of 10 in the X10 range. Consequently, the variable gain is from 0 to 2X in the X1 range and from 0 to 20X in the X10 range. In the fixed gain amplifier mode, the gain is controlled only by the feedback resistor(s) which provide a gain of 1X in the X1 range and 10X in the X10 range.

4-51 Current Comparison Amplifiers. Current comparison amplifiers A2U3 (positive) and A2U4 (negative) control BPS/A operation between constant voltage and constant current by continuously monitoring the voltage drop across the current sampling resistor (A2R27). This voltage drop is applied to the current comparison amplifiers via the front panel CURRENT control A5R t or the remote programming input terminals. The other input to the current comparison amplifiers is a stable fixed reference current. Any disturbance in load current whether/by variation of the CURRENT control (or remote programming input) or in the current flow through the sampling resistor (as in line or load change) will cause a corrective voltage to alter the appropriate series

regulator (positive or negative) conduction thereby restoring the load current to some initial value.

4.52 Positive current comparison amplifier A2U3 monitors positive output currents and negative current comparison amplifier A2U4 monitors negative output currents. These amplifiers control switching the BPS/A belween constant voltage and constant current operation. In constant voltage operation, they are in saturation, reverse blasing A2CR13, and A2CR14 and preventing any current control action. In constant current operation, they become linear comparison amplifiers allowing BPS/A operation as a constant current source. Also, for current sink conditions, they limit the output current to 1/2 maximum rated output through separate control circuits consisting of A2CR3, CR4, CR7, CR8, CR11, CR12, R28, and R29. Because the two comparison amplifier's described in detail.

4-53 The voltage drop across the current sampling resistor A2R27 is applied to pin 3 of A2U3 via the front panel CURRENT control A5R1 (or the remote programming input terminals). Current control through A5R1 (local control) is achieved with rear terminals A12, A13, and A14 strapped together for positive currents and with A19, A20, and A21 strapped together for negative currents. External digital resistance control can be implemented by connecting the proper resistances between A13, A14 (strapped together) and A18 for positive currents, and between A20, A21 (strapped together) and A18 for negative currents. Another method of control of the current is through voltage programming via terminals A14 and A18 and A20 and A18 for positive and negative currents respectively.

A fixed reference current is applied to the other. 4-54 input (pin 2) of A2U3. During constant voltage operation, A2U3 is saturated causing the output to be positive. Zener, The A2VR5 and diode A2CR5 clamp the output at +7.5V preventing A2U3 from going too far into saturation. For his condition, diode A2CR14 is back biased and PNP witching transistor A2Q5 is turned off causing A2CR9 and A2CR10 to be forward biased. With A2CR14 back Biased, constant voltage operation is enabled and constant current operation is disabled (the negative constant current diode A2CR13 must also be back biased for this condition). With A2CR9 forward biased, transistor A1Q1 is turned-on allowing capacitor A2C9 to charge during constant voltage operation. This will speed up the transition from constant voltage to constant current operation. With A2CR10 forward biased, the CURRENT MODE indicator DS1 is off (A202 turned on and A203 turned off) and the FLAG output is disabled (low level, FLAG output with A2Q4 turned-on). Networks consisting of A2C11, R63, R64 and A2C12, R65, R66 are included in the inputs of A2U3 and A2U4 respectively. These networks ill conjunction with local compensation represented by A2C9, R46, and R47 (common to both A2U3 and A2U4) provide response stabilizing compensation.

4-65 If the output current increases above the set value, the input to pin 3 of A2U3 becomes less positive. For this condition, the output (pin 6) of A2U3 goes negative forward biasing A2CR14. With A2CR14 forward biased, the BPS/A switches from constant voltage to constant current operation and an error signal is applied to alter the series regulator, (positive) conduction and maintain the output current at the desired value. Also, for this condition, A2O5 is switched on back biased, A2O1 is turned off. With A2CR10 With A2CR9 back biased, A2O1 is turned off. With A2CR10 back biased, the CURRENT MODE Indicator DS1 lights (A2O2 off, A2O3 on) and the FLAG output is enabled (A2O4 turned off providing a high FLAG output):

During current sinking operations, the input to 4-56 A2U3 (negative voltage case) is altered causing the current being sinked to increase or decrease in response to the voltage magnitude of the active load. When the output voltage is negative, diodes A2CR3 and A2CR12 become forward blased through A2R28 altering the reference current to A203. This condition in conjunction with the voltage phange across A2R27 will cause the output of A2U3 to adjust the drive to the appropriate output transistors to limit the imposed load current. The operation of A2U4 is similar in principle for the positive voltage case. Front panel controls +I ZERO (A1R2) and - I ZERO (A1R3), in the positive and negative current reference circuits are used to adjust the respective zero for programming accuracy. Variable resistors A1R19 and A2R21 are used to calibrate the positive and negative current references.

### 4-57 MODEL 6827A OUTPUT POWER AMPLIFIER CIRCUITS (Figure 7-3, Sheet 1)

4-58 AC Input. AC input power is applied to the chassis mounted power transformer T1 via the power module on the rear of the unit and the LINE ON switch S1 on the front panel. The power module contains fuse F1 (2A for 115Vac or 1A for 230Vac input power) and a slide switch for connecting 115 or 230Vac to the primary of the power transformer. The power transformer secondly provides the proper magnitude ac inputs to the rectifier filter and to the bias supply.

4-59 Rectifier-Filter. The rectifier-filter circuits, contained on the interconnect and power supply board A1, provide the main dc power outputs. These circuits consist of rectifier diodes arranged in full-wave center-tapped rectifier configurations with associated filter capacitors and bleeder resistors to provide ±140 and ±155 volt unregulated de outputs. The front panel LINE ON indicator, DS1 is connected across the  $\pm$ 140 volt output to indicate when the BPS/A is turned on. The  $\pm$ 140 volt outputs are the main input lines to the series regulator/output amplifier. The  $\pm$ 155 volt outputs are the bias supply voltages for the amplifier and driver circuits on board A3.

4-60 Bias Supply. The bias supply circuit provides stable, ±15 volt outputs which are used throughout the instrument for biasing purposes and to develop the reference voltages. The bias supply also provides 20 volt (filtered and unfiltered) auxiliary outputs. Two series regulator circuits maintain the ±15 volt outputs constant. Circuit operation is identical to that described in Paragraph 4-24 for Model 6826A.

4-61 Series Regulator/Output Amplifier. NPN power transistors Q1 through Q6, mounted on the heat sink assembly, are utilized as series regulators during power supply operation or as a power amplifier during amplifier operation. During power supply operation, transistors Q1 through Q6 serve as series control elements. Power transistors Q1 through Q3 are connected in series with the +140V supply and Q4 through Q6 are connected in series with the -140V supply. The conduction of Q3 or Q4 is controlled directly by the output of driver stages A3Q12 (positive) or A3Q10 (negative) depending upon which section is active at the time. The conduction of the other series regulator transistors Q1, Q2 or.Q5; Q6 is controlled by the positive or negative bias transistors (A3Q17-Q20 or A3Q13-Q16) in response to the output voltage magnitude, as determined by the VOLTAGE control or remote resistance setting.

4-62 Coupling Amplifier and Driver Circuits. The coupling amplifier and driver circuits on board A3 amplify the voltage or current error signal received from the voltage or current control circuits on board A2. This amplified signal controls the conduction of the series regulator/output amplifier transistors; thus, controlling the output of the BPS/A output. The amplifier and driver circuits consist of coupling stages (Q6, Q7), single ended amplifier stage Q8, positive driver stages (Q11, Q12 and Q17 through Q19) and negative driver stages (Q9, Q10, and Q13 through Q16). For positive output voltages, the positive driver stages are conducting and the negative stages are cut off. The reverse is true for negative voltages.

4-63 Coupling amplifier stage Q7 serves as a "level changing" transistor coupling the relatively small error signal level input to the considerably higher output levels used in the driver circuits. Notice that the supply voltages for the Q7 input circuits are low level and referenced to D common (see Figure 7-3, sheet 2). The amplifier and driver stages on board A3, however, use high-level supply voltages (±140V) and ±155V) that are referenced to D common. Transistor Q6, in the emitter circuit of coupling amplifier Q7, serves to minimize unwanted ground current from flowing in the

#### low output sense terminal.

4-64 Transistor QB is a voltage amplifier, having a gain of approximately 50X, while the driver stages provide most of the current gain of the power amplifier. Hence, the voltage at the bases of positive (Q11) and negative (Q9) input stages is essentially equal to the output voltage of the BPS/A. Q8, together with VR3, R31-R33, CR11, CR12, CR15, CR16 and VR4 form a voltage divider in the base circuits of input stages. The conduction of Q8 controls the current flowing through the voltage divider and, thus, the bias at the bases of Q11 and Q9. Consequently, Q11 or Q9 is driven into conduction provided that a turn-on condition is present (see Paragraph 4-69):

4.65 Driver stages Q12 (positive) and Q10 (negative) drive the power output transistors Q1-Q3 and Q4-Q6 respectively. The conduction of power output transistor Q3 or Q4 is controlled directly by the output of Q12 or Q10 depending upon which driver is active at the time. The conduction of the other series power output transistors (Q1, Q2 or Q5, Q6) is controlled by the positive or negative bias transistors (Q17-Q20 or Q13-Q16).

4.66 The function of the bias networks is to divide the voltage drop (and thus the power dissipation) among the three series connected power transistors in the active branch. This is accomplished by sensing the programmed output voltage level and using it to develop two additional voltages; one representing the output voltage plus 2/3 of the difference between 140 volts and the output voltage, and the other representing approximately 1/3 of the same value. For the positive bias network, R56 and R58 develop the 2/3 voltage function while R55 and R57 develop the 1/3 voltage function. (R42, R43, R45, and R46 perform the same function for the negative bias network.) The 2/3 voltage level at the junction of R56 and R58 is power amplified by compound emitter followers Q20 and Q19 and appears at approximately the same 2/3 voltage level at the emitter ... of power transistor Q1. The 1/3 voltage level is similarly amplified by Q18 and Q17 and appears at the emitter of power transistor Q2. From this it can be seen that 1/3 of the voltage drop between 140 volts and the programmed. voltage level appears across each of the three series connected power transistors. The negative bias network operates 'in a similar manner.

4-67 The remaining components of the bias networks improve general circuit operation. Diodes CR18 through CR21 protect the base emitter junctions of the bias transistors from becoming excessively reversed biased. Resistors R44, R47; R50-R53, R59-R63 offset undesireable leakage currents. Capacitors C13, C14, and C17 permit the circuit to respond to rapid changes in programmed output voltage. Capacitors C16, C18 and resistor R70 connected between the HI<sup>®</sup>(+) and LO (--) output terminals help to shape and stabilize the BPS/A output response.

4-68 Bipolar Overvoitage and Current Limiting Circuits. Bipolar overvoltage and current limiting circuits are located on Board A3. Zener diodes VR5 through VR8 connected in the base circuits of Q11 and Q9 prevent the output voltage from exceeding approximately  $\pm 110$  volts. Diodes CR22 through CR25 monitor the current flowing through the output power transistors and limit the current to a value approximately 2 times the nominal rated output during the transition from constant voltage to constant current operation.

4-69 Turn On/Off Circuit. The operation of this circuit is exactly the same as described in Paragraph 4-35 for the 6826A model except that the control action, in this case, is to remove drive from A3Q9 or A3Q11 depending upon the polarity being programmed.

Meter Circuits. The meter circuits provide contin-4.70 uous indications of output voltage and current. VOLTAGE-METER M1 is connected across the BPS/A output and can a be used to monitor ac or dc output voltage depending upon the position of switch A1S1. With A1S1 in the AC position, diode A1CR19 and A1CR20 rectify the ac output voltage. in order to obtain an rms reading. Variable resistors A1R8 (dc adjust) and A1R13 (ac adjust) are used when calibrating the voltmeter, CURRENT METER M2 is connected across the current sampling resistor A2R27 whose voltage drop is proportional to the output current. Meter M2 can measure ac or dc output current depending upon the position of switch A1S2. With A1S2 in the ac position, current meter driver A2U5 and diode CR22; 23 amplifier and rectify the s ac input (applied through C13 and CR18) in order to obtain an rms reading. Variable resistors A1R20 (dc adjust) and A1B18 (ac adjust) are used when calibrating the ammeter.

4-71 Switches A1S1 and A1S2 are arranged to allowthe meter circuits to provide indications of output voltages and currents when the RANGE switch is used to scale the output by 10:1. Each switch provides two ranges, with a 10:1 ratio, for each of the dc and ac functions. Resistor R54 is a thermistor which in conjunction with R55 compensates for temperature effects.

### 4-72 MODEL 6827A VOLTAGE AND CURRENT CONTROL CIRCUITS (Figure 7-3, Sheet 2)

4.73 These circuits are similar to the 6826A voltage and control circuits described in Paragraphs 4-40 through 4-56 except for certain component additions and value differences due to the difference in instrument specifications. For example, the values of the feedback resistors (scaling resistors) A2R16 and A2R42 are larger in order to obtain the higher output voltage ranges of the 6827A. As described in Paragraph 4.49, the BPS/A output voltage (EO), ' in the power supply mode, is a function of the internal fixed reference voltage (VREF) and the programming control (VOLTAGE control or remote programming resistance). For the 6827A instrument, EO is given by the following equations in the X1 and X10 output ranges:

$$E_{O} \neq +V_{REF} \left( \frac{R_{PV}}{A1R42} \times \frac{R_{F}}{A2R6} \right) - V_{REF} \left( \frac{R_{F}}{A2R7} \right)$$

where  $x^{*}$ 

Rpv = 0 to 20.48KΩ (front panel VOLTAGE control of remote programming resistance) R<sub>F</sub> = (fedback resistance (A2R16 or A2R16 + A2R42) = 20.48KΩ or 204.8KΩ (XT or X10 range re-

.spectively) A1R42 = A2R6 = A2R7 = 10.24KΩ, and VREF = 5.12V

In the X1 range:

$$E_{O} = 5.12V \left( \frac{R_{PV}}{10.24K} \times \frac{20.48K}{10.24K} \right) - 5.12V \left( \frac{20.48K}{10.24K} \right)$$
$$E_{O} = 5.12V \left( \frac{2R_{PV}}{10.24K} - 2 \right)$$

therefore; 
$$E_0 = -10.24V$$
, if  $R_{PV} = 0$   
 $E_0 = 0$ , if  $R_{PV} = 10.24K$   
 $E_0 = +10.24V$ , if  $R_{PV} = 20.48K$ 

In the X10 range:

En

$$E_{O} = 5.12V \left( \frac{R_{PV}}{10.24K} \times \frac{204.8K}{10.24K} \right) - 5.12V \left( \frac{204.8K}{10.24K} \right)$$

$$= 5.12V \left(\frac{2R_{PV}}{1.024K} - 20\right)$$

therefore; 
$$E_0 = -102.4V$$
, if RpV = 0  
 $E_0 = 0$ , if RpV = 10.24K  
 $E_0 = +102.4V$ , if RpV = 20.48K

4.74 In the variable gain amplifier mode, the BPS/A controls the gain of an externally applied dc or ac signal. For this mode, the internal fixed dc reference is disconnected and the reference/gain control circuit attenuates or amplifies the externally applied signal from 0 to 2X depending upon the setting of the VOLTAGE control A5R2 (or remote programming resistance). The feedback resistor(s)\_provide a gain of 2 in the X1 range or 20 in the X10 range. Consequently, the variable gain is from 0 to 4X in the X1 range and from 0 to 40X in the X10 range.

4-75 In the fixed gain amplifier mode of operation, the gain is controlled only by the feedback resistor(s) which provide a gain of 2X in the X1 range and 20X in the X10 range.

a girt

4-76 The operation of the 6827A current comparison amplifier circuits is identical to that described in Paragraphs 4-51 Through 4-56 for Model 6826A. Certain components are added (A2R23 and A2R26) in the 6827A model because of the difference in current ratings.

4-11



### SECTION V MAINTENANCE

### 5.1 - INTRODUCTION

5-2 The performance checks (Paragraph 5-5) should be made to check the operation of the BPS/A after repairs or for periodic maintenance. These checks are also suitable for incoming inspection. If a fault is detected in the BPS/A while making the performance check or during normal operation, proceed to the troubleshooting procedures (Paragraph 5-60). After repair and replacement (Paragraph 5-84), perform any necessary adjustments and calibrations (Paragraph 5-98). Before returning the BPS/A to normal operation, repeat the performance check to ensure that the fault has been properly corrected and that no other faults exist. Before performing any maintenance checks, turn on the BPS/A and, allow a half-hour warm-up.

### 5-3 TEST EQUIPMENT REQUIRED

5-4 Table 5-1 lists the jest equipment required to perform the various procedures described in this section.

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түре		USE ,	
Differential . Voltmeter	Sensitivity: 500μV full scale (min.). Input impedance: 100MΩ (min.).	Measure dc voltages; calibration procedures. Measure amplifier gain (Option 001).	HP345013 with Option 001
Digital Voltmeter Oscilloscope	Accuracy: 0.004% Sensitivity: 1μV, flòating input. Sensitivity and bandwidth: 1mV/ cm and 50MHz.	Measure do voltages, calibration procedures. Measure ripple; display translent recovery waveforms; measure noise spikes. Measure response.	<ul> <li>HP3462A or HP3420B</li> <li>HP180A plus</li> <li>1801A, and 1821A</li> <li>plug-ins.</li> </ul>
Function * Generator	100Hz/squarewave and sinewave.	Measure frequency response and output impedance.	HP3310A
Distortion Analyzer	Accuracy: ±3% from 10Hz to 1MHz.	Measure amplifier distortion.	HP331A
Variable Voltage Transformer	Current rating: 2A; Rangé: 90- 130Vac; Equipped with voltmeter accurate within 1 volt.	Vary ac input for high line to low , line regulation.	
Repetitive Load Switch	Rate: 60-400Hz; 2µsec rise and fall time.	Measure transient résponse.	Sec Figure 5-4.
Current Sampling Resistor	Value: 10 ± .1%, 24W	Measure, output current, calibrate: 'ammeter.	
Resistive Loads	Value: See Figure 5-1,±1%, 50W	Load resistors.	
Terminating Resistors	Value: 50 ohms, ½W, ±5%, non- inductive: 4 required.	Noise spike measurement.	
Blocking Capacitors	Values: 0.01µF, 100Vdc, 2 required; 1000µF, 60Vdc, 1 required.	Noise spike measurement; output impedance measurement.	
Programming Resistors	5.12K ± .05% 10.24K ± .05% 20.48K ± .05%		0811-2957 0811-2958 0811-2959 (Micro Ohm Type 132F)

Table 5-1. Test Equipment Required

### 5-5 PERFORMANCE TEST

5.6 The following test can be used as an incoming inspection check and appropriate portions of the test can be repeated either to check the operation of the instrument after repairs or for periodic maintenance tests. The tests are performed using a 115Vac, 60Hz, single phase input power source.

### 5-7 POWER SUPPLY MODE TESTS

5-8 All measuring devices must be connected to the rear sensing terminals of the supply and not to the front output terminals if maximum accuracy is to be obtained in the following measurements. In addition, the measuring devices must be connected as close to the sensing terminals as possible. This is particularly important when measuring the transient response, regulation, or ripple of the power supply. Note that under no circumstances should the measuring instruments be connected across the load. A measurement made across the load includes the impedance of the leads to the load and such lead lengths can easily have an impedance several orders of magnitude greater than the supply impedance, thus invalidating the measurement.

5.9 To avoid mutual coupling effects, each monitoring device must be connected to the sensing terminals by a separate pair of leads. Twisted pairs or shielded two-wire cables should be used to avoid pickup on the measuring leads. The load resistor should be connected across the output terminals as close to the supply as possible. When measuring the constant voltage performance specifications, the current controls should be set well above (at least 10%) the maximum output current which the supply will draw; since the onset of constant current action will cause a drop in output voltage, increased ripple, and other performance changes not properly, scribed to the constant voltage operation of the supply.

5-10 DC Voltage Output and Voltmeter Accuracy. To check the DC voltage output and voltmeter accuracy, pro-

#### in NOTE 🖌

The CURRENT MODE light should be off during this test.

a. Connect appropriate high range load resistor (RL) across output terminals (see Figure 5-1).

> b. Connect DVM across +S and -S terminals. c. Set BPS/A front panel controls as follows: MODE switch: POWER SUPPLY RANGE switch: X10 VOLTAGE control: midposition

	-  - +5   +				0	
	/ +5/+				-0.1	
1 0000	000		•	: <b>I</b>		
				•		
	Ľvvu <sup>R</sup> Ľ					
	MODEL NO.	HI RAI	1 1 101	MS) LO RAN		
	6826A	50		5		
1	6827A,	200	0	20		e por el

Figure 5-1: Power Supply Mode Test Setup

	CURRENT control:	fully clockwise
	VOLTAGE METER:	high range DC
	CURRENT METER:	high range DC
d	. Turn on BPS/A and al	low a five minute warm.
up'period.		

e	Turn VOLT	AGE control c	ockwise until	DVM
	<ol> <li>1.1.1</li> <li>1.1.1</li> <li>1.1.1</li> </ol>		ut voltage as la	
	6826A:		+50V	
	6827A:		+100V	1.19
f.	Observe that	front panel vo	Itmeter reads a	IS .
follows:				

6826A:		+50V ± 1.5V
6827A:		+100V ± 3V

g. Turn VOLTAGE control counterclockwise until DVM indicates maximum rated negative/output voltage as follows:

3.	· · · •	· · · · ·		• •				
		6826A:	•	1.		-50	V	
		6827A:		•		-10	0V	
	h.	Observe	that f	ront p	anel vo	Itmete	r reads a	95
s:	•						е на 19	
× 1		<sup>-</sup>	,		1.		<u></u>	

÷	6826A:		- 11 L	1.22/2010	-50V ±	1.5V 🗄
• .		1. J. 1. 1. J. 1.	61.12			
*	6827A:		$(J_i) \in \mathbb{R}$		⊆ <u>—</u> 100V :	1 3V

i. Turn off BPS/A., Change load resistor RL to appropriate low range value (see Figure 5-1) and set RANGE switch to X1.

(j. Repeat steps (d) through (h) for following DVM and front panel voltmeter readings (use fow range DC scale).

요즘 것 같아요.		N 1997 - 1		林山 ごうさうしょう		
Mode		S DVI	1		Voltmeto	<u>ir</u> - 1
1 a fort		(a. 1944).				
<u>. 46 P</u>	196 4		일 문문		4	
68267	¥ 4	+5V/-	-5V	. +5V±15	0mV/5V	±160mV
	こんき しょう			1. State 1.	(1) 11 (1) (0) (1) (1)	ふいしょり しょう マイ・シャ
	그는 것 같은					
			n inder			
6827 <i>/</i>		+10V/	(	+10V±3	00mÝ/10	)∨±300mi∨

follow

Source Effect (Line Regulation). Definition: The change  $\Delta EOUT$  in the static value of dc output voltage resulting from a change in ac input voltage over the specified range from low line (usually 104/208 volts) to high line (usually 127/ 254 volts), or from high line to low line.

### NOTE

The CURRENT MODE light should be off during this test.

5-12 To check the line regulation, proceed as follows: a. Connect the test setup shown in Figure 5-1. Use high range (X10) load resistor value.

b. Connect variable auto transformer between the input power source and the BPS/A power input.

c. Adjust variable transformer for a 104 volts ac

. Set BPS	A front panel	controls as follows:
MODE s		POWER SUPPLY
RANGE		X10
	GE control:	midposition
	NT control:	fully clockwise
		high range DC
	NT METER:	high range DC
e. Connect	a DVM to the	-S and +S terminals of

the BPS/A

f. Turn on BPS/A and adjust VOLTAGE control clockwise for maximum rated positive output voltage (high range), ±50V (6826A) or ±100V (6827A), as indicated on DVM.

g. Adjust variable auto transformer for a 127 volts ac input.

h. Reading on DVM should not vary from reading in step (f) by more than:

6826A:

- 6827A:
- i. Set variable auto transformer for a 104Vac input.

10mV

20mV

j. Adjust VOLTAGE control counterclockwise, for maximum rated negative high range output voltage, -50V

(6826A) or -100V (6827A), as indicated on DVM. k. Adjust variable auto transformer for a 127Vac input.

I. Reading on DVM should not vary from reading in step (j) by more than:

•	57	2	11	÷ 4	۰		1.	. •	11.1	èс.		1	н	Ο	风日	÷ 14.	24	ы. н.	11 ÷	÷.	~	۰.	11	1.1	
	1	1	20	20	C.	٨	• · · ·	611		1	197		٦	121	17	) (ř.		÷	1.	1	On	n.1	10	5	÷
đ,	11	1	20	24	U	m	- 7	÷.,		11	111	10	S I		14	1.1	91	- C	۰.	ς.			1	(1. i	۰,
2		5	6.5	ാ	1.1		14 3	10.1	۱.		10	1.	÷.,	λ.	14	1	64	خ:	16	3	On	~	17	2.	,
2		1	58	77	7	А		°.,	24	1.3	сų,	ŧ٩,	ſч			- Y 1	1.11	- C -	10	4	U		¥ •	ы Ş	
÷,	٩.						t		20	٠.	$10^{\circ}$	10	ì.	<u>ون</u>	÷ .	14	i.	1.5		$12^{1}$	÷	67	л÷,	1	ŝ

m. Turn off BPS/A and change load resistor to low range (X1) value, and RANGE switch to X1. n. Adjust variable auto transformer for a 104Vac input. o. Turn on BPS/A and adjust VOLTAGE control clockwise for the maximum rated positive output voltage (low range), +5V (6826A) or +10V (6827A) as indicated on DVM.

p. Adjust variable auto transformer for a 127Vac input. g. Reading on DVM should not vary from reading st

in step (o) by more than: 6826A:

6827A:

5.13

2mV

r. Set variable auto transformed for a 104Vac in-

put. s. Adjust VOL VAGE control counterclockwise for maximum rated negative low range butput voltage, -5V. (6826A) or -10V (6827A), as indicated on DVM.

t. Adjust variable auto transformer for a 127Vac input.

u. Reading on DVM should not vary from reading in step (s) by more than:

1	7				1	
6826A:	1 :		3.1.1.1	1.1	1	1mV
	· ·	<u> </u>		< i i i	{ .n.*	
6827A	. *		- 1	19		∷2mV`
UUZINI		- 1	- 11 i	5 B	a di se	

Load Effect (Load Regulation). 5 Definition: The change  $\Delta EOUT$  in the static value of dc output voltage resulting from a change in load resistance from open circuit to a value which yields maximum rated output current (or vice versa).

### NOTE

The CURRENT MODE light should be off dur-

5-14/ The load regulation check is performed at low line conditions. To check load regulation, proceed as follows: a. Connect the test setup shown in Figure 5-1. Use the high range (X10) load registance value.

b. Connect variable auto transformer, between the input power source and the BPS/A power input. Adjust variable auto transformer for a 104Vac input.

c. Set BPS/A	front pan	el controls a	s follo	ws:
MODESW	itch (	POWER	SUPPL	<b>.</b> Y
P RANGE s		X10		
1 VOLTAG		midposi		
CURREN		fully clo	an geographic and and	
	E METER		=	
CURREN	T METER	high ran		11 01
d Connect	DVM to t	he –S and 1	IS term	ninals of

the BPS/A.

53

e. Turn on BPS/A and adjust VOLTAGE control clockwise for the maximum rated positive output voltage (high range), +50V (6826A) or +100V (6827A), as indicated

#### on DVM.

f. Disconnect load resistor. Reading on DVM should not vary from the reading in step (e) by more than:

6mV

11mV

### 6826A:

6827A:

g. Adjust VOLTAGE control counterclockwise for maximum rated negative output (high range), -50V (6826A) or -100V (6827A), as indicated on DVM.

h. Connect load resistor (high range value). Reading on DVM should not vary from reading in step (g) by more than:

6826A:		. 6mV
6827A:		11mÝ
002/A.	a Parlande Se	

i. Turn off BPS/A and change load resistor to low range (X1) value and RANGE switch to X1.

j. Turn on BPS/A and adjust VOLTAGE control clockwise for the maximum rated positive output voltage (low range), +5V (6826A) or +10V (6827A), as indicated on DVM.

k. Disconnect load resistor. Reading on DVM should not vary from the reading in step (j) by more than: 6826A: 0.6mV 6827A:

6827A: 1.3mV I. Adjust VOLTAGE control counterclockwise for the maximum rated negative output voltage (low range), -5V (6826A) or -10V (6827A), as indicated on DVM.

m. Connect load resistor (low range value)." Reading on DVM should not vary from reading in step (e) by more than:

	6826A:	• .• .	0.6mV
• • •	6827A:		1.3mV

5-15 PARD (Ripple and Noise).

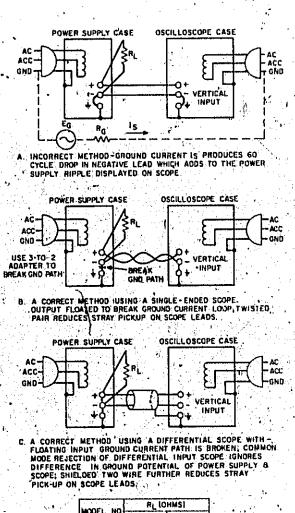
Definition: The residual AC voltage which is superimposed on the DC output of a regulated power supply. Ripple and noise may be specified and measured in terms of its BMS or (preferably) peak-to-peak value.

Ripple and noise measurement can be made at any input AC line voltage combined with any DC output voltage and load current within rating.

5-16. The amount of ripple and noise that is present on the power supply output is measured either in terms of the RMS or (preferably) peak-to-peak value. The peak-to-peak measurement is particularly important for applications where noise spikes could be detrimental to a sensitive load, such as logic circuitry. The RMS measurement is not an ideal representation of the noise, since fairly high output noise spikes of short duration could be present in the ripple and not appreciably increase the RMS value.

5-17. The technique used to measure high frequency noise or "spikes" on the output of a power supply is more

5-4



	MODEL NO	RLIO		
	MODEL NU	HI RANGE	LO HANGE	
$\geq$	6826A	50	5	1 · · · ·
	6827A	200	20	
				• •

Figure 5-2. Ripple and Noise, Test Setup

critical than the low frequency ripple and noise measurement technique; therefore the former is discussed separately in Paragraph 5-25.

5-18 Ripple and Noise Measurements. Figure 5-2A shows an incofrect method of measuring p pripple. Note that a continuous ground loop exists from the third wire of the input power cord of the supply to the third wire of the input power cord of the oscilloscope via the grounded power supply case, the wire between the negative output terminal of the power supply and the vertical input of the scope, and the grounded scope case. Any ground current circulating in this loop as a result of the difference in potential EG between the two ground points causes an IR drop, which is in series with the scope input. This IR drop, normally having a 60Hz line frequency fundamental, plus any pickup on the unshielded leads interconnecting the power supply and scope, appears on the face of the CRT. The magnitude of this resulting noise signal can easily be much greater than the true ripple developed between the plus and minus output terminals of the power supply, and can completely invalidate the measurement.

5-19 The same ground current and pickup problems can exist if an RMS voltmeter is substituted in place of the osciltoscope in Figure 5-2. However, the oscilloscope display, unlike the true RMS meter reading, tells the observer immediately whether the fundamental period of the signal displayed is 8.3 milliseconds (1/120Hz) or 16.7 milliseconds. (1/60Hz). Since the fundamental ripple frequency present on the output of an HP supply is 120Hz (due to full-wave rectification), an oscilloscope display showing a 120Hz fundamental component is indicative of a "clean" measurement setup, while the presence of a 60Hz fundamental usually means that an improved setup will result in a more accurate (and lower) value of measured ripple.

5-20 Figure 5-28 shows a correct method of measuring the output ripple of a constant voltage power supply using a single-ended scope. The ground loop path is broken by floating the power supply. Note that to ensure that no potential difference exists between the supply and the oscilloscope it is recommended that whenever possible they both be plugged into the same ac power buss. If the same buss cannot be used, both ac grounds must be at earth ground potential.

5-21 Either a twisted pair or (preferably) a shielded twowire cable should be used to connect the output terminals of the power supply to the vertical input terminals of the scope. When using a twisted pair, care must be taken that one of the two wires is connected to the grounded input terminal of the oscilloscope. When using shielded two-wire, it is essential for the shield to be connected to ground at one end only so that no ground current will flow through this shield, thus inducing a noise signal in the shielded leads.

5-22 To verify that the oscilloscope is not displaying ripple that is induced in the leads or picked up from the grounds, the (+) scope lead should be shorted to the (--) " scope lead at the power supply terminals. The ripple value obtained when the leads are shorted should be subtracted from the actual ripple measurement.

5-23 In most cases, the single-ended scope method of Figure 5-2B will be adequate to eliminate non-real components of ripple and noise so that a satisfactory measurement may be obtained. However, in more stubborn cases it may be necessary to use a differential scope with floating input as shown in Figure 5-2C. If desired, two single conductor shielded cables may be substituted in place of the shielded two-wire cable with equal success. Because of its common mode rejection, a differential oscilloscope displays only the difference in signal between its two vertical input terminals, thus ignoring the effects of any common mode signal introduced because of the difference in the ac potential between the power supply case and scope case. Before using a differential input scope in this manner, however, it is imperative that the common mode rejection capability of the scope be verified by shorting together its two input leads at the power supply and observing the trace on the CRT. If this trace is not a straight line, then the scope is not rejecting the ground. signal and must be realigned in accordance with the manufacturer's instructions until proper common mode rejection is attained.

5 24 To check the ripple and noise output, proceed as follows:

a. Connect the oscilloscope or RMS voltmeter as shown in Figures 5-2B or 5-2C. Set MODE switch to POWER SUPPLY. Turn CURRENT control fully clockwise.

b. Adjust VOLTAGE control in the X1 and X10 ranges until front panel meter indicates maximum rated output voltage. Check both maximum rated positive and negative output voltages.

c. The observed ripple and noise should be less

 Model	X1 Range	X10 Range
682GA		6mVrms/35mVp-p
6827A	2,5mVrms/15mVp-p	10mVrms/50mVp-p

5-25 Noise Spike Measurement. When a high frequency spike measurement is being made, an instrument of sufficient bandwidth must be used; an oscilloscope with a bandwidth of 20MHz or more is adequate. Measuring noise with an instrument that has insufficient bandwidth may concealhigh frequency spikes detrimental to the load.

5-26. The test setups illustrated in Figures 5-2A and 5-2B are generally not acceptable for measuring spikes; a differential oscilloscope is necessary. Furthermore, the measurement concept of Figure 5-2C must be modified if accurate spike measurement is to be achieved:

a. As shown in Figure 5-3, two coax cables, must be substituted for the shielded two-wire cable.

b. Impedance matching resistors must be included to eliminate standing waves and cable ringing, and the cap acitors must be connected to block the DC current path.

c. The length by the test leads outside the coax is critical and must be kept as short as possible; the blocking capacitor and the impedance matching resistor should be connected directly from the inner conductor of the cable to the power supply terminals.

5.5

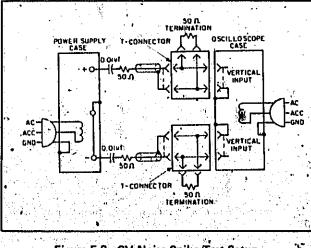


Figure 5-3. CV Noise Spike Test Setup

d. Notice that the shields of the power supply end of the two coax cables are not connected to the power supply ground, since such a connection would give rise to a ground current path through the coax shield, resulting in an erroneous measurement.

e. Since the impedance matching resistors constitute a 2-to-1 attenuator, the noise spikes observed on the oscilloscope should be less than:

Model No.	X1 Range	X10 Range
6826A	5mVp+p instead of 10mVp+p	17.5mVp-p instead of 35mVp-p
6827A	7.5mVp-p instead	25mVp-p instead of
	of 15mVp-p	50mVp•p

.

5-27

Transient Recovery Time.

Definition: The time "X" for the output voltage recovery to within "Y" millivolts of the nominal output voltage following a "Z" amp step change in load current, where: "Y" is specified as 50mV (6826A) or 100mV (6827A), the nominal output voltage is defined as the dc level between the static output voltage before and after the imposed load change, and "Z" is the specified load current change of the full load current rating of the supply.

5-28 Transient recovery time may be measured at any input line voltage combined with any output voltage and load current within rating.

5-29 Reasonable care must be taken in switching the load resistance on and off. A hand-operated switch in series with the load is not adequate, since the resulting one-shot displays are difficult to observe on most oscilloscopes, and

the arc energy occurring during switching action completely masks the display with a noise burst.

5-30 A mercury-wetted relay, as connected in the load switching circuit of Figure 5-4 should be used for loading and unloading the supply. When this load switch is connected to a 60Hz ac input, the mercury-wetted relay will open and close 60 times per second. Adjustment of the 25K control permits adjustment of the duty cycle of the load current switching and reduction in jitter of the oscilloscope display.

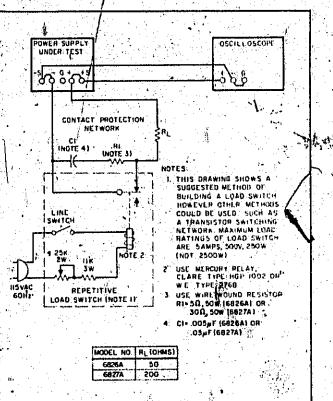


Figure 5-4. Transient Recovery Time Test Setup

5-31 To check the transient recovery time, proceed as follows:

a. Connect test setup shown in Figure 5-4. Set . MODE switch to POWER SUPPLY and RANGE switch to X10.

b. Turn CURRENT control fully clockwise.

c. Turn on supply and adjust VOLTAGE control clockwise until front panel ammeter indicates maximum positive rated output current.

d. Close line switch on repetitive load switch set, up.

e. Set oscilloscope for internal sync and lock on either positive or negative load transient spike.

f. Set vertical input of oscilloscope for ac coupling so that small do level changes in power supply output voltage

will not causy/display to shift.

g Adjust the vertical centering on the scope so that the tail ends of the no load and full load waveforms are symmetrically displayed about the horizontal center line of the oscilloscope. This center line now represents the noinal output voltage defined in the specification.

 h. Adjust the horizontal positioning control so that the trace starts at a point coincident with a major graticule division. This point is then representative of time zero.
 i. Increase the sweep rate so that a single transient spike can be examined in detail.

j. Adjust the sync controls separately for the positive and negative going transients so that not only the recovery waveshape but also as much as possible of the rise time of the transient is displayed.

k. Starting from the major graticule division represelftative of time zero, count to the right 100µsec and vertically 50mV for 6826A or 100mV for 6827A. Recovery should be within these tolerances as illustrated in Figure 5-5.

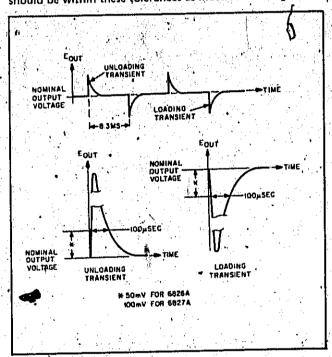


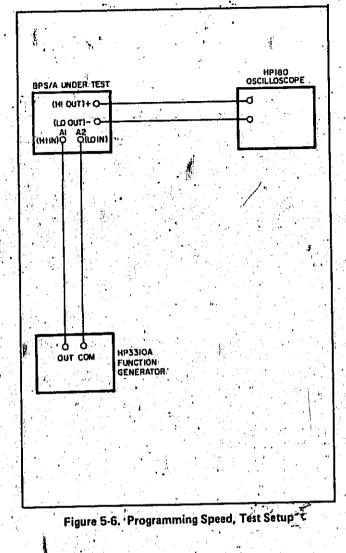
Figure 5-5. Transient Recovery Time, Waveforms

5-32 Programming Speed. To check the unit's programming speed, a square wave is applied to the unit and it is operated in the amplifier mode. This has the same effect as rapidly programming the unit, up and down, in the power supply mode. To make this test, proceed as follows:

- a. Connect test setup as shown in Figure 5-6.
- b. Set MODE switch to VAR GAIN AMPL,

RANGE switch to X10, and turn unit on.

- c. Rotate VOLTAGE control fully clockwise.
- d. On function generator, set input frequency to



about 100Hz squarewave and adjust amplitude to obtain maximum rated peak-to-peak output signal on oscilloscope (-50V to +50V on Model 6826A and -100V to +100V on Model 6827A).

e. Adjust oscilloscope to observe rise time of one squarewave. The waveshape should be within the tolerances shown on Figure 5-7 (output should change from maximum rated negative value to maximum rated positive value in less than 50usec).

f. Check the fall time of one squarewave. It should be almost identical to the rise time except for inversion.

5-33 Output Impedance. To check the output impedance, proceed as follows:

a. Connect test setup as shown in Figure 5-8.
 b. Set MODE switch to POWER SUPPLY, RANGE switch to X10, and turn unit on.

c. Adjust VOLTAGE control until front panel meter reads +50V for Model 6826A or +100V for Model 6827A.

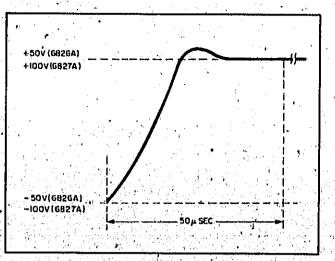


Figure 5-7. Typical Programming Speed Waveforms

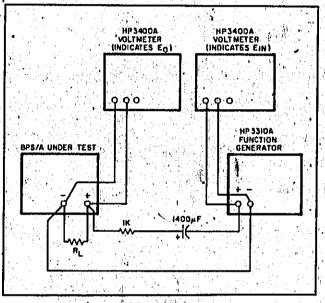


Figure 5-8. Output Impedance, Test Setup

4......

d. Set AMPLITUDE control on Oscillator to 10 volts (Ein), and FREQUENCY control to 100Hz sinewave. e. Record voltage across output terminals of the power supply (E<sub>0</sub>) as indicated on AC voltmeter:

f. Calculate the output impedance by the following formula:

# $Z_{out} = \frac{E_0R}{E_{in} - E_0}$

Eo = rms voltage across power supply output terminals.

R = 1000 E<sub>in</sub> = 10 volts

g. The output impedance should be less than: 6826A: 1 milliohm 6827A: 2 milliohms

5-8

### Temperature Coefficient.

5-34

Definition: The change in output voltage per degree Centigrade change in the ambient temperature under conditions of constant input ac line voltage, output voltage setting, and load resistance.

5.35 The temperature coefficient of apower supply is measured by placing the power supply in an oven and varying it over any temperature span within its rating. (Most HP power supplies are rated for operation from 0°C to 55°C.) The power supply must be allowed to thermally stabilize for a sufficient period of time at each measurement temperature.

5-36 The temperature coefficient given in the specifications is the maximum-temperature-dependent output voltage change which will result over any one degree Centigrade interval. The differential voltmeter or digital voltmeter used to measure the output voltage change of the supply should be placed outside the oven and should have a long term stability adequate to insure that its drift will not affect the overall measurement accuracy.

5-37 To check the temperature coefficient, proceed as follows:

a. Connect load resistance (high range) and differential voltmeter as illustrated in Figure 5-1.

### NOTE

Connect voltmeter to ±\$ terminals, NOT across load.

b. Set MODE switch to POWER SUPPLY and 'RANGE switch to X10. Turn CURRENT control fully clockwise.

c., Adjust front panel VOLTAGE control until front panel voltmeter indicates maximum rated output voltage.

d. Place power supply in temperature controlled oven (differential voltmeter and load remains outside oven). Set temperature to 30°C and allow 30 minutes for stabilization.

e. Record differential voltmeter reading.

f. Raise temperature to 40°C and allow 30 minutes for stabilization.

g. Observe differential voltmeter reading. Difference in voltage reading between step. (e) and (g) should be less than the following:

6826A:		80mV
6827A:		160mV

h. Repeat steps (a) through (g) with low range (X1) load resistance connected as shown in Figure 5-1. Set RANGE switch to X1. i. Observe differential voltmeter readings. Difference in voltage reading between step (e) and (g) should be less than the following:

6826A: 8.5mV 6827A: 17mV

Drift (Output Stability).

5-38

Definition: The change in output voltage for the first eight hours following a 30minute warm-up period. During the interval of measurement all parameters, such as load resistance, ambient temperature, and input line voltage are held constant.

This measurement is made by monitoring the out-5/39 put of the power supply on a differential voltmeter or digital voltmeter over the stated measurement interval; a strip chart recorder can be used to provide a permanent record. A thermometer should be placed near the supply to verify that the ambient temperature remains constant during the period of measurement. The supply should be put in a location limmune from stray air currents (open doors or windows, air conditioning vents); if possible, the supply should be placed in an oven which is held at a constant temperature. Care must be taken that the measuring instrument has a stability over the eight hour interval which is at least an order of magnitude better than the stability specification of the power supply being measured. The supply will drift considerably less over the eight hour measurement interval than during the half-hour warm-up.

5-40 To check the output stability, proceed as follows: a. Connect load resistance (high range) and differential voltmeter as illustrated in Figure 5-1.

b. Set MODE switch to POWER SUPPLY and RANGE switch to X10. Turn CURRENT control fully clockwise.

c. Adjust front panel VOLTAGE control clockwise until differential voltmeter indicates maximum rated output voltage.

d. Allow 30 minutes warm-up, then record differential voltmeter reading.

e. After 8 hours, differential voltmeter should change from reading recorded in step (d) by less than the following:

6826A: 25mV (pot wiper jump effect may add 50mV) 6827A: 50mV (pot wiper jump effect may add 100mV) f, Repeat steps (a) through (e) with low range (X1)

load resistance connected as shown in Figure 5.1. Set RANGE switch to X1.

g: Observe differential voltmeter reading. Difference in voltage reading between step (d) and (e) should be less than:

6826A: 2.5mV (pot wiper jump effect may add 5mV) 6827A: 5.0mV (pot wiper jump effect may add 5mV)

### NOTE

If remote programming is employed, the potentiometer wiper jumper effect is eliminated.

### 5-42 CONSTANT CURRENT TESTS

5-43 The instruments, methods, and precautions for the proper measurement of constant current power supply characteristics are for the most part identical to those already described for the measurement of constant voltage power supplies. There are, however, two main differences: First, the power supply performance will be checked between short circuit and full ic ad rather than open eircuit and full load. Second, a current monitoring resistor is inserted between the output of the power supply and the load.

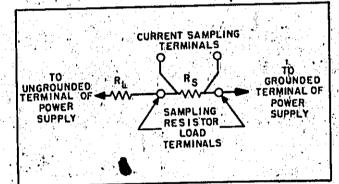


Figure 5.9. Current Sampling Resistor Connections

5-44 For all output current measurements the current sampling resistor must be treated as a four terminal device. In the manner of a meter shunt, the load current is fed to the extremes of the wire leading to the resistor while the sampling terminals are located as close as possible to the resistance portion itself (see Figure 5-9). Generally, any current sampling resistor should be of the low noise, low temperature coefficient (less than 20ppm/°C) type and should be used at no more than 10% of its rated power so that its temperature rise will be minimized.

### NOTE

The CURRENT MODE light should be on during these tests.

5-45 Rated Output and Meter Accuracy.

a. Connect test setup shown in Figure 5-10. Use high range load resistor (RL) connected in series with the  $1\Omega$  resistor (RS).

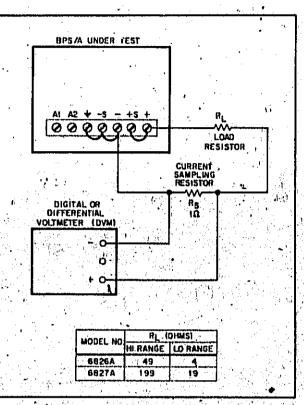


Figure 5-10. Constant Current, Test Setup

b. Set BPS/A front panel	controls as follows:
MODE switch:	POWER SUPPLY
RANGE switch:	X10
VOLTAGE control:	fully clockwise
CURRENT control:	fully counterclockwise
VOLTAGE METER:	high range DC
CURRENT METER:	high range DC
c. Turn on BPS/A and ac	

until front panel ammeter indicates maximum rated positive output current; +1.0A (6826A) or +0.5A (6827A).

d,	DVM should	read as	follows:	· ·
	6826A:			+1.0V
,	6827A:		·	+0.5V

e. Turn VOLTAGE control fully counterclockwise and adjust CURRENT control until front panel ammeter indicates maximum rated negative output current, -1.0A (6826A) or -0.5A (6827A).

. DVM	l should i	read as	follows		
6826	Δ:		•	<u>1</u> .	ויייג <b>ע</b> ניין.
e e e terre	a tanàna amin'ny kaodim-paositra dia mampika mandritra dia kaominina dia kaominina dia kaominina dia kaominina Ny INSEE dia mampika dia kaominina dia kaominina dia kaominina dia kaominina dia kaominina dia kaominina dia kao			-0.	
6827	A: your	a 14		្រ—បររ	3 V-

Source Effect (Line Regulation). Definition: The change  $\Delta I_{OUT}$  in the static value of dc output current resulting from a change in ac input voltage over the specified range from low line (usually 104 volts) to high line (usually 127 volts), or from high line to low line.

546

To check the line regulation, proceed as follows: a. Utilize test setup and front panel settings of Paragraph 5-45.

b. Connect variable auto transformer between in put power source and power supply power input.

.c. Adjust auto transformer for 104Vac input.

d. Turn VOLTAGE control fully clockwise.

e. Adjust CURRENT control until front panel ammeter reads exactly maximum rated positive output current. f. Read and record voltage indicated on differential voltmeter.

g. Adjust variable auto transformer for 127Vac

h. Reading on differential voltmeter should not vary from reading recorded in step (f) by more than the following:

> 6826A: 6827A: 1001 TACE

i. Turn VOLTAGE control fully counterclockwise and repeat steps (e) through (h) for negative output current.

5-47 - Load Effect (Load Regulation). Definition: The change △IOUT in the static value of the dc output current result. Ing from a change in load resistance from short circuit to a value which yields maximum rated output voltage.

5-48 To check the constant current load regulation, pro-

a. Utilize test'setup and front panel settings of Paragraph 5-45.

b. Turn VOLTAGE control fully clockwise.

 c. Adjust CURBENT control until front panel meter reads exactly maximum rated positive output voltage.
 d. Read and record voltage indicated on differential voltmeter.

e. Short circuit load resistor (RL).

 f. Reading on differential voltmeter should not vary from reading recorded in step (d) by more than the following;

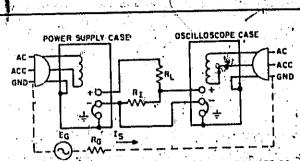
6826A: 4350μV 6827A: g, Turn VOLTAGE control fully counterclockwise and repeat steps (c) through (f) for negative output

voltage,

Ripple and Noise. Definition: The residual ac current which is superimposed on the dc output current of a regulated power supply. Ripple and noise may be specified and measured in terms of its RMS or (preferably) peak topeak value.

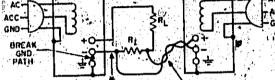
5-49

90



A. INCORRECT METHOD - GROUND CURHENT IS PRODUCES SP CYCLE DROP IN NEGATIVE LEAD WHICH, ADDS TO THE POPER SURPLY, RIPPLE DISPLAYED ON SCOPE

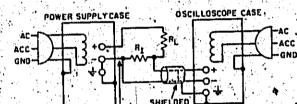




TWISTED PAIR

M LENGTH OF LEAD BETWEEN RI AND OUTPUT' TERMINAL OF POWER SUPPLY MUST BE HELD TO ABSOLUTE

8. A CORRECT METHOD USING A SINGLE - ENDED SCOPE. Output Floated to Break Ground Current Loop, twisted Pair Reduces Stray Pickup on Scope: Leads



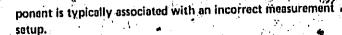
H LENGTH OF LEAD BETWEEN RE AND GROUNDED DUTPUT TERMINAL OF POWER SUPPLY MUST BE HELD TO ABSOLUTE MINIMUM

C. A CORRECT METHOD USING A DIFFERENTIAL SCOPE WITH FLOATING INPUT. GROUND CURRENT PATH IS BROKEN; COMMON MODE REJECTION OF DIFFERENTIAL INPUT SCOPE IGNORES DIFFERENCE IN GROUND POTENTIAL OF POWER SUPPLY & SCOPE, SHIELDED TWO-WIRE FURTHER REDUCES. STRAY PICKUP ON SCOPE LEAD.

MODEL NO.	RL	RI	
6826A	49Ω°	10,0,1%	
6827A	199	-1Ω,0.1%	

Figure 5-11. CC Ripple and Noise Test Setup

5-50 Most of the instructions pertaining to the ground loop and pickup problems associated with constant voltage ripple and noise measurements also apply to the measurement of constant current ripple and noise. Figures 5-11 and 5-12 illustrate the most important precautions to be observed when measuring the ripple and noise of a constant current supply. The presence of a 120Hz waveform on the oscilloscope is normally indicative of a correct measurement method. A waveshape having 60Hz as its fundamental com-



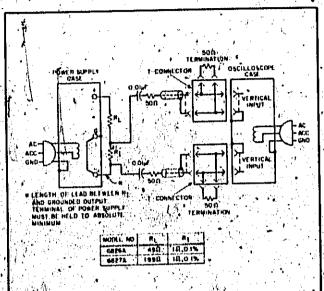


Figure 5-12. Constant Current Noise Spike Test Setup

5-51 Ripple Measurement. To check the output ripple, proceed as follows:

a. Connect the oscilloscope as shown in Figures 5-11B or 511C.

b. Rotate the VOLTAGE control fully cw. c. Set RANGE switch to X10, MODE switch to

POWER SUPPLY and turn on BPS/A.

d. Adjust CURRENT control until front panel meter reads exactly the maximum rated positive output current.

e. The observed ripple should be less than; 6826A: 5mV p-p '

6827A: 5mV p·p

f, Turn VOLTAGE control fully counterclockwise and repeat steps (d) and (e) for maximum rated negative output current.

5-52 Noise Spike Measurement. To check the noise spike output, proceed as follows:

a. Connect test setup shown in Figure 5-12.

b. Turn VOLTAGE control fully clockwise.

c. Set RANGE switch to X10, MODE switch to POWER SUPPLY, and turn on BPS/A.

d. Adjust CURRENT control until front panel ammeter indicates the exact maximum rated positive output current.

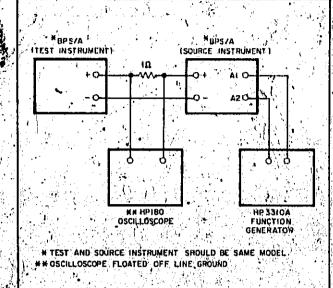
e. Since the impedance matching resistors constitute a 2:1 divider, the observed noise spikes should be less than:

6826	Δ'	d here's	• • •	2.5mV	р <b>.</b> р.
			16 d		
. <u>}</u> ∈ 6827	<b>A:</b>			2.5mv	р-р

5-53 Current Sink Test. The current sink test is performed using two BPS/A's. One is used as a set instrument and the other is used as a source instrument. Two identical BPS/A's are preferred to perform this test.

## CAUTION

If two BPS/A's of the same model are not availed able, this test can be performed utilizing any other Bipolar supply. However, it is of the utmost importance that the BPS/A output voltage be set below the other supply so that it will sink rather than force the other Supply to sink which it may not be capable of doing.





To check the current sink performance of the BPS/A, proceed as follows: a. On the test instrument, set controls as follows:

MODE switch:	POWER SUPPLY
RANGE switch:	: X10
VOLTAGE control:	fully.clockwise
CURRENT control:	fully clockwise
b. On the source instrum	ient, set controls as 🕐

FXD GAIN AMP MODE switch: X10 **RANGE** switch:

follows:

c. Turn on test instrument and set output to: +50V 6826A: 15 6827Å: +100V/

d. Connect function generator to terminals A1 and A2 of source instrument. Turn on and adjust source instrument output as follows:

100V, p-p at 100Hz (approximately) 6826A:

6827A: 200V p-p at 100Hz (approximately)\* .e. Turn off test and source instruments and connect test setup of Figure 5-13.

If Turn on both instruments simultaneously and observe that waveform sampled across the 1 ohm resistor. is as illustrated in Figure 5-14.

g. Repeat test with VOLTAGE control on test instrument set fully counterclockwise. Waveform should be same as Figure 5-14.

Overcurrent Protection Test. To check the over-5-54 current protect circuit, proceed as follows:

> a. Set BPS/A front panel controls as follows: **MODE** switch: FXD GAIN AMP **RANGE** switch: •X10⇒ VOLTAGE control: fully clockwise CURRENT control: •fully clockwise

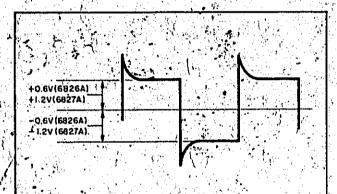


Figure 5-14. Current Sink Test Waveform

b., Apply a 5V p-p, 100Hz squarewave to the A1 · (HI IN) and A2 (LO IN) terminals;

c. Connect a  $1\Omega_{15}$  5W resistor across + (HI OUT) and - (LO OUT) terminals. Connect oscilloscope across  $1\Omega$  resistor.

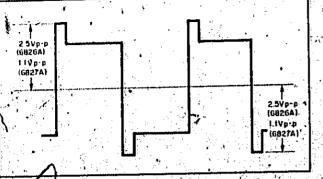
d. Turn on BPS/A and observe waveforms (see Figure 5-15). Overshoot should not exceed: 2.5V p-p 6826A: 6827A:

1.1V p-p

5-65 Turn-on/off Transient Protect. To test the turn-on /off transient protect circuit proceed as follows:

a. Set front panel controls as follows: POWER SUPPLY MODE switch: X10 RANGE switch: VOLTAGE control: fully clockwise fully clockwise CURRENT control:

21



Pigure 5-15: Overcurrent Protect Test-Waveform

ground c. Turn on BPS/A. Output should be from 0 to 1.5Vdc.

d. Remove clip lead, output should by 50V (6826A) or +100V (6827A).

e. Repeat steps (a) through (d) except turn VOLT-AGE control fully ccw for -50V (6826A) or -100V (6827A) / output.

## 5-56 AMPLIFIER MODE TESTS

5-57 Gain and Meter Accuracy Test. To check gain and the meter accuracy in the amplifier modes, proceed as follows:

a. Connect the test setup as shown in Figure 5-16. Use the appropriate low range load resistor (RL).

<ol> <li>Set BPS/A front panel</li> </ol>	controls as follows;
MQDE switch:	VAR GAIN AMP
RANGE switch:	X1
VOLTAGE control:	fully clockwise
CURRENT control:	fully clockwise
VOLTAGE METER:	low range AC
CURRENT METER:	high range AC
Set constator frequer	cy at 100Hz and output

at 5Vac peak-to-peak.

d. Turn on BPS/A and allow a five minute warmup period.

e. Connect oscilloscope to +S and -S terminals. f. Adjust VOLTAGE control to obtain a 10V p-p (6826A) or 20V p-p (6827A) reading on the oscilloscope.

g. Observe that front panel voltmeter reads 3.5V. rms (6826A) or 7V rms (6827A) and the front panel ammeter reads. 7A rms (6826A) or .35A rms (6827A). h. Turn off BPS/A and connect appropriate high range load resistor. Set RANGE switch to X10 and VOLT-AGE METER switch to high range AC.

I. Turn on BPS/A and observe oscilloscope for a 100V p-p (6826A) or 200V p-p (6827A) signal.

•j. Observe that front panel voltmater reads 35V mis (6826A) or 70V rms (6827A) and front panel ammeter reads. 7A rms (6826A) or .35A rms (6827A).

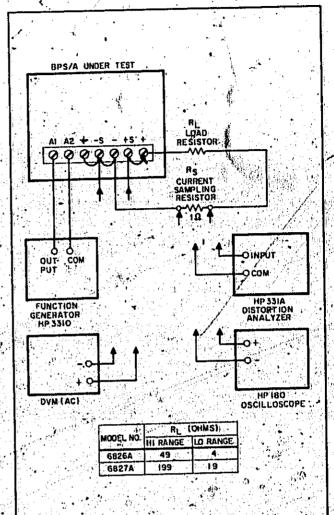


Figure 5-16. Amplifier Mode Test Setup

k. Set MODE switch to FXD GAIN AMP MODE and increase generator output to 10V p-p.

I. Observe a 100V p-p (6826A) or 200V p-p (6827A) signal on oscilloscope.

5-58 Frequency Response. To check amplifier mode frequency response, proceed as follows:

a. Connect the test setup as shown in Figure 5-16. b. Set MODE switch to VAR GAIN AMP and set VOLTAGE and CURRENT controls fully clockwise. c. Set HP3310A generator output at 100Hz sinewave and adjust signal amplitude to provide 100V-p-p

(6826A) or 200V p-p (6827A) output. d. Adjust the generator frequency until output drops to 71V p-p (6826A) or 142V p-p (6827A). This frequency should not be less than 15kHz.

e. Set MODE switch to FXD GAIN AMP and repeat steps (c) and (d) above. Frequency should not be less than: 6826A: 35kHz 6827A: 25kHz

5-59 Distortion Test. To check the total harmonic distortion (THD) in the amplifier output, proceed as follows: a. Connect the test setup as shown in Figure 5-16.

b. Set MODE switch to VAR GAIN AMP

c. Set generator at 100Hz sinewave and adjust output for full BPS/A output voltage and current with appropriate load.

d. Measure the distortion at the output using HP 331A Distortion Analyzer.

e. -The THD should be less than .1%

## NOTE

The above is a difficult measurement because the THD is so low. Most audio generators will contain more than .1% THD in their output. A first order figure can be obtained by the following relationship:

THD of Amplifier HD of (gen.+amp) - THD gen.

## 5-60 TROUBLESHOOTING

## WARNING

The following troubleshooting procedures are performed with power applied to the, BPS/A while its protective covers are removed. Be careful when performing the procedures as line voltage is always present on the power input connector, fuse holder, and in-the power supply rectifier circuits. In addition, when the supply is on, energy available at-many points, particularly the power transistors on the rear heat sink, may result in personal injury or death when contacted.

## 5-61 GENERAL

5-62 Before attempting to troubleshoot this instrument, ensure that the fault is with the instrument and not with an associated circulat. The performance test (Paragraph 5-5) enables this to be determined withput removing the instrument's covers. A good understanding of the principles of operation is a helpful aid in troubleshooting, and it is recommended that the reader review Section IV of the manual before attempting to troubleshoot-the instrument. Once the principles of operation are understood, refer to the trouble solation procedures.

5.14

5-63 Figure 7-1 is a simplified schematic of the BPS/A and is useful in tracing signal flow through the entire instrument. Figures 7-2 and 7-3 are detailed schematics (2 sheets each) of the 6826A and 6827A instruments respectively. The circled test point numbers in Figures 7-2 and 7-3 are also marked on the component location diagrams which accompany the schematics. References are made to these test points in the troubleshooting procedures.

## 5-64 OVERALL TROUBLE ISOLATION PROCEDURE

5-65 Figure 5=17 illustrates the overall scheme of the trouble isolation and troubleshooting procedures which follow. The trouble isolation procedures represented by the boxes in the left-han solumn are intended to localize a problem to a particular area, both by direct testing and a process of elimination. Instructions at each stage of the iso-alation procedure direct you to the appropriate troubleshoot-ing instructions, if required. These steps must be followed in the order in which they are given so that circuits are operational that are needed for testing other circuits. It is not necessary to make any calibration adjustments until trouble shooting has been completed. At that time, any necessary is adjustments should be completed.

#### -CAUTION

Trouble isolation by swapping a good board for a suspected faulty one is <u>not</u> recommended unless it is certain that the fault is not destructive.

5-66 Preliminary Trouble Isolation Checks. Make the following checks for obvious troubles before continuing with the troubleshooting procedures.

1. Check that the rear terminal strapping is correct for local or remote programming [see Section 1]]).

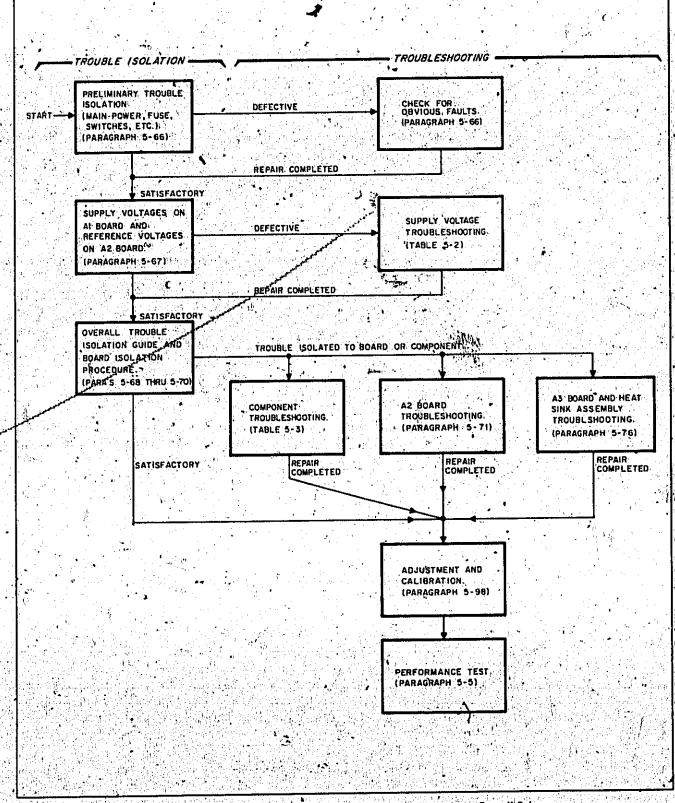
#### - CAUTION

The rear terminals must be strapped correctly before power is applied to the instrument.

2. Ensure that the MODE and RANGE switches are in the desired position.

 3. Check the line fuse. If the line fuse is open; proceed as follows:

> a. Ensure that the proper ac input (115 or 230Vac) is selected (slide switch on power, module) and install a fuse of proper rating; 2A for 115Vac or 1A for 230Vac.



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Figure 5-17. Trouble Isolation and Troubleshooting Procedura, Overall Scheme

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components.

b. Check the following:

- On chassis check for short circuits Main power transformer T1 and filter capacitors C1, C2 (6826A) or C1, C2, C3 (6827A).
- On board A1 check for short circuits All filter capacitors and rectifier diodes, Also, check for shorts across power tracks on board:

On board A3 check for short or open circuits -

6826A: A3C11, Q12-Q19 (shorted) A3CR14-CR17 (opened) 6827A: A3C18, Q9-Q12 (shorted) A3CR12, CR15 (opened) On heat sink assembly — Check output power transistors.

4. Check that the LOCAL/AUTO switch on board A2 is in the LOCAL position (see Figure 3.9). For normal operation of the BPS/A, this switch must be left in the LOCAL position. The AUTO position is used only for autoseries, auto-parallel operation (see Section 111).

 Check continuity of ribboh cables W1 and W2 from the A1 board to the heat sink assembly and rear terminal strips respectively.

# 6. Check for defective meter(s), power cord, and loosely connected circuit boards. Visually inspect circuit boards for mechanical damage and discolored or charred 7. If steps (1) through (6) have not isolated the trouble, check the supply voltages (Paragraph 5-67).

5-67 Supply Voltage Checks. In almost all cases, the trouble can be caused by an incorrect supply voltage (main, bias, or reference voltage); thus, it is a good practice to check these voltages (see Table 5-2). Although isolation of the trouble source to a particular board is desireable, possible trouble in one of the internal power sources should be investigated first. The tests described in Table 5-2 constitute a relatively fast check for trouble in this area. In many, cases, these checks can save many hours of troubleshooting circuits which are actually operating properly. If the supply voltage checks have not isolated the trouble, proceed according to the overall trouble isolation guide. (Paragraph 5-68).

#### NOTE

There are two separate supply voltage returns in the BPS/A designated ① and ② in addition to chassis ground <del>↓</del> When making voltage or waveform measurements, be sure to use the appropriate return. The DVM or oscilloscope used must have a floating input since the ① and ② returns are <u>not</u> at chassis ground.

			Voltages

- METER COMMON	METER POSITIVE	NORMAL READING	CHECK IF NOT CORRECT		
	Main Supply Voltages	6826A 6827A	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
Ū	TP1	+65 ± 3Vdc +140 ± 7Vdc	Á1C7, C9, C10, CR12, CR13, R5		
TP2	Ū	-65 ± 3Vdc -140 ± 7Vdc	A1C11, C12, C15, CR14, CR15, R6		
	TP3	+80 ± 4Vdc +155 ± 8Vdc	A1C5, C6, C8, CR10, CR11		
TP4	Ū		A1C13, C14, C16, CR16, CR17		
	Bias Supply Voltages				
2	, TP5	+15 ± .8Vdc	A1C3, C17, CR3, CR5, CR6, O1, O2, VR1		
TP6	. 3	-15 ± .8Vdc	A1C4, C18, CR7, CR8, O3, O4, VR3.		
ୖୖୖୖୖ	TP7	+20 ± 2Vdc (unfil)	A1CR1, CR2, 37, 77, 77		
	Reference Voltages				
<b>S</b>	TP9	+6.2 ± .35Vdc	A2VR4		
TP10	<b>S</b>	-6.2 ± .35Vdc •	A2VR3		
-OUT	• TP11	+6.2 ± .35Vdc	·A2VR2		
TP12	-OUT	-6.2 ± .35Vdc	A2VR1		

5-68 Overall Trouble Isolation Guide. After checking the supply voltages, disconnect the load and examine Table 5-3. This table contains a list of symptons and probable causes that may cut down on troubleshooting time. For each trouble symptom, Table 5-3 isolates the trouble to a component or group of components or directs the reader to additional procedures if further isolation of the trouble is necessary.

5-69 In general, if the BPS/A operates properly in the power supply mode, it should also operate properly in the amplifier mode (variable gain or fixed gain amplifier mode). The trouble symptoms listed in Table 5-3 isolate the trouble to defective components or groups of components (functional circuit areas). The voltage control stages on board A2 in conjunction with the output power amplifier stages on board A3 and the heat sink assembly provide the desired

output voltage/gain. The voltage control stages A2U1 and A2U2 are common to both positive and negative outputs. The bipolar amplifier circuits on board A3 and the bipolar series regulator/output amplifier stages on the heat sink assembly consist of positive and negative stages for positive and negative outputs respectively. The current control circuits consist of positive current comparison stage (A2U3) and negative current comparison stage (A2U4) and associated common circuits consisting of dual ganged CURRENT control A5R1, speedup network (A2Q1, C9), and current sampling resistor A2R27. The CURRENT MODE indicator A5DS2 lights and a FLAG indication is present (high level at terminal A17) when the BPS/A is in constant current operation. During constant current operation, stages A2Q5 or A206 provide the proper level to control the CURRENT MODE lamp driver (A202, 03) and FLAG output driver. (A2Q4) stages for positive or negative output current respectively.

SYMPTOM	PROBABLE CAUSE
No output voltage (All modes: POWER SUPPLY, VAR GAIN AMP, FXD GAIN AMP)	<ul> <li>a. Fuse blown or incorrect rear terminal strip strapping, etc. (see Paragraph 5-66).</li> <li>b. Main, bias, or reference voltages defective (see Paragraph 5-67).</li> <li>c. Relay A1K1, circuit board (A2 or A3), or output power transistors on heat sink assembly defective (see Paragraph 5-70).</li> </ul>
Zero or low putput voltage (POWER SUPPLY mode only)	a. MODE switch defective b. Internal positive dc reference defective (A2C10, R3, R58, R59)
Zero or low output voltage (POWER SUPPLY and VAR GAIN AMP modes only).	<ul> <li>a. Voltage reference/gain control amplifier stage A2U2 defective (see Paragraph 5-71).</li> <li>b. VOLTAGE control A5R2 defective.</li> </ul>
No output (VAR GAIN AMP and FXD GAIN AMP modes only).	<ul> <li>a. MODE switch not in proper position.</li> <li>b. Improper connections to rear terminals A1 and A2 or front panel terminals HI IN and LO IN.</li> </ul>
Output voltage correct in X10 range, but incorrect in X1 range or vice versa.	a. RANGE switch defective. b. Relays A2K2 and/or A2K3 defective.
Negative output normal but zero or low positive output.	<ul> <li>a. Check the main positive supply voltages (see Table 5-2): +65V, +80V for 6826A or +140V, +155V for 6827A.</li> <li>b. Positive turn on/off circuit defective (A2Q2 shorted).</li> <li>c. Defective positive output power trapsistor stage on heat sink assembly: Q1, Q2 (6826A) or Q1-Q3 (6827A).</li> <li>d. Defective positive coupling amplifier or driver stages on A3 board. 6826A: , A3Q6, Q7, Q8 (opened), VR2 (shorted) 6827A: A3Q11, Q12 (opened), VR5, VR6 (shorted)</li> </ul>
Output voltage latched to maxi- mum positive	<ul> <li>a. Amplifier stage on A3 board defective: A3Q6 shorted (6826A) or A3Q8 opened (6827A).</li> <li>b. Positive current comparison amplifier output diode (A2CR14) shorted.</li> </ul>

Table 5-3. Overall Trouble Isolation Guide

SYMPTOM	PROBABLE CAUSE
Positive output normal, but zero or low negative output.	a. Check the main negative supply voltages (see Table 5-2): -65V, -80V for 6826A or -140V, -155V for 6827A.
	b. Negative turn on/off circuit defective (A2Q3 shorted).
	c. Defective negative output power transistor on heat sink assembly: Q3, Q4 (6826A) or Q4, Q5, Q6 (6827A).
	<ul> <li>d. Defective negative coupling amplifier or driver stages on A3 board:</li> <li>6826A; A309, 010, 011 (opened), VR1 (shorted)</li> <li>6827A; A309, 010 (opened), VR7, VR8 (shorted)</li> </ul>
Output voltage latched to maximum negative.	a. Amplifier stage on A3 board defective. A3Q11 shorted (6826A) or A3Q8 shorted (6827A).
	<ul> <li>b. Negative current comparison amplifier output diode (A2CR13) shorted.</li> </ul>
No constant current operation.	<ul> <li>a. Check reference voltages at TP11 and TP12 and bias voltages at TP5 and TP (see Table 5-2).</li> </ul>
	<ul> <li>b. Check circuit components common to positive (A2U3) and negative (A2U4 comparison amplifiers:</li> <li>Dual ganged CURRENT control – A5R2</li> <li>Speed up network – A2C9, Q1, R27, R46, R47</li> </ul>
No positive constant current op-	a. Check positive reference voltage at TP11 (see Table 5-2).
eration (negative constant current circuits operate properly).	b. Positive current comparison amplifier A2U3 defective (see Paragraph 5-71). c. A2CR5, CR14, or VR5 defective.
No negative constant current '	a. Check negative reference voltage at TP1-2 (see Table 5-2).
operation (positive constant cur- rent circuits operate properly).	b. Negative current comparison amplifier A2U4 defective (see Paragraph 5-71) c. A2CR13 or VR6 defective.
Positive constant current circuits operate properly but CURRENT MODE indicator does not light.	PNP switch A2Q5 opened.
Negative constant current cir-	NPN switch A2Q6 opened.
cuits operate properly but CURRENT MODE indicator does not light.	
CURRENT MODE Indicator always on (FLAG output low).	Defective lamp driver circuit: A2Q2 opened or A2Q3 shorted.
FLAG output (terminal A17) always high (about +16V).	FLAG driver A2Q4 opened.
CURRENT MODE indicator always on and FLAG output always high.	Diode A2CR14 opened.
Genstant current circuits operate normally but CURRENT MODE indicator does not light.	a. Indicator (LED) A5DS2 defective. b. Defective lamp driver circuit: A2O2 shorted or A2O3 opened.

# Table 5-3- Overall Trouble Isolation Guide (Continued)

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Table 5-3.	<b>Overall Trou</b>	uble Isolation	Guide	(Continued)	

SYMPTOM	PROBABLE CAUSE
Constant current circuits operate normally but no FLAG output (always low).	b. Jumper A2W1 not installed.
Positive current sink inoperative.	Defective A2CR3, CR11, CR12, R28 (6826A) or A2CR3, CR11, CR12, R23, " R28 (6827A).
Negative current sink inopera-	Defective A2CR4, CR7, CR8, R29 (6826A) or A2CR4, CR7, CR8, R26, R29 (6827A).
Bandwidth too narrow in VAR GAIN AMP mode.	A2U1 defective.

5-70 Board Isolation Procedure. The board isolation procedure describes how to isolate trouble to the turn on/off circuit on boards A1 and A3, the voltage/current control circuits on board A2, or to the output power amplifier stages on board A3 and the heat sink assembly. The board isolation procedures assumes that an output problem exists in all three modes of operation and all trouble isolation procedures up to this point have been completed. To isolate the trouble to the defective board(s), proceed as follows:

## WARNING

The following troubleshooting procedures are performed with power applied to the BPS/A while its protective covers are removed. Be careful when performing the procedures as line voltage is always present on the power input connector, fuse holder, and in the power supply rectifier circuits. In addition, when the supply is on, energy available at many points, particularly the power transistors on the rear heat sink, may result in personal injury or death when contacted.

a. Remove covers and A3 board from the instru-

 B. Remove load and connect a DVM to the +S and -S rear terminals.

ment.

c. Set controls on fro	ont panel as follows:
MODE switch:	POWER SUPPLY
RANGE switch:	. X10
VOLTAGE contro	
CURRENT contro	d: fully clockwise;

## VOLTAGE METER: high range DC CURRENT METER: high range DC d. Turn on power and observe that LINE indica-

tor lights: e. Check that turn on/off relay A1K1 is operating properly by connecting ohmmeter between A1K1 pin 1 and 20 Ohmmeter should indicate an open circuit. If a short circuit (zero ohms) is present, check relay A1K1 and associated components (A1C2, CR4, R32, R37). If open circuit is present, proceed to step (f).

f. Turn off power and isolate the turn on/off circuit on board A3 by lifting the connections from diodes A3CR3 and A3CR4 to the collectors of transistors A3Q2 and A3Q3 respectively. Install A3 board in Instrument.

g. Turn on power. If output voltage is normal (max. positive), the turn on/off circuit (A3Q1-A3Q5) is defective. If output is zero or low, proceed to step (h).

h. Turn off power and reconnect diodes A3CR3 and A3CR4. Connect –5Vdc to the A1 (HI IN) and A2 (LO IN) terminals.

i. Set MODE switch to FXD GAIN AMP position and turn on power. If output voltage is normal (max. positive), the voltage/gain reference stage A2U2 is probably defective (see Paragraph 5-71). If output is zero, proceed to step (j).

j. Turn power off. Remove the A2 board. Connect a variable dc voltage source (-2.5V to -4.5V) between A3 pin 5 and (2) . Connect negative potential to A3 pin 5.

k. Turn on power and vary the negative source voltage from -2.5V to -4.5V. Output voltage should vary accordingly from maximum positive to maximum negative value through zero. Thoutput voltage is normal, the A2 board is defective (see Paragraph 5-71). If output is not normal, the A3 board or output power transistor stages on heat sink assembly are defective (see Paragraph 5-76).

## A2 BOARD TROUBLESHOOTING

#### NOTE

For normal operation of the BPS/A, the Local/ Auto switch A2S1 <u>must</u> be in the "Local" position (see Figure 3-9). The "Auto" position is used only for auto-series, auto-parallel, or autotracking operation (see Section III). If this switch is left in the "Auto" position for normal operation, the output will be latched at full positive or negative depending on other control settings.

5-72 The A2 plug-in board contains the voltage and current control circuits which can be separated functionally allowing the trouble to be isolated to the individual circuit level. The following paragraphs provide troubleshooting procedures for the voltage control, current control, and RMS current meter driver circuits located on board A2.

5-73 Voltage Control Circuits. Integrated circuit amplifies A2U1 (voltage comparison amplifier) and A2U2 (voltage reference/gain control amplifier) with their circuit components are part of the constant voltage feedback loop. The following procedure consists of a series of fast checks to isolate trouble in these circuits.

a. Remove top and right side covers from instrument. Remove the A3 board.

b. Ensure that rear terminal strip is strapped correctly for local operation (see Section 111).

. Set front panel contro	ols as follows:
MODE switch:	POWER SUPPLY
RANGE switch:	X1
VOLTAGE control:	fully counterclockwise
CURRENT control:	fully clockwise
Connect a DVM betw	een A2U2 pin 6 (TP14)

and -S. e. Turn VOLTAGE control through its range and observe that DVM reading varies from 0 to -10V. If volt-

age reading is correct, proceed to step (f). If the output at pin 6 is  $\pm 15V$ , check A2CR1, CR2, U2 for short circuits. If the output at pin 6 is zero, VOLTAGE control A5R2 is open or defective, or A2U2 is defective.

f. Set VOLTAGE control for reading of -5V on DVM.

g. Set MODE switch to VAR GAIN AMP positive and connect oscilloscope between A2U1 and -S.

h. Apply a 100Hz sinewave (about 40mV p-p) to the HI IN (A1) and LO IN (A2) terminals. If A2U1 is operational, a sinewave (approximately 8V p-p) should be observed on oscilloscope. If there is no output, A2U1 or A2K1 is defective. If the output at A2U1 pin 6 is ±15Vdc, A2CR18, CR19, or A2U1 is shorted. 5-74 Current Control Circuits. Integrated circuit amplifiers A2U3 (positive current comparison amplifier) and A2U4 (negative current comparison amplifier) control constant current operation for positive and negative output currents respectively. An "OR" function results if either circuit is operational and control is established. To check these circuits proceed as follows:

a. Remove top and right side covers from instrument. Remove the A3 board.

b. Remove strap between terminals A13 and A14 and apply a small variable dc voltage (approximately ±0.2. Vdc) between terminals A14 and A18:

c. Connect a DVM between A2U3 pin 6 (TP16) and -S. Turn on power and note that DVM reads from approximately +7V to -8V as the source voltage is varied " through zero. If voltage reading is correct, proceed to step (d). If reading is ±15V, check A2U3 for short. If reading is zero, A2U3 is defective.

d. Turn power off and replace straps between terminals A13 and A14. Remove straps between terminals A20 and A21. Apply a small variable dc voltage (approximately ±0.2Vdc) between terminals A21 and A18.

e. Connect a DVM between A2U4 pin 6 (TP17) and -S. Turn on power and note the DVM reads approximately +7V to -8V as the source voltage is varied through zero. If reading is not correct, the A2U4 stage is defective.

5:75 RMS Current Meter Driver. Integrated circuit A2U5 provides the gain necessary to drive diode detector A1CR18 which allows ac current to be metered through the detection process. To determine if A2U5 is operational, apply a sinewave (2V p.p. 100Hz) with a dc offset of -0.2 Vdc to the -OUT side of A2C13. Observe that a sinewave of approximately 28-30V p.p is present at pin 6 of A2U5. Connect oscilloscope between A2U5 pin 6 and 1 for this measurement.

5-76 A3 BOARD AND HEAT SINK ASSEMBLY TROUBLESHOOTING

# WARNING

The following troubleshooting procedures are performed with power applied to the BPS/A while its protective covers are removed. Be careful when performing the procedures as line voltage is always present on the power input connector, fuse holder, and in the power supply rectifier circuits: In addition, when the supply is on, energy available at many points, particularly the power transistors on the rear heat sink, may result in personal injury or death when contacted. 5-77 The A3 plug-in board contains positive and negative amplifier and driver stages which amplify the control voltage from board A2 in order to control the conduction of the output power transistors on the heat sink assembly. The A3 board and heat sink assembly stages can be isolated from the voltage and current feedback loops by removing the A2 board from the instrument and providing an external control voltage input to the A3 board. The following paragraphs describe troubleshooting procedures for the A3 board and heat sink assembly circuits:

5-78 Output Amplifier Stages. To troubleshoot the amplifier and driver stages on the A3 board and the output power transistors on the heat sink assembly, proceed as follows:

a. Remove the A2 board from the unit and remove the load from the output terminals.

b. Connect function generator (HP3310A) output terminals between the connector side of A3R15 (6826A) or A3R29 (6827A) and 2 . Set output of function generator for a sinewave of approximately 2V p-p at 100Hz with a dc offset of -3.5V. Connect an oscilloscope to +S and -S terminals.

c. Turn on power and observe a sinewave output of 100V p-p (6826A) or 200V p-p (6827A). The sinewave should not be clipped or distorted.

d. If either polarity of the sinewave is missing or distorted, troubleshoot by tracing the sinewave back to the source. Refer to Figure 7-2 (sheet 1) for the 6826A or Figure 7-3 (sheet 1) for the 6827A. Also, check the turn on/off circuit (Paragraph 5-79).

#### NOTE

When troubleshooting the power amplifier circuits, keep in mind that possible trouble areas exist in the interconnections (A1 board, W1, and W2 ribbon cables) as well as the A3 board circuits and the output power transistors Q1-Q4 (6826A) or Q1-Q6 (6827A) on the heat sink assembly. 5-79 Turn On/Off Circuit. The turn on/off circuit on board A3 can be isolated from the main amplifier driver circuits by disconnecting A3CR3 and/or A3CR4. If the trouble is in the turn on/off circuit, the output should rise to the proper level with the diode(s) disconnected. To check the operation of the turn on/off circuit (diodes A3CR3, CR4, are connected), short the base of A3O1 to [2], and the sinewave output will drop to .5V p-p. When the short is removed, the output will return to the full sinewave output.

5-80 Overvoltage Protection Circuit. The overvoltage protection clamping diodes are another potential trouble area. These diodes ASVR1, VR2 (6826A) or A3VR5-VR8 (6827A) can be lifted (disconnected) individually or together while observing the amplifier output. If one or more are shorted, the complete sinewave will be restored when the defective diode is disconnected.

5-81 Overcurrent Protection Circuit. Protection against overcurrent during the transition from constant voltage to constant current operation is provided by diode(s) A3CR22 (6826A) or A3CR22, CR23 (6827A) on the negative output and diodes A3CR20, CR21 (6826A) or A3CR24, CR25 (6827A) on the positive output. If these diodes are defective, the output will be badly clipped or the output level will be much lower than normal?

## 5-82 DEGRADED PERFORMANCE PROBLEMS

5-83 Table 5-4 contains a list of less common troubles and their probable causes. The troubles in this table are less catastrophic than those previously described in that, generally, they lead to degraded performance rather than complete failure.

SYMPTOM	PROBABLE CAUSE
Poor constant voltage line	Bias and reference supply: Check A1Q1-Q4, A1VR1, A2VR3, A2VR4
Poor constant current line	Bias and reference supply: Check A1Q1-Q4, A1VR1, A2VR1, A2VR2
Poor constant voltage load	<ul> <li>a. Constant current operation taking place: Check setting of CUBRENT control.</li> <li>b. A2U1, A2U2 defective,</li> <li>c. Check measurement technique.</li> </ul>
Poor constant current load regulation.	<ul> <li>a. CURRENT control set too low.</li> <li>b. A2U3, A2U4 defective.</li> <li>c. Check measurement technique.</li> </ul>
High ripple.	<ul> <li>a. Ground loop, through test equipment, check test setup.</li> <li>b. Excessive ripple in reference voltages. Check reference voltages (Table 5-2).</li> <li>c. Supply crossing over into constant, current operation, check setting of CURRENT control (may be set too close to crossover point).</li> <li>d. Defective rectifier circuits (half wave instead of full wave rectification).</li> </ul>
Excessive distortion in amplifier modes.	<ul> <li>a. Supply crossing over into constant current operation. Check setting of CURRENT control.</li> <li>b. Defective component in amplifier circuit. Check ACCRI 4-CR17, R29, R30 (6826A) or A3CR11, CR12, CR15 (6827A).</li> </ul>

5-22

Table 5-4. Degraded Performance Problems

## 5-84 REPAIR AND REPLACEMENT

5-85 Section VI of this manual contains a list of replaceable parts. Table 5-5 contains replacement data for the semiconductors used in the BPS/A described by this manual. When replacing a semiconductor, use a Hewlett-Packard part or a commercial replacement part, if applicable. In cases where neither of these parts are immediatley available and a part is needed for emergency operation or troubleshooting-verification, the alternate part (see Table 5-5) can be tried with at least a 90% probability of success.

#### 5-86 COVERS AND FRONT PANEL

5-87 Top or Bottom Cover. To remove either the top or bottom cover:

a. Turn off unit.

b. Remove two, ¼-inch, No.6 self-tapping flat-head , screws at rear of cover.
c. Slide cover toward rear of unit approximately 3/4 inches and lift out of unit.

5-88 Side Cover. To remove either side cover, remove four, 1/4-inch, No. 6 flat-head screws and lift cover off.

- 5-89 Side Castings. To remove either side casting:
  - a. Remove top, bottom, and side cover. b. Remove eight, No. 6 flat-head screws securing
- side casting to instrument cross members.
  - c. Lift side casting off.
  - C. Litt side casting of

5-90 Front Panel. To remove the front panel: a. Remove top, bottom, side covers, and left side casting.

b. Loosen the VQLTAGE METER and CURRENT METER knobs with allen wrench and remove knobs.

c. Front panel may now be pulled forward away from front of unit.

5-91 Foot Assemblies and Tilt Stand. The front and rear foot assemblies and the tilt stand on the bottom of the unit must be removed before the unit is rack mounted (see Paragraph 2-15). To remove these assemblies, proceed as follows:

a. Remove the rear foot assembly on bottom of the unit by pushing the release button in the center of the foot assembly and sliding the assembly OFF as indicated.

b. Remove bottom cover (Paragraph 5-87). The bottom cover is removed to gain access to the A1 board.

## NOTE

The release button on the front foot assembly is located directly beneath the -1 ZERO ADJ potentiometer on board AT. By pressing slightly inward on the A1 board, sufficient clearance is provided to remove the front foot assembly.

.....

c. Remove the front foot assembly as in step (a)
 except also apply slight inward pressure to the A1 board
 d. Remove one of the side castings (Paragraph
 5-89) to allow removal of the tilt stand.

e. Remove tilt stand.

f. Replace bottom cover if the unit is to be rack mounted.

Model	Reference Designation	HP Part No.	Commercial Replacement	Alternative
6826A	01-04	1854-0421	60128 RCA	
6827A	Q1-Q6	1854-0421	60128 RCA	
6826A	A1CR1-CR4; A3CR1-CR7; A2CR1, CR2, CR5, CR9, CR13, CR14, CR18-CR24; A3CR15-CR17, A3CR19-CR21	1901-0050	1N4148	
6827A	A1CR1-CR4: A2CR1, CR2, CR5, CR9, CR13, CR14, CR18-CR24; A3CR1, CR2; CR5, CR6, CR18-CR21	1901-0050	1N4148	
6826A/6827A	A1CR5-CR8	1901-0327	-1N5059	
6826A/6827A	A1CR10-CR17	1901-0328	A14D GE	والمحرب المواد المراجع
6826A/6827A	A1CR18; A2CR10	1901-0535		
6826A	A1CR20	1901-0518	و مرکز او در بالا کار از است می شد.	
6827A	A1CR19, CR20	1901-0518		
6826A/6827A	A101	1853-0041	2N4036	
6826A/6827A	A1Q2, A2Q1, Q2, Q3, Q4, Q6; A3Q1, Q4, Q6	1854-0071		* 2N4141
6826A/6827A	A103	1854-0244	2N1711A	
6826A/6827A	A1Q4; A2Q5	1853-0099		2N2907
6826A/6827A	A1VR1, VR3; A2VR1-5	1902-1221	- 1N825	E.
6826A	A2CR3, CR4, CR7, CR8, CR11, CR12	1901-0033	1N4B5	÷ Ř
6827A	A2CR3, CR4, CR7, CR8, CR11, CR12; A3CR3, CR4	1901.0033	1N485	
6826A/6827A	A2U1-U5	1820-0223	LM301AH National	
6826A/6827A	A2VR6, VR8	1902-0064	SZ10939-146 Motorola	
.6826A	A3CR14, CR22	1901-0460	1N4157	
6827A	A3CR11, CR12, CR15, CR16, CR22-CR25	1901-0460	1Ň4157	
6826A	A3Q3, Q6, Q13, Q17, Q18, Q19	1853-0038		•
6827A	A3Q3, Q7, Q9, Q10, Q13-Q16	1853-0038	SJ5099 Motorola	
6826A	A302, 07, 010 012, 014 016	1854-0095	40346 RCA	te constant and a
6827A	A302, 06	1854-0095	40346 RCA	
6826A	A308, Q9	1853-0037		
6827A	A308	1854-0232	SJ1679 Motorola	

Table 5-5. Semiconductor Replacement Data



	Model	Reference Designation	HP Part No.	Commercial Replacement Alternative
	6827A	A3Q11, Q12, Q17-Q20	/1854-0271	MM2258 Motorola
	6826A	A3VR1	1902-0660	SZ11213-368 Motorola
	6826A	A3VR2	1902-0597	SZJ 1213-366 Motorola
1	6827A	A3VR3	1902-0184	1N966
	6827A	A3VR4	1902-0182	SZ10939-272 Motorola
	6827A	A3VR5, VR6	1102-0597	SZ11213-356 Motorala
	6827A	A3VR7, VR8	/ 1902-0660	SZ11213-368 Motorola

Table 5-5. Semiconductor Replacement Data (Continued)

#### 5-92 REAR HEAT SINK ASSEMBLY

5-93 Interder to remove the power transistors from the heat sink, the rear panels must first be removed. After the rear panels are removed, the transistors are exposed and can be removed. Notice that if a new power transistor is installed, be sure to apply silicon grease (Dow DC-5, HP 8500-0059) to both sides of the transistor's mica insulator to assure proper heat exchange.

5-94 Rear Panels. To remove the rear panel containing the rear terminal boards and the panel containing the power receptacle, proceed as described below.

#### 5-95 Terminal Board Panel.

a. Remove top cover (Paragraph 5-87).

b. Remove two screws at top of unit (near Service

tag),

AI.

c. Remove cable W2 from connector J4 on board d. Lift the terminal board panel straight up and

out.

#### 5-96 Power Receptacle Panel.

a. Remove bottom cover (Paragraph 5-87).

b. Remove two screws securing corner of panel.

c. Lift panel straight up and out.

5-97 Heat Sink. To remove the heat sink, proceed as follows:

a. Remove all covers (Paragraph 5-86).

b. Remove terminal board and power receptacle rear panels (see above).

c. Remove four screws securing heat sink to side frames.

d. Remove cable W1 from connector J3 on board A1. The heat sink can now be lifted out.

## 5-98 ADJUSTMENT AND CALIBRATION

5-99 Adjustment and calibration may be required after performance testing, troubleshooting, or repair and replacement.

#### 5-100 🗧 METËR ZERO , 👘

5-101 The meter pointer must rest on the zero calibration mark on the meter scale when the instrument is at normal operating temperature, resting in its normal operating position, and turned off. To zero set the voltmeter and amineter, proceed as follows:

a. Turn on instrument and allow it to come up to normal operating temperature (about 30 minutes).

b. Turn instrument off. Wait one minute for power supply capacitors to discharge completely.

c. Insert sharp pointed object (pen point or awl) into small indentation near top of round black plastic disc located directly below meter face.

d. Rotate plastic disc clockwise until meter reads zero, then rotate counterclockwise slightly in order to free adjustment screw from meter suspension. Pointer should not move during latter part of adjustment.

5-102 CONSTANT VOLTAGE CALIBRATION

#### NOTE

The CURRENT MODE light should be off during these procedures.

5-103 Output Zero and Offset Adjustments. a. Remove top cover to gain access to potentiometers on boards A1 and A2. b: Connect DVM to the +S and -S rear terminals. c, Short BPS/A front panel input terminals (HI IN , to LO IN). Output terminals HI OUT (+) and LO OU<sup>4</sup> (-) jue open circuited.

d. Set MODE switch to FXD GAIN AMP position. Turn CURRENT control fully clockwise.

e. Turn on BPS/A and allow a 10-minute warmup. f. While switching the RANGE switch between the.

X1 and X10 positions, adjust A2R60 until the X10 reading is of the same polarity and 10 times the X1 reading within 2.5mV. For example, if X1 reading is +.1mV, adjust A2R60 for +3.5mV or less.

g. Set RANGE switch to X1 and adjust A2R61 for OV 10,25mV reading on DVM.

h. Set RANGE switch to X10, DVM should read OV ±2.5mV. If not, repeat steps (f) through (g);

i. Remove short from HI and LO IN terminals.

5-104 Constant Voltage Programming Accuracy, a. Set MODE switch to POWER SUPPLY position and RANGE switch to X1 position.

b. Short terminals A9 and A10 on rear terminal strip.

c. Adjust potentiometers A2R58 (coarse) and A2R59 (fine) for a DVM reading of  $-5,120V \pm .2mV$  (6826A) or  $-10.24V \pm .4mV$  (6827A).

d. Turn BPS/A off. Remove jumper between terminals A8 and A9 and connect a precision  $10.24 \text{K}\Omega$  (±.05%) resistor between terminals A9 and A10.

e. Turn BPS/A on and adjust front panel V ZERO ADJ  $\beta_{1}^{A}R1$  for OV  $\pm$  .2mV (6826A) or OV  $\pm$  .4mV (6827A).

f. Set RANGE switch to X10 position. DVM should read OV  $\pm 2mV$  (6826A) or OV  $\pm 4mV$  (6827A). If not, check A2R60 adjustment (step f of Paragraph 5-103). g. Turn BPS/A off. Remove 10.24K $\Omega$  resistor and connect a 20.48K $\Omega$  resistor between terminals A9 and A10.

<sup>16</sup>h, Turn BPS/A on. DVM should read +51.20V ± 25mV (6826A) or 102.40V ± 50mV (6827A).

5-105 DE Voltmeter Calibration.

range DC, 60V (6826A) or 120V (6827A), position. b. Adjust A1R8 for +51.20V (6826A) or +102.40V

(6827A) indication on BPS/A's front panel voltmeter.

c. Connect short across 20.48K $\Omega$  (±0.5%) resistor (A9 to A10). Front panel voltmeter should read -51.2V (682GA) or +102.40V (6827A).

d. Turn BPS/A off, remove 20.48K:resistor, install, jumper between A8 and A9; remove DVM from output ter, minals, and replace top cover. 5-106 CONSTANT CURRENT CALIBRATION

NOTE.

The CURRENT MODE light should be on dur ing these procedures.

5-107 Constant Current Programming Accuracy. a. Remove top cover to gain access to potentio-

meters on boards A1 and A2.

b. Bemove jumpers from A19 to A20 and from A12 to A13 on rear terminal strip.

c: Short terminals A18 and A13 and A18 to A20 on rear terminal strip.

d. Connect a 1 $\Omega$  1% precision resistor (R<sub>S</sub>) in series with the appropriate high range load resistor (R<sub>L</sub>), 49 $\Omega$  (6826A) or 199 $\Omega$  (6827A) as shown in Figure 5-16. Connect the DVM across the 1 $\Omega$  Resistor.

e. Turn on BPS/A and allow a 30-minute warmup. f. Set MODE switch to POWER SUPPLY. Set the

RANGE switch to X10 and turn the VOLTAGE control fully clockwise.

g. Adjust front panel + I ZERO ADJ (A1R2) for a reading of 0.000 ± .3mV on DVM.

h. Turn VOLTAGE control fully counterclockwise i. Adjust front panel – LZERO ADJ (A1R3) for a reading of 0.000 ± .3mV on DVM.

#### NOTE

The A1R2 and A1R3 adjustments may interact. Repeat steps (f) through (i) several times to minimize errors.

j. Turn BPS/A off and remove jumper from A18 to A20. Connect a precision (±0.5%) resistor between A18 and A20: 10.24K $\Omega$  (6826A) or 5.12K $\Omega$  (6827A).

k. Turn VOLTAGE control fully counterclockwise and turn on the BPS/A.

t. Adjust A2R21 for -1.024V ± .25mV (6826A) or -.512V ± .125mV (6827A) as indicated on DVM.

m. Turn BPS/A off and remove the jumper between A18 and A13. Connect a precision (±0.5%) resistor between A18 and A13: 10.24K (6826A) or 5.12K (6827A), n. Turn VOLTAGE control fully clockwise and turn on the BPS/A.

o. Adjust A2R19 for +1.024V ± .25mV (6826A) or +.512V ± .125mV (6827A) as indicated on DVM.

5.25

5-108 - DC Ammeter Calibration.

a. Set the CURRENT METER switch to the high range DC, 1.2A (6826A) or 0.6A (6827A), position, at

b. Adjust A1R20 for a front panel ammeter indication of 1.0A (6826A) or 0.5A (6827A).

c. Turn off BPS/A. Remove the 10.24 K $\Omega$  resistors, replace jumpers from A20 to A21 and from A18 to A14. Ensure that jumpers are also connected from A12 to A13 and from A19 to A21. Replace top cover.

#### 5-109 AC METER CALIBRATION

#### 5-110 AC Voltmeter Calibration

a. Rémove top cover to gain access to potention mêters on board A1.

b. Connect test setup as shown in Figure 5-16 with appropriate high range load resistor (RL), 49Ω (6826A) or 499Ω (6827A), connected in series with  $1\Omega$  resistor (Rg) across + and - output terminals. Set function generator for a 5 volt 100Hz squarewave output.

c. Set BPS/A front panel/controls as follows: MODE switch: FXD GAIN AMP RANGE switch: X10 VOLTAGE control; May be left, in any position for this procedure. CURRENT control: fully clockwise - VOLTAGE METER: high range AC CURRENT METER: high range AC

d. Turn on BPS/A and allow a 10-minute warmup.

e. Connect oscilloscope to +S and -S terminals

and observe-waveform for overshoot and ringing.

f. Remove oscilloscope and connect DVM to +S. and -S terminals.

g. Adjust the function generator output level for a DVM reading of 35.3  $\pm$  0.5V rms (6826A) or 70.7  $\pm$  1.0V rms (6827A):

h. Adjust A1,R13 for 35.3V rms (6826A) or 70.7V rms (6827A) indication on BPS/A front panel voltmeter.

5-111 AC Ammeter Calibration .

5-26

a. Connect DVM across the 1Ω resistor.

b: Adjust function generator output level for a DVM reading of .707 ± .03V rms (6826A) or .305 ± .015V rms (6827A).

c. Adjust A1R18 for .7A rms (6826A) or .3A rms (6827A) on BPS(A front panel ammeter.



# SECTION VI REPLACEABLE PAR

## INTRODUCTION

This section contains information for ordering re-G-2 placement parts. Table 6-4 lists parts in alpha-numeric order by reference designators and provides the following information:

a. Reference Designators. Refer to Table 6-1.

b. Description, Refer to Table 6-2 for abreviations.

c. Total Quantity (TQ). Given only the first time the part number is listed except in instruments containing many sub-modular assemblies, in which case the TO appears the first time the part number is listed in each assembly.

d. Manufacturer's Part Number or Type.

e, Manufacturer's Federal Supply Code Number. Refer to Table 6:3 for manufacturer's name and address.

f. Hewlett-Packard Part Number. g: Recommended Spare Parts Quantity (RS) for

complete-maintenance of one instrument during one year of isolated service. 1

h. Parts not identified by a reference designator are listed at the end of Table 6-4 under Mechanical and/or Miscellaneous. The former consists of parts belonging to and grouped by individual assemblies; the latter consists of alf parts not immediately associated with an assembly.

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#### **ORDERING INFORMATION** 6.3

To order a replacement part, address order or in-6.4 quiry to your local Hewlett-Packard sales of the (see lists at rear of this manual for addresses). Specify the following information for each part: Model, complete serial number, and any Option or special modification (J) numbers of the instrument; Hewlett-Packard part number; circuit reference designator; and description. To order a part not listed in Table 6:4, give/a complete description of the part, its function, and its location.

Table 6-1. Reference Designators

A = assembl		an (15 an 15 an Na tao ng tao n	miscellaneous
A ≜ assembl B = blower	Y		electronic part
.C. ≕ capacit	or 👌 🛉 🖓 🖓		fuse jack, jumper
on the second	washing in the	Sec. 19 19 19 19 19 19	isek unmener.
CB = circuit	Heaker	e caller a subsection a subsectio	
CR = diode		e caller a subsection a subsectio	relay

## Table 6-1, Reference Designators (Continued)

P = plug	V = vacuum tube,
Q = transistor	neon bulb,
R = resistor	photocell, etc.
S = switch	VR = zener diode
T = transformer	X = socket
	Z = integrated cire
TS = thermal switch	cuit or network

## Table 6-2. Description Abbreviations

	a series a series de la series d	and the second
A	= ampere	mod, = modular or
ac 👘	= alternating current	modified
assy.	= assembly	mtg = mounting
bd	≓ board	n = nano = 10 <sup>-9</sup>
bkt	= bracket	NC = normally closed
°C	= degree Centigrade	NO = normally open
cd	= card	NP = nickel-plated
coef	=/coefficient	$\Omega$ = ohm
comp	= composition	obd = order by
	= cathode-ray tube	description
CT	= center-tapped	OD = outside diameter
dc 🍴	= direct current	p = pico = 10 <sup>-12</sup>
DPD1	= double pole,	P.C: = printed circuit
· .	double throw	pot. · = potentiometer
DPST	= double pole,,	p-p = peak-to-peak
	single throw	ppm = parts per million
elect	= electrolytic	pvr = peak reverse
	= encapsulated	voltage
F	= farad	rect = rectifier
٥F	= degree, Farenheit	rms = root mean square
fxd	= fixed	Si - = silicon
Ge	= germanium	SPDT= single pole,
Н	= Henry	double throw .
Ĥz	= Hertz	SPST = single pole,
່າເ	= integrated circuit	single throw
ID.	= inside diameter	SS = small signal
incnd	= incandescent	T = slow-blow
k	= kilo = 10 <sup>3</sup>	tan. =dantulum
m	= milli = 10 <sup>-3</sup> -	Ti ∓titanium
M	± mega = 10 <sup>6</sup>	V · ≔volt
μ.,	= micro = 10 <sup>-6</sup>	var = variable
	= metal	ww 📬 wirewound
	= manufacturer	. W = Watt
9 <b>1</b> 9 (19)		

CODE	MANUFACTUBER ADDRESS	CODE	MANUFACTURER, ADDRESS
00629	EBY Sales Co., Inc. Jamaica, N.Y.	• 07.137 •	Transistor Electronics Corp.
00656 *	Aerovox Corp New Bed ford, Mass.		Minneapolis, Minn-
00853	Sangamo, Electric Co.	07 138	Westinghouse Electric Corp. Elmira, N.Y.
	S, Carolina Div. Pickens, S.C.	07263	Fairchild Camera and Instrument
01121	Allen Bładley Co Milwaukee, Wis		Mountain View, Calif.
01255	Litton Ind. Beverly Hills, Calif.	07387	Birte Corp., The Los Angeles, Galife
01281	TRW Semiconductors Inc.	07397	Sylvania Electric Prod. Inc.
282 8	Lawndale, Calif.		Mountainview, Calif.
01295	Texas Instruments, Inc. Dallas, Texas	07716	IRC Div. of TRW Inc. Burlington, Iown
01686	RCL Electronics, Inc. Manchester, N.H.	1 07910 🖉	🗧 Continental Device Corp.
01930	Amerock Corp. Rock ford, III.		Hawthorne, Calif.
02107	Sparta Mfg. Co.	07933	Raytheon Co. Components Div.
~02114 02606	Ferroxcube Corp. Saugerties, N.Y.		Mountain View, Calif.
02060	/Fenwal Laboratories Morton Grove, III.	08484	Breeze Corporations, Inc. Union, N.J.
02000	Amphenol Corp. Broadview, III.	08530	Reliance Mica Corp. Brooklyn, N.Y.
02730	Radio Corpi of America, Solid State and	08717	Sloan Company, The Sun Valley, Calif.
03508	Receiving Tube Div Somerville, N.J.	08730	Vemaline Products Co. Inc.
03000	G.E: Semiconductor Products Dept.	08806	Wyckoff, N,J.
03797	Syracuse, N.Y. Eldema Corp. Compton, Calif.	90000	
03877	Transitron Electronic Corp.	08863	Lamp Dept. Cleveland, Obio Nylomatic Corp. Norrisville, Pa.
	Wakefield, Mass.	08919	RCH Supply Co. Vernon, Galif.
03888	Pyrofilm Resistor Co., Inc.	09021	Airco Speer Electronic Components
	Cedar Knolls; N.J.		Bradford, Pa.
04009	Arrow, Hart and Hegeman Electric Co.	09182	*Hewlett-Packard Co. New Jersey Div.
	Hartford, Conn.		Rockaway, N.J.
04072	ADC Electronics, Inc. Harbor, City, Calif.	09213	General Elect. Co. Semiconductor
04213	Caddell & Burns Mfg. Co. Inc.		Prod. Dept. Buffalo, N.Y.
	Mineola, N.Y.	09214	General Elect. Co. Semiconductor
04404	Hewlett Packard Co. Palo Alto Div.		Prod. Dept. Auburn, N.Y.
	Palo Alto, Galif.	09353	C & K Components Inc. Newton, Mass.
04713	Motorola Semiconductor Prod. Inc.	09922	Burndy Corp. Norwalk, Conn.
	Bhoenix: Arizona	11115	Wagner Electric Corp.
05277	Westinghouse Electric Corp.		Jung Sol Div. Bloomfield, N.J.
	Semiconductor Dept. Youngwood, Pa.	11236	CTS of Berne, Inc. Berne, Ind.
05347	Ultronix, Inc. Grand Junction, Colo.	11237	Chicago Telephone of Cal. Inc.
05820	Wakefield Engr. Inc. Wakefield, Mass.		So. Pasadena, Calif.
06001	General Elect. Co. Electronic	11502	IRC Div. of TRW Inc. Boone, N.C.
00004	Capacitor & Battery Dept. Irmo, S.C.	11711	General Instrument Corp. Newark, N.J.
06004	Bassik Div. Stewart-Warner Corp.	12136	Rhiladelphia Handle Co. Camden, N.J.
06406	Bridgeport, Conn.	12615	U.Ş. Terminals, Inc. Cincinnati, Ohio
06486	IRC Div. of TRW Inc.	12617	Hamlin Inc. Lake Mills, Wisconsin
DEE 40	Semiconductor Plant Lynn, Mass.	12697	Clarostat Mfg. Co. Inc. Dover, N.H.
06540	Amatom Electronic Hardware Co. Inc.	13103	Thermalloy Co. Dallas, Texas
06555	New Rochelle, N.Y.	14493	*Hewlett-Packard Co. • Loveland, Colo.
00000	12. 「たんえくもの語言はないにもない」は特徴には、「よう語言な」やいたとし、「それにいたないない」ないでも、「ない	14655	Cornell-Dubilier Electronics Div.
06666	Fenacook, N.H. General Devices Co. Indianapolis, Ind.		Federal Pacific Electric Co.
06751	General Devices Co. Indianapolis, Ind. Semoor Div. Components, Inc.		Newark, N.J.
	Phoenix, Arizona	14936	General Instrument Corp. Semicon-
06776	Robinson Nugent, Inc. New Albany, N.Y.	15001	ductor Prod. Group Hicksville, N.Y.
06812	Torrington Mfg. Co. Van Nuys, Calif.,	15801	Framingham, Mass
00012		16299-	Corning Glass Works Raleigh, N.C.

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Use Code 28480 assigned to Hewlett Packard Co., Palo Alto, California

6.2

Table 6-3: Code List of Manufacturers

CODE	MANUFACTURER ADDRESS	CODE	MANUFACTURER ADDRES
16758	Delco Radio Div. of General Motors	59730	Thomas and Betts Co. Philadelphia, Pa
10100 0	Corp. Kokomo, Ind.	61637	Union Carbide Corp. New York, N.Y
17646	Atlantic Semiconductors, Inc.	63743	Ward Leonard Electric Co.
17545	Atlantic Semiconductors, me. Asbury Park, N.J.		Mt. Vernon, N.Y
7001	Fairchild Camera and Instrument Corp.	, 70563	Amperite Co. Inc. Union City, N.
17808	Mountain View, Calif.	70901	Beemer Engrg Co.
	Daven Div. Thomas A. Edison Industries		Fort Washington, P
17870	McGraw-Edison Co. Orange, N.J.	70903	Belden Corp. Chicago, II
		71218	Bud Radio; Inc. Willoughby, Oh
18324		71279	· Cambridge Thermionic Corp.
19315	Bendix Corp. The Navigation and Control Div. Teterboro, N.J. •		Cambridge, Mas
		71400	Bussmann Mfg: Div.of McGraw &
19701	Electra/Midland Corp.	-	Edison Co. St. Louis, M
1. 1	Mineral Wells, Texas	71450	Elkhart, In
21520	Fansteel Metallurgical Corp:	1	I.T.7. Cannon Electric Inc.
e en tra	No. Chicago, III.	71468	Los Angeles Cal
22229	Union Carbide Corp. Electronics Div.		
	Mountain, View, Calif.	71590.	Globe-Union Inc. Milwaukee, W
22753	UID Electronics Corp. Hollywood, Fla.		■ 4.1 「「」」「「「」」「「」」「」」「」」「」」「」」「」」「」」「」」「」」「」
23936	Pamotor, Ins. Pampa, Texas	71700	General Cable Corp. Cornish
24446	General Electric Co. Schenectady, N.Y.		Wire Co. Div. Williamstown, Ma
24455	General Electric Co.	71707	Coto Coil Co. Inc. Providence, R
21,00	Nela Park, Cleveland, Ohio	71744	Chicago Miniature Lamp Works
24655	General Radio Co. West Concord, Mass.		Chicago, 1
24681	LTV Electrosystems Inc. Memcor/Com-	71785	Cinch Mtg. Co. and Howard
24001	ponents Operations Huntington, Ind.	•	B. Jones Div. Chicago,
00000	Dynacool Mfg. Co. Inc. Saugerties, N.Y.	71984	Dow Corning Corp. Midland, Mid
26982	National Semiconductor Corp. "	72136	Electro Motive Mfg. Co. Inc.
27014	Santa Clara, Calif.	12100	Willimantic, Cor
	[[전 · · · · · · · · · · · · · · · · · ·	72619	Dialight Corp. Brooklyn, N.
28480	Hewlett-Packard'Co. Palo Alto, Calif.		General Instrument Corp. Newark, N
28520	Heyman Mfg. Co Kenilworth, N.J.	72699 '	General Instrument Corp. Newark, w
28875	IMC Magnetics Corp. Rochester; N.H.	72765	Drake Mfg. Co. Harwood Heights,
31514	SAE Advance Packaging, Inc.	72962	Elastic Stop Nut Div. of
•	Santa Ana, Calif.		Amerace Esna Corp. Union, N
31827.	Budwig Mfg. Co. Ramona, Calif.	72982	Erie Technological Products
33173	G.E. Co. Tube Dept. Owensboro, Ky.		Erie,
35434	Lectrohm, Inc. Chicago, III.	73096	Hart Mfg. Co. Hartford, Co
37942	P.R. Mallory & Co. Indianapolis, Ind.	73138	Beckman Instruments
42190	Muter Co. Chicago, III.		Fullerton, Ca
43334	New Departure-Hyatt Bearings Div.	73168	Fenwal, Inc. Ashland, Ma
	General Motors Corp.	73293	Hughes Aircraft Co. Electron
	Serieral Motors Confin Sandusky, Ohio		Dynamics Div. Torrance, Ca
34000 -	Ohmite Manufacturing Co. Skokie, III.	73445	Amperex Electronic
44655			Hicksville, N
46384	Penn Engr. and Mfg. Corp. Doylestown, Pa.	, 73506	Bradley Semiconductor, Corp
	에 이 사내는 것이 가지 않는 것을 하는 것을 수 있는 것을 가지 않는 것을 가지 않는 것을 가지 않는 것을 하는 것을 수 있는 것을 가지 않는 것을 하는 것을 하는 것을 하는 것을 하는 것을 하는 것		New Haven, Co
47904	Polaroid Corp. Cambridge, Mass.	73559	Carling Electric, Inc. Hartford, Co
49956	Raytheon Co. Lexington, Mass.	73734	Federal Screw Products, Inc.
55026	Simpson Electric Co- Div. of American		Chicago,
	Gage and Machine Co. Chicago, III.		i y 📲 a china an ing a partin 🚅 1997. Ya sanabara na galaring a sing aka ngan na sang ang sang sang sang sang s
56289	Sprague Electric Co.	74193	
	North Adams, Mass.	74545	Hubbell Harvey Inc. Bridgeport, Co
58474	Superior Electric Co. Bristol, Conn.	74868	Amphenol Corp. Amphenol RF Div.
58849	Syntron Div. of FMC Corp.		Danbury, Co
	Homer City, Pa.	74970	E, F. Johnson Co Waseca, Mi

CODĘ	MANUFACTURER ADDRESS		CODE	MANUFACTURER ADDRES
75042	IRC Div. of TRW, Inc. Philadelphia, Pa.	t:≹ 	82866	Research Products Corp. Madison, Wise
75183	*Heward B. Jones Div: of Cinch		82877	Rotron Inc. Woodstock, N.Y
	Mfg. Corp. New York, N.Y.		82893	Vector Electronic Co. Glendale, Cali
75376	Kurz and Kasch, Inc. Dayton, Ohio		83058	Carr Fastener Co. Cambridge, Mas
75382	Kilka Electric Corp. Mt. Vernon, N.Y.	- N	83186	
75915				Victory Engineering Springfield, N.
76381		,	83298	Bendix Corp. Eatontown, N.
10301	Mingesota Mining and Mig. Co.		83330	Herman H. Smith, Inc. Brooklyn, N.Y
S	St. Paul; Minn.		. 83385	Central Screw Co. Chicago, II
76385	Minor Rubber Co. Inc. Bloomfield, N.J.		83501	Gaving re and Cable Brook field, Mas
76487	James Millen Mfg. Co. Inc. Malden, Mass.		83508	- Granit Fulley and Hardware Co.
76493	J.W. Miller Co. Compton; Calif			West Nyack, N.Y
76530	Cinch City of Industry, Calif.		83594	Burroughs Corp. Plainfield, N.
76854	Oak Mfg. Co. Div, of Oak Electro/	• •	83835	U.S. Radium Corp. Morristown, N.J
	Netics Corp Crystal Lake, III.	5	83877	Yardeny Laboratories New York, N.Y
77068	Bendix Corp., Electrodynamics Div.		84171	
	Ne United Dive		84411	
77100	No. Hollywood, Calif.	ч		TRW Capacitor Div, Ogallala, Neb
77122	Palnut Co. Mountainside/N.J.	н	86684	RCA Corp. Harrison, N:J
77147	Patton-MacGuyer Co. / Providence, R.I.	e Barangan Barangan	86838	Rummel Fibre Co. Newark, N.J
77221	Phaostron Instrument and Electronic Co,		87034	<ul> <li>Marco &amp; Oak Industries Anabeim, Calil</li> </ul>
	South Paradena, Calif.		1. 87216	Philco Corp. Lansdale, Pa
77252	Philadelphia Steel and Wire Corp.		1 87585	Stockwell Rubber Co. Philadelphia, Pa
	S Philadelphia Pa.	4	87929	Tower-Olschan Corp. Bridgeport, Conn
77342	American Machine and Foundry Co.		88140*	Cutler-Hammer, Inc. Lincoln; III
	Princeton, Ind.		88245	Litton Precision Products Inc, USECO
77630	TRW Electronic Components Div.	- 1 ø		
11000			90634	Van Nyys, Call
777640	Camden, NJ	<u>х</u> е.,	· · · · · · · · · · · · · · · · · · ·	Gulton Industries Inc. Metuchen, N.
77764	Resistance Products Co. Harrisburg, Pa.		90763	United-Car. Inc. Chicago, Ill
78189	Illinois Tool Works Inc. Elgin, IU		91345	Miller Dial and Nameplate Co.
78452	Everlook Chicago, Inc. Chicago, III.			El Monte, Calif
78488	Stackpole Carbon Co. St. Marys, Pa.	1.15	91418	Radio Materials Co. 👘 Chicago, III.
78526	Stanwyck Winding Div. San Fernando		91506	Augat, Inc. Attleboro, Mass
	Electric Mfg. Co. Inc. Newburgh, N.Y.		91637	Dale Electronics, Inc. Columbus, Neb.
78553	Tinnerman Products, Inc. Cleveland, Ohio	1	91662	Elco Corp. 🐔 🛝 -Willow Grove, Pa
78584	Stewart Stamping Corp. Yonkers, N.Y.		91929	Honeywell Inc. // Freeport, III.
79136		(1, 1)	92825	Whitso, Inc. ) Schiller Pk., Ill.
			93332	
79307	Whitehead Metals Inc. New York, N.Y	· .		- Sylvania Electric Prod. Woburn, Mass.
79727	Continental-Wirt Electronics Corp.		93410	Essex Wire Corp. Mansfield, Ohio
-	Philadelphia, Pa.		94144 *	Raytheon Co. Quincy, Mass.
79963 ·	· Zierick Mfg. Co. Mt. Kisco, N.Y.		94154	Wagner Electric Corp. Livingston, N.J.
80031	Mepco Morristown, N.J.		94222	Southco Inc. Lester, Pa.
80294	Bourns, Inc. Riverside, Calif.	·	95263	Leecraft Mfg. Co. Inc. L.I.C., N.Y.
81042	Howard Industries Racine, Wisc.	·	95354	Methode Mfg: Co. Rolling:Meadows, III.
81073	Grayhill? Inc. La Grange, III.		95712	Bendix Corp. )Franklin, Int.
81483	International Davidian St. C. M. C. M.		95987	Weckesser Co. Inc. / Chicago, U.
	International Rectifier "El Segundo, Calif.	N		
81751	Columbus Electronics Yonkers; N.Y.	5	96791	Amphenol Corp. Janesville, Wis,
82099	Goodyear Sundries & Mechanical Co. Ind		97464 /	Industrial Retaining Ring Co.
	New York, N.Y	Â		Irvington, N.J.
82142	Airco Speer Electronic Components	221	97702	IMC Magnetics Corp. Westbury, N.Y.
4	Du Bois, Pa.		98291)	Sealectic Corp. Matharoneck, N.Y.
82219.	Sylvania Electric Products Inc.		984.10	ETC Inc.
	Emporium, Pa.	<b>-</b>	-98978	International Electronic Research Corp.
82389	Switchcraft, Inc. 11. Chicago, III.	• • • •		Burbank, Calif.
	· · · · · · · · · · · · · · · · · · ·		99934	I Renbrandt, Inc. Boston, Mass.
82647	Motals and Controls Inc. Attleboro, Mass.			Training the state of the state

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Table 6-3. Code List of Manufacturers

Use Code 71785 assigned to Circh Mfg. Co., Chicago, Ill.

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REF. DESIG.	DESCRIPTION	то	MFR. PART NO.	MFR. CODE	HP PART NO.	RS
A1	Interconnect and Power Supply Board			•	-	
C1 1	Not Assigned					
C2	fxd, elect. 100µF 25Vdc	1	30D107G025DD2-DSM	56289	0180-0094	
C3.4 "	fxd, elect. 325µF 35Vdc	2	D34656-DEE	56289	0180-0332	1
C5 €7	fxd, cer05µF 40QVdc	8	33C17A3-CDH	56289	0150-0052	2
C8	fxd, elect, 200µF 175Vdc	2	68D10223	56289	0180-1885	'
C9	fxd, cer05µF 400Vdc		33C17A3 CDH	56289	0150-0052	
C10		2			0100 0102	
682GA	fxd, elect, 3000µF 85Vdc		36D302G085AC2A-DQB	56289	0180-2193	
6827A	fxd, elect. 430µF 200Vdc		32D600B	56289	0180-1808	
C11-C14	fxd, cer. 0.5µF 400Vdc	1	33C17A3-CDH	56289	0150-0052	
C15					a	]
6826A	fxd, elect. 3000µF 85Vdc		36D302G085AC2A-DOB	56289	0180-2193	
6827A	fxd, elect: 430µF 200Vdc	1	32D6008	56289	0180-1808	
. C16	fxd, elect. 200µF 175Vdc	1	68D10223	56289	0180-1885	
. C17, C18	fxd, cer47µF 25Vdc	2	5C1187-CML	56289	0160-0174	
CR14	Diode, Si: 200mA 75V	4	1N4148	28480	1901-0050	- C
CR5-8	Diode, Si. 200prv 1A	8	1N5095	28480	1901-0327	5
CR9	Not Assigned					
CR10-17	Diode, Si, 400V 1A	8	A14D	03508	1901-0328	
	Diode, Hot Carrier	1		28480	1901-0535	1.
CR18 *	Dioue, flot outliet	2		· · · · ·		1
CR19	Not Used (Jumper)					
6826A	Diode, Hot Carrier			28480	1901-0518	1.
6827A	Diode, Hot Carrier	1	•	28480	1901 0518	
CR20	Connector, Printed Circuit Edge	2	252-18-30-340	.71785	1251-2134	1
J1, J2		2		76381	1251-3119	) [ <b>1</b> ]
J3, J4	Connector, Multi-contact	1	603-6	09023	0490-0745	5 1
K1	Relay, 6Vdc coll voltage	1	2N4036	02735	1853-0041	1 <b>1</b> -
01	Power PNP Si.	1	2N4141	01295	1854-0071	i 11
Q2	SS NPN Si			28480	1854 0244	F   1 -
03	Power NPN Si.	- 1	2N2907	56289	1853-0099	) (1)
Q4	SS PNP SI.	3		84048	2100-1755	5 1
R1,2,3	•ovar, ww. 100, 5%, 1W	1 1		1.1.1		1
R4	A COLL A COLL HOM	2	EB5635	01.121	0686-5635	5
6826A	fxd, comp. 56K ±5%, ½W		EB5135	01121	0686-513	5
.6827⁄A	fxd, comp. 51K ±5%, ½W	2				1
R5, 6			RG42	11502	0698-364	4
6826A	// fxd, metal oxide, 5.1K ±5%, 2W		RG42	11502	1 · · · · · · · · · · · · · · · · · · ·	
िंग 6827A 🔥	fxd, metal oxide, 33K ±5%, 2W		11072			
· · · · · · · · · · · · · · · · · · ·		eis s	CR5625	01121	0686-563	5
6826A	fxd; comp. 56K ±5%, ½W		EB5635	01121		1 - C.
6827A	fxd, comp. 51K ±5% ½W		EB5135	84048	and the state of the	· · · · ·
: R8	(*) var, ww 5K ±5%, 1W	2				
• R9		2	- 24日 - 127 赤 キー 留た し たえがい ニート ビン林 しいしょ ビント	07716	0757-028	- 15 k
6826A 🕴	fxd; film; 9.09K ±1%; 1/8W		CEATO	07716	1 - 1 - 1 - 1 - 1 - 1 - 2 - 2 - 2 - 2 -	
6827A	fxd, film, 6.2K ±1%, 1/8W		CEA T-0			
. R10				07710	0757-044	1
6826A	fxd, film, 8.25K ±1%, 1/8W		CEA T-0	07716		
6827A	fxd) film, 12K ±1%, 1/8W		CEA T-0	07716	0000.000	<b>3</b> 16

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REF. DESIG.	DESCRIPTION	та	MFR. PART NO.	MFR. CODE	HP PART NO.	F
	1. 1.	2	······································		<b>9</b>	
A1R11		. "	CEA T-0	07716	0757-0453	
6826 <u>A</u>	fxd, film, 30.1K ±1%, 1/8W +		CEA T-0	07716	0698-3157	ŀ
6827A	fxd, film, 19.6K ±1%, 1/8W	1	CEATO		1000-0101	1
R12				07716	0698-3572	
6826A	fxd, film, 60.4K ±1%, 1/8W		CEATO	07716	069B-3265	
6827A	fxd, film, 118K ±1%, 1/8W		CEA, T-O	84048	2100-0741	
R13	var, ww, 5K ±5%, 1W		CT-100-4	04040	2100-0741	6
R14		1		07710	0767 0440	
6826A	fxd, film, 7.5K ±1%, 1/8W	1.0	CEA T-0	07716	0757-0440	
6827A	fxd, film, 6.2K ±1%, 1/8W		CEA,T-0	07716	0698-5087	
R15		1				
6826A	fxd, film, 1.21K ±1%, 1/8W		CEA T-0	07716	0757-0274	
6827A	fxd, film, 1.1K ±1%, 1/8W		CEA T-0	07716	0757-0424	
R16		*2'			•	
.6826A	1xd, film, 30.1K ±1%, 1/8W	1.	CEA T-0	07716	0757-0453	
6827A	fxd, film, 42.2K ±1%, 1/8W,		CEA T-0	07716	0698-3450	
R17		2				
6826A	fxd, film, 1.69K ±1%, 1/8W	e de la face Nota de la composición	CEA T-0	07716	0698-4428	
6827A	• fxd, film, 16.2K ±1%, 1/8W		CEA T-0	07716	0757-0447	
R18_	var, ww.200 ±5%, 1W	2	CT-100-4	84048	2100-1771	
	Val, WW 200 - 0101					
R19	fxd, film, 1-78K ±1%, 1/8W		CEA T-0	07716	0757-0278	ŀ
6826A	fxd, film, 16.2K ±1%, 1/8W		CEA T-0	07716	0757-0447	
6827A			CT-100-4	. 84048	2100-1771	
R20	var, ww, 200 ±5%, 1W	4 4	***	d		
R21			CEA T-0	07716	:0757-0430	
6826A	fxd, film, 2.21K ±1%, 1/8W	1.1	CEA T-0	07716	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
6827A	fxd, film, 1.18K ±1%, 1/8W		CEA 1-0			
R22			CEA T-0	07716	0698'3435	
6826A	fxd, film, 38.3 ±1%, 1/8W	1.1	LEA 1.0	0,,,0		47
6827A	Not Used (Jumper)	1.				1
R23	[10] A. S.	1		07716	0757-0410	.1
6826A	fxd, film, 511 ±1%, 1/8W		CEA T-0			
6827A	fxd, film, 178,±1%, 1/8W		CEA T-0	07716	0098-3435	1
R24		1			0000 500	,   <sub>2</sub>
6826A	fxd, film, 6.2K.±1%, 1/8W		CEA T-0	07716	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
6827A	* fxd, film, 3.48K ±1%, 1/8W	e e	. CEA T-0	07716		- D-
R25	fxd, comp. 4.3K ±5%, %W	1	EB-4325	.01121		
R26	fxd, comp. 7.5K ±5%, %W	2	EB-7525	01121	1.	
R27	fxd, comp. 750 ±5%, ½W	1	EB-7515	01121		• •1
R28	fxd, comp. 1K ±5%; ¼W	1	EB-1025	01121		
R29	fxd, film, 3.92K ±1%, 1/8W	1	CEATO	• 07716		
R30	fxd, film, 6.81K ±1%, 1/8W	1	CEA T-0	07716		
	fxd, comp. 7.5K ±5%, %W	ି ଏ 2	EB-7525	01121	. 0686-752	5
R31	fxd, film, 1.3K ±1% ½W	1	CCATO .	07716	0757-073	5
R32	Not Assigned					ľ
R33	fxd, film, 5.49K ±1%, 1/8W	े   1	CEA T-0	07710	0698-338	2
<b>R34</b>						
, R35	Not Assigned		CEA T-0	07710	0757-028	8
R36	fxd, film, 9.09K ±1%, 1/8W			01121		- L.
R07	fxd, comp. 620 ±5%, ½W	- <b>1</b>			세계 이가 집중	5 - E

REF. DESIG.	DESCRIPTION	τα	MFR. PART NO.	MFR. CODE	HP PART NO.	
A1R38		1	· · · · · · · · · · · · · · · · · · ·	<u>,</u>		ŀ
6826A	fxd, ww, 750 ±5%, 5W	:	243E	56289	0811-1861	ŀ
	Not Used (Jumper)				ii.	Ŀ
6827A	(ADE Open formiber)	1				ŀ
R39.	fxd, ww, 750 ±5%, 5W		. 243E	56289	0811-1861	<b>.</b>
6826A	fxd, comp, 36K ±5%, 5W		EB-3635	01121	0686-3635	
6827A						
R40	Not Assigned	1	CEA T-0	07716	0757-0442	
R41	C fxd, film, 10K ±1%, 1/8W	1	132F	20940	0811-2958	E
• R42	fxd, ww, 10.24K ±,05%, ½W	1	EB-1325	01121	0686-1325	
R43	fxd, comp. 1.3K ±5%, ½W		CEA T-0	07716	0698-3430	
R44,45	fxd, film, 21.5 ±1%, 178W	4	CEA 1-0			i i
R46	Not Assigned				1	
R51,52		2	MF4C T-0	19701	0757-0460	
6826A	fxd, film, 61.9K ±1%, 1/8W			07716	0757-0441	ł
6827A	fxd, film, 8.25K ±1%, 1/8W		CEA T-0	07716	0698-5092	
R53	fxd, film, 160K ±1%, 1/8W	1	CEA T-0	07716	0757-0316	
R54	fxd, film, 42.2 ±1%, 1/8W	作1.1	CEA T-0	The second second second	0737-0310	i ji
R55	Thermistor, 64 ±10%	1	LB16J1	.02606	1	- P
R56	fxd, film, 3.16K ±1%, 1/8W	1.	CEATO	07716	0757-0279	1
R57	Not Assigned		$\sim$			
		1				
R58	fxd, film, 2K ±1%, 1/8W		CEA T-0	07716	0757-0283	<b>`</b>
6826A	fxd, film, 5.11K ±1%, 1/8W	1997 - 19	CEA T-0	07716	0757-0438	
6827A			7	1		
R59	Not Assigned	1	242E 1R05	56289	0811-1732	j,
R60	fxd, ww, 1 ±5%, 3W	1		· · · · ·		•
R61-65	Not Assigned	4	CEA T-0	07716	0698-3476	
R66-69	fxd, film, 6K ±1%, 1/8W		CEA T-0	07716	0698-3430	;
/R70,71	fxd, film, 21,5 ±1%, 1/8W	2		28480	3100-1941	, i
S1, 2	Switch, rotary, 3 sections	2	1N825	28480	1902-1221	
VR1/	Diode, zener 6.2V	<b>1 2</b>				
VR2	Not Assigned		1N825	28480	1902-1221	
VR3	Diode, zener 6,2V		11825			4
A2	Voitage and Current Control Plug-In					
	Board	5	RDM15E300J3S	00853	0160-0181	7
Ċ1	fxd, mica, 30pF ±5%, 300V	2	T TDM DC00000			. 1
C2,3	Not Assigned		PDM15E200 139	00853	0160-0181	- 1
C4,5	fxd, mica, 30pF ±5%, 300V		RDM15E300J3S			
C6		1	00000 40014E02007 0011	56289	0160-2477	
6826A	fxd, cer. 015µF1KV		C023B102M1537S27-CDH	• 56289	0150-0012	
6827A	fxd, cer01µF 1KV		C023A102J103MS38-CDH	1 20703		њ. 13.
C7		1 I.			0100 2000	<u>,</u>
6826A	fxd, mice, 1500pF ±5% 300V		_RDM19F152J3S	00853	0160-3068	· · · ·
6827A	fxd, mica, 1000pF ±5% 100V		RDM15E102JIC	00853		
	fxd, mica, 30pF ±5% 300V		RDM15E300J3S	00853		
C8)	fxd, mylar, .001µF ±10% 200V	<u>``</u>	292P10292.PTS	56289		- 61 I
;C9	fxd, tanti 2.2µF 20Vdc		150D225X0020A2-DYS	56289	0180-0158	5
C10		2	이 집 가슴을 들었다. 한 것이라도 이 같아요. 이번 것이 것 같아?			1
<b>C</b> 11, 12	000 C +100 200V		292P22392-PTS	56289		
6826A	fxd, mylar, .022µF ±10% 200V fxd, mylar, .0068µF ±10% 200V		.292P68292-PTS	56289	0160-015	3
6827A		2 I. K. A	에 Mine 특별한 이번 전통	24 8 8 8 7 <u>8 8</u>		_

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REF. DESIG,	DESCRIPTION	то	MFR. PART NO.	MFR. CODE	HP PART NO.	
A2C13	fxd, elect. 100µF, 6Vdc	1	30D107G006CC2-DSM	56289.	0180-1734	1.
C14	fxd, mica, 30pF ±5%, 300V		RDM15E300J3S	00853	0160-0181	
CR1, 2	Diode, Si. 200mA 75V	13	1N4148	28480	1901-0050	7
CR3, 4	Diode, Si. 250mW 200prv	6	1N485	28480	1901-0033	5-
C <b>R</b> 5	Diode, Si, 200mA 75V		1N4148	28480	1901-0050	· A
CR6	Not Assigned					
CR7, 8	Diode, Si. 250mW 200prv		1N485	28480	1901-0033	
CR9	Diode, Si. 200mA 75V		1N4148	28480	1901-0050	
CR10	Diode, Hot Carrier	1		28480	1901-0535	1
CR11,12 -	Diode, Si. 250mW 200pw		1N485	28480	1901-0033	
CR13,14	Diode, Si. 200mA 75V		1N4148	28480	1901-0050	
. CR15-17	Not Assigned	n an tha				
CR18-24	Diode, Si. 200mA, 75V		1N4148	28480	1901-0050	
K1	Reed Relay:		•	28480	0490-1013	
K2,3	Reed Relay	2 1		28480	0490-0399	2
L1 Q1-4	Indicator, 1 microhenry SS NPN SI.	5	2N4141	28480		5
Q1-4 Q5	SS PNP Si.	$\gamma = -\frac{1}{2} \frac{1}{2} \frac{1}{2} \frac{1}{2}$	2N2907	01295 56289	1854-007.1	1.0
Q6	SS PNP SI.	:1	2N2507	01295	1853-0099 1854:0071	
R1,2	fxd, film, 1K ±1%, 1/BW	6			07,57-0280	
• R3	fxd, ww, 714±1%, %W	6	CEA T-0 R303B	07716	07,57-0285	14 C
.R4	fxd, film, 1K ±1%, 1/8W			1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.		
R5	fxd, film, 6K ±1%, 1/8W	5	CEA TO	07716	0757-0280	
R6, 7	fxd, ww, 10.24K ±,05%, ½W		CEA T-0	07716	0698-3476	
• <b>R8</b>	fxd, film, 6K ±1%, 1/8W	.2	132F CEA T-0	20940	0811-2958	
R9	Not Used (Jumper)		CEA 1-0	07716	0698-3476	
R10	fxd, 1 m, 6K,±1%, 1/8W		CEA T-0	07716	0698-3476	· · ·
'R11,12	fxd, fim, 100 ±1%, 1/8W	4	CEA T-O	07716	0757-0401	1
R13				07710	0/0/-0401	1
6826A	fxd, film, 4.75K ±1%, 1/8W	1	CEA T-0	07716	0757:0437	1.
' 6827A	fxd, film, 6.81K ±1%, 1/8W	2	CEA T-0	07716,	0757-0439	
, R14	1x0, 11111, 0.0 1 x + 1.0, 1701	<b>. .</b>	UCA 10	07710,	0707-0455	
6826A	fxd, film, 5.11K ±1%, 1/8W	4	CEA T-0	07716	0757-0438	
6827A	fxd, film, 6.2K ±1%, 1/8W	2	CEATO	07716		
R15					0000,0007	1. *
6826A	fxd, film, 511 ±1%, 1/8W	1	CEA T-0	07716	0757-0416	1
6827A	fxd, film, 619 ±1%, 1/8W	1	CEA T-0	07716	0757-0418	
,R16			6			
6826A	fxd, ww, 10.24K ±.05%, %W		132F	20940	0811-2958	1
6827A	fxd, ww, 20.48K ±.05%, ½W	1.	132F	20940	0811-2959	1
R17, 18	fxd, film, 1.18K ±1%, 1/8W	2	CEA T-0	07716	0698-3512	
R19	var, ww. 10K ±5%, 1W	2	CT-106-4	11502	2100-0989	
R20-	£xd, film, 57.6K ±1%, %W	2	CCA T-0	07716	0757-0114	1
R21	Var, ww 10K ±5%, 1W		CT-106-4	11502	2100-0989	
R22	fxd, film, 57.6K ±1%, %W	5	CCA T-0	07716	0757-0114	<b>.</b>
R23				n in the states. National states		
6826A	Not Used (Jumper)					
6827A	fxd, film, 1Meg ±1%, %W	4	CCA T O	07716	0757-0344	1
R24	fxd, comp, 3:9.±5%, ½W	l i l	EB39G5	.01121	0698-5139	1
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REF. DESIG.	DESCRIPTION	та	MFR. PART NO.	MFR. CODE	HP PART NO.	R
		- - 15			-	ŀ
A2R25	fxd, film, 221K ±1%, 1/8W		CEA T-0	07716	0757-0473	
6826A	1X0, 11111, 2215 - 170, 170W		CEA T-0	07716	0698-3460	• • •
6827A	fxd, film, 422K ±1%, 1/8W					1.1
R26		10 J			•	
6826A	Not Used (Jumper)		CCA T-0	07716	0757-3444	
6827A	fxd, film, 1Meg ±1%, %W		T7A"	01686	0811-2133	Į.
R27	; fxd, ww <sub>e</sub> 1 ±.5%, 8₩					
Ř28,29			COA TO	07716	0757-0344	4
6826A	, TXO, TIIM, TIVE9 + 1 /0, /4 /4	2	CCA T-0	07716	0757-0344	
56 6827A	fxd, film; 1Meg ±1%, KW	10.2	CCA T-0	07710		
R30,31	Not Assigned			07716	0757-0288	
R32,33	fxd, film, 9.09K ±1%, 1/8W	2	CEA T-0		0698-5088	
R34	fxd, film, 12K ±1%, 1/8W	-l _l	CEAT-0	07716	0757-0283	- E.
R35 🕌	fxd, film, 2K ±1%, 1/8W	] <b>3</b> [	CEA T-O	077.16		
R36	fxd, film, 8.25K ±1%, 1/8W	322	CEA T-0	07716	0757-0441	
R37,38	fxd, film, 21.5 ±1%, 1/8W	2	CEA T-0	07716	0698-3430	
R39	fxd, film, 8.25K ±1%, 1/8W	- 1	CEA T-0	07716	0757-0441	
R40	•fxd, comp. 82 ±5%, 1/2W	1	EB-8205	01121	0686-8205	÷ .
R411	<sup>™</sup> fxd, film, 5.11K ±1%, 1/8W		CEA T-0	07716	0757-0438	
R42						
6826A	fxd, ww. 92.16K ±.05%, ½W	1	132F	20940	0811-3200	- <b>1</b> '
A 4 5 1	fxd, ww. 184.32K ±.05%, ½W	1	132F	20940	0811-3201	
6827A	Not Assigned			1 1 1 1 1 1		а, <sup>1</sup> .
R43,44	fxd; film, 3K ±1%, 1/8W	2	CEA T-0	07716	0757-1093	
R45	fxd, film, 7.5K ±1%, 1/8W	4	CEAT-0	07716	0757-0440	Ľ.
R46	fxd, film, 6.2K ±1%, 1/8W		CEA T-0	07716	0698-5087	
R47			CEA T-0	07716	0757-0440	)- [
R48	fxd, film, 3K ±1%, 1/8W		CEA T-0	07716	0757-0446	j ŀ
R49	fxd, film, 15K ±1%, 1/8W		EB-1025	01121	0686-1025	- 1
R50	fxd, comp. 1K ±5%, ½W		CEA T-0	07716	0698-3476	
R51	fxd, film, 6K ±1%, 1/8W			07716	0757-0401	
R52, 53	fxd, film, 100 ±1%, 1/8W		CEA T-O	07716		
R54 🏙	fxd, film, 6K ±1%, 1/8W	9. 11 11. U	CEA T-0	07716	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1
R55	fxd, film, 27,4K ±1%, 1/8W	Service 1	CEA T-0		0757-0283	
R56	fxd, film, 2K ±1%, 1/8W		CEA T-0		0757-0448	1
R57	fxd, film, 7.5K ±1%, 1/8W	· •	CEA T-0	<ul> <li>07716</li> <li>04048</li> </ul>		- 1
R58	var, ww, 1K ±5%, 1W	1	1 CT-106-4	84048		
R59	var, ww, 10 ±5%, 1W	1	CT-106-4	84048		- 12
R60,61	var, ww, 100 ±5%, 1W,*	2	CT-106-4	84048	2100-1755	2
R62	Not Assigned					
R63	fxd, film, 7.5K ±1%, 1/8W		CEA T-0	7 07716		
R64	fxd, film, 5.11K ±1%, 1/8W		CEA T-0	07716		
	fxd, film, 7.5K ±1%, 1/8W		CEA T-0	07716		
R65	fxd, film, 5.11K ±1%, 1/8W		CEA T-0	07716	0757-0438	3
R66						
R67-70	Not Assigned	j."⇒	CEA T-0.	07716	0757-0280	<b>3</b> :
R71,72	fxd, film, 1K ±1%, 1/8W		CEAT-0	07716		
R73	fxd, film, 6.81K ±1%, 1/8W		CEA T-0	07716	그 말한 것이 않는 것 이가 이 것	· · ·
R74	fxd, film, 1K ±1%, 1/8W	6.8 07 F	영국에 실패되었다. 이 가슴	07716		
R75	, fxd, film, 5.49K ±1%, 1/8W	. <b>1</b>		07716		
R76	fxd, film,,24.3K ±1%, 1/8W	୍ର 2				<u> </u>

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	REF. DESIG.	, DESCRIPTION	то	MFR. PART NO.	MFR. CODE	HP PART NO,	RS
A:	2R77	fxd, film, 2.43K ±1%, 1/8W	1	CEA T-0	07716	0757-0431	· 1
	R78	fxd, film, 24.3K ±1%, 1/8W		CEA T-0	07716	0757-0451	
	R79	fxd, film, 2K ±1%, 1/8W		CEA T-0	07716	0757-0283	
	51	Slide Switch, 0.5A, 125Vac/dc	1	GF126-0020	79727	3101-1311	1
· ·	VR1-6	Diode, zeher 6.2V	5	1N825	28480	1902-1221	. 5
	VR6	Diode, zeher 7.50V 400mW	2	SZ10939-146	04713	1902-0064	2
	VR7	Not Assigned			01110	1002 0004	· *
	(VR8	Diode, zener 7.50V 400mW	· . •	SZ10939-146	04713	1902-0064	
	Ú1-5	IC, Lingar Amplifier	5	LM301AH ,	27014	1820=0223	5
					2,014	IUZO UZZU	
A	3	6826A Power Amplifier Plug-In Board					
	C1	Ixd, elect. 20µF 15Vdc	2 <b>1</b> 8	- 30D206G016BB2-DSM	56289	0180-0300	
	C2 .	fxd, effect. 1µF 35Vdc		150D105X9035A2~	56289	0180-0291	1.1
4 L .	C2-8	Not Assigned					
	C9	fxd, mica, 150pF 300Vdc	ា	RDM15F151J3C	00853	0140-0196	1
· ·	C10	fxd; mica, 330pF 500Vdc	1	RDM15F331J55	00853	0160-2012	1
. ا	C11	fxd, mylar, .001µF 200Vdc	1	192P10292	56289	0160-0153	1
l.	C12,13	fxd, mylar .047µF 200Vdc	2	292P47352-PTS	56289	0160-0138	
	C14	fxid, cer02µF 500Vdc	1	C023B501J203ZS25	56289	0160-0468	
	C15. *	fxd, cer. 5000pF 1KV	1.	C023B102G502ZS31-CDH	56289	0160-0899	[ ]
	CR1.7	Diode, Si. 200mA 75V	13	1N4148	28480	1901-0050	57
	CR8-13	Not Assigned			20400	150110000	
	CR14	Stabistor, Si. 10prv 400mW	2	1N4157	28480	1901-0460	2
1 A A A	CR15-17	Diode, Si. 200mA 75V	2	1N4157 1N4148	28480	1901-0460	4
	CR18	Not Assigned		1114140	20480	1901-0050	
	CR19-21	Diode, Si, 200mA 75V		1N4148	00400	1001-0070	. ÷
1	CR22			1N4148 1N4157	28480	1901:0050	
Ι.		Stabistor, Si. 10prv 400mW	3		28480	1901-0460	_ ·
	Q1	SS NPN SI. SS NPN SI.		2N4141	28480	1854-0071	3
	Q2		8	- 40346 Curpoo	86684	1854-0095	, <b>8</b>
1	Q3	SS PNP Si	6	SJ5099	04713	1853-0038	بعب
1.	Q4,5	SS NPN SI		1 2N4141	28480	1854-0071	а. Дж.
·	Q6	SS PNP SI		SJ5099	04713	1853-0038	
· ·	07	SS NPN Si.	6	◆ 40346	86684	1854-0095	
	O8,9	SS PNP SI.	2		28480	1853-0037	.2
	Q10-12	SS NPN SI.	•	40346	86684	1854-0095	
	013	SS PNP Si.		-SJ5099 •	04713	1853-0038	
	Q14-16	SS NPN Si.		40346	86684	1854-0095	
	017-19	SS PNP SL		SJ5099	04713	1853-0038	
	R1,2	fxd, comp, 15K ±5%, ½W	2	EB-1535	01121	0686-1535	
1 1 1 A A	R3	fxd, comp, 510 ±5%, ½W	1	EB-5115	01121	0686-5115	
	R4	• Not Assigned					29
. , .	R5	fxd, comp, 3K ±5%, ½W	1	EB-3025	01121	06B6-3025	
	R6	fxd, comp, 1K ±5%, ½W	2	EB-1025	01121	0686-1025	1
	R7	fxd, comp, 1.2K.±5%, ½W	2	EB-1225	01121	0686-1225	1
I 4	R8	fxd, comp, 8.2K ±5%, ½W	5	EB-8225'	01121	0686-8225	
1 1 C	R9	fxd, comp, 750 ±5%, ½W	2	EB-7515	01/121	0686-7515	1
1.11.12	R10	fxd, comp, 6.2K ±5%, ½W		EB-6225	01121	0686-6225	
1.	R11,12 🐛	, fxd, comp, 8,2K ±5%, ½W		EB-8225	01121	0686-8225	
1	R13	fxd, comp. 1.2K ±5%, %W		EB-1225	01121	0686-1225	
				的最大的最高,有"你们就是你有了的情况"	1997 - BA		

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Table 6-4. Replaceable Parts

- REF. ' DESIG.	DESCRIPTION	τa	MFR. PART NO.	MFR. CODE	HP PART NO.	RS
	fxd, comp. 8.2K ±5%, %W		EB-8225	ੈ 01121	0686-8225	
A3R14		3	EB-5125	01121	0686-5125	3
R15	1/xd, comp. 5,1K ±5%, %₩	3	/EB-8225	01121	0686-8225	
R16	fxd, comp. 8.2K ±5%, %W	19	EB:7215	01121	0686-7515	
R17	fxd, comp, 750 ±5%, ½W,			01121	0686-1025	· •
R18	1xd, comp. 1K ±5%, %W	· ·	EB-1025	01121	0000-1025	
"R19, 20	Not Assigned	17		01121	0686-5125	
R21	fxd, comp, 5.1K ±5%, %W		EB-5125	01121	0686-1525	
• R22	fxd, comp, 1.5K ±5%, ½W	•	EB-1525		0686-5125	
'€ 'R23	fxd, comp, 5.1K ±5%, %W		EB-5125	01121	0686-5625	
1724	fxd, comp, 5.6K ±5%, %W		EB-5625	01121	0686-1015	
i R25	[xd, comp, 100 ± 5%, ½W	( <b>1</b> )	EB-1025	01121		
R26	fxd, comp, 4.3K ±5%, ½W		EB-4328	01121	0686-4325	L
R27	fxd, comp, 1.6K ±5%, ½W	1	EB-1625	01121	0686-1625	<b>[</b> ]
* R28	fxd, comp, 18K ±5%, %W	۱. <b>۲</b>	EB-1835	01121	0686-1835	1
R29	fxd, comp, 390 ±5%, ½W	18	EB-3915	01121	0686-3915	<b>1</b> 5₹4
R30	fxd, comp, 200 ±5%, %W	0. <b>1</b>	EB-2015	01121	0686-2015	
R31	fxd, comp, 10K±5%, %W	2	EB-1035	01121	0686-1035	
R32	fxd, comp, 30 ±5%, %W	3	EB-3005	01121	0686-3005	
R33	fxd, comp, 10K±5%, %W	ta sa	EB-1035	01121	0686-1035	
R34	fxd, comp, 30 ±5%, %W		EB-3005	01121	0686-3005	1.15
R35	fxd, comp, 360 ±5%, %W	3	EB-3615	01121	0686-3615	1
R36	fxd, comp, 330 ±5%, ½W	1	EB-3315	01121	0686-3315	
R37-42	fxd, comp, 39 ±5%, ½W	6	EB-3905	01121	0686-3905	16
R43,44	fxd, comp, 360 ±5%, ½W		EB-3615	01121	0686-3615	
R45-48	fxd, ww, 3 ±5%, 3W	4	242E	56289	0811-1224	
R49,50	Not Assigned			· · · ·		
	fxd; ww, 1.25 ±1%, 4W		NS-2-18	91637	0811-2556	1
R51		5				1
R52	Not Assigned		EB 3005	01121	0686-3005	
R53	,* fxd, comp, 30 ±5%, %W		EB-4705	.01121	0686-4705	1
R54	Jxd, comp, 47 ±5%, ½W		EB-1005	01121	0686-1005	
R55	fxd, comp, 10 ±5%, ½W		ED.1000		1	
R56-65	Not Assigned			01101	0686-1645	1
R66	fxd, comp, 160K ±5%, ½W	-1	EB-1645	01121	1902-0660	
VRT	Diode, zener 61.9Vdc	1	SZ11213-368			
VR2	Diode, zener 56.2Vdc	1.4	- SZ11213-356	04713	1902-0597	1.
e			6(			. A
A3	6827A Power Amplifier Plug-In Board	"		1	0100 0000	1.6
C1	fxd; elect, 20µF 15Vdc	1	.30D06G016BB2·DSM	56289	0180-0300	<b>1</b>   -
C2	fxd, elect. 1µF 35Vdc	1	150D105X9035A2	56289	0180-0291	1
-C3	fxd, mylar, .047µF 200Vdc	1	292P47352 PTS	56289	0160-0138	1
C4 12	Not Assigned					
+ C13	fxd, mylar, .01µF 400Vdc	1	_ 663UW	84411	0160-0381	1
C14	fxd mylar, .01µF 200Vdc	2	192P10392	56289	0160-0161	1
C15	fxd, elect. 5µF 150Vdc	1	40D505F150DC4	56 89	0180-1841	1
C16	fxd, mica, 150pF 300Vdc	1	RDM15F151J3C	00853	Q140-0196	1
C17-	fxd, mylar, .022µF 200Vdc	1.	19P22392	56289	6.60-0162	1 <b>1</b> 2
C18	fxd, mylar, .01µF 400Vdc	1000	192P10392	, 56289	0160-0161	
CR1,2	n Diode, Si. 200mA 75V	8	1N4148	28480	1901-0500	6
CR3;4	Diode, Si. 250mW 200prv	2	1N485B	28480	1901-0033	2,
		4	网络马克拉拉马克拉拉拉 计正式	1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1		1 . <b>1</b>
·			NY FU <u>HUNG</u> YU NG <u>NG T</u> ÌC GUIG <mark>T</mark> AIGC			الجنب الابرادية

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REF. *DESIG.	DESCRIPTION	<b>. TO</b>	MFR. PART NO.	MFR	HP PART NO.	RS
A3CR5,6	- Diode, Si. 200mA 75V		1N4148	28480	1901-0500	
- CR7-10	Not Assigned				•	
CR11,12	Stabistor, Si: 10prv 400mW	8	1N4157	28480	1901-0460	6
CR13, 14	Not Assigned			•		
CR15, 16	Stabistor, Si. 10prv 400mW	1	1N4157	.28480	1901-0460	
CR17	Not Assigned		42. 	1.1.1.1		
CR18-21	Diode, Si. 200mA 75V		1N4148	28480	1901-0500	
CR22-25 🔨	Stabistor, Sil 10prv 400mW	**	1N4157	28480	1901-0460	
Qí	SS NPN Si	3 -	2N4141	28480	1854-0071	3
Q2 · ,	SS NPN SI	2	40346	86684	1854-0095	• 2
03	SS PNP Si.	8	,SJ5099	04273	1853-0038	6
Ó4,5	SS NPN SI.	م العوار في	2N4141	28480	1854-007 <u>1</u>	
<b>O</b> 6	SS NPN SI.		40346	\$6684	1854-0095	
Q7	SS PNP SI.		SJ5099	04713	1853-0038	5. A <b>1</b> 7
08	SS NPD SI.	11	SJ1679	04713		
(09,10	SS PNP SI	•	SJ5099	04713	-1853-0038	
01 12	SS NPN Si.	6	MM2258	04713	1854-0271 ,	•6
Q13116	SS PNP SI		SJ5099	04713	1853-0038	<b>.</b>
Q17-20	SS'NPN Si.		MM2258	04713	1854-0271	
R1.2	fxd; comp, 15K:±5%, ½W	_ <b>2</b> _ ₹	EB-1535	01121	0686-1535	1
R3	fxd, comp, 510 ±5%, ½W	1	EB-5115	01121	0686-5115	• 1•
R4	Not Assigned	1997 <b>- 1</b> 99				
r R5	fxd, comp, 3K ±5%, ½W-	1	EB-3025	01121	0686-3025	1
R6-20	Not Assigned				и 11 Ц Ц	
R21	fxd, comp, 5.1K #5%, %W	: 3	EB-5125	01121	0686-5125	. 3
R22	fxd, comp, 1.5K ±5%, ½W	1	EB-1525 1		0686-1525	1
,R23	fxd, comp, 5.1K ±5%, ½W		EB 5125	01121	0686 5125	1
Ŕ24	fxd, comp, 5.6K ±5%, ½W	1.	EB-5625	01121	0686-5625~	1
R25	fxd, comp, 100 ±5%, ½W	<b>2</b>	EB-1015	01121		.2
R26	fxd, comp, 4.3K ±5%, ½W	<del>,</del> 1	EB-4325		0686-4325	( 1
R27	fxd, comp, 10K±5%, ½We	1	EB-1035	01121	0686-1035	1
R28	• fxd, comp, 820 ±5%, ½W	1	EB-8215	01121	0686-8215	1
R29	fxd, comp, 5.1K,±5%) ½W	1	EB 5125	01121	0686-5125	
• R30	fxd, comp, 1.2K ±5%, %W	1	EB-1225	01121	0686-1225	• 1~ 1
R31	fxd, comp, 910 ±5%, ½W	1	EB 9115	01121	0680 9115	$ 1_{i_{i_{i_{i_{i_{i_{i_{i_{i_{i_{i_{i_{i_$
R32	fxd, metal oxide, 36K ±5%, 2W.	1	RG-42	11502	0698-3651	
R33	fxd, comp, 2K ±5%, 1W	1	GB-2025	01121	0689-2025	1
R34	fxd, comp, 300 ±5%, ½W	1	EB-3015	01121	0686-3015	
R35. /	fxd, comp, 180 ±5%, ½W,	3	EB-1815	01121	0686-1815	3
R36	fxd, comp, 750 ±5%, ½W	1	EB-7515	01121	0686-7515	1.
R37	fxd, comp, 100 ±5%, ½W		EB-1015	01121	0686-1015	
R38	fxd, comp, 1K ±5%, ½W	1	EB4025	01121	0686 1025	1
R39	fxd/comp, 240 ±5%, ½W	1	EB 2415	01121	0686-2415	1
R40 /	fxd/ comp, 39 ±6%, ½W	1	EB 1905	01121	.0686-3905	J
R41'	fxd, comp 360,±5%, ½W	1	EB-3615	01121	0686-3615	
R42/	.fxd, metal oxide, 33K, 2W	2	Type C42S	16299	0764-0046	
R43	fxt, metal oxide, 47K, 2W	5	• Type C42S	16299	0764-0031	. <b>I</b> .
R44	fxd, comp, 160K ±5%, ¼W	2	EB-1645	01121	0686-1645	
• • <b>R45</b>	fxd, metal oxide, 47K, 2W		Type C42S	16299•	0764-0031	
	en al connected a la contra factorizada en la parte a Cherrera de la c	- <b>M</b> -2012	[14:19] 20:19 - 19:19 - 19:19 - 19:19 - 19:19 - 19:19 - 19:19	- 10 C (20 )		L 19 - C.

REF. DESIG.	DESCRIPTION	TQ	MFR. PART NO	MFR. CODE	HP PART NO,	F
3R46	fxd, metal oxide, 22K, 2W	3	Type C42S	16299	0764-0045	5
	fxd, comp, 200 ±5%, ¼W	4	EB-2015	01121	0686-2015	
R47	fxd, comp, TB0 ±5%, %W		EB-1815	01121	0686-1815	
R48	fxd, metal oxide, 47K, 2W		Type C42S	16299	0764-0031	2
R49	fxd, comp, 82,±5%; /2W	2	EB-8205	01121	0686-8205	1. 1.
R50	fxd, comp, 200 ±5%, %W		EB-2015	, 01121	0686 2015	13
R51	fxd, comp, 82 ±5%, %W	•	EB-8205	01121	0686-8205	;
R52.	fxd, comp, 180 ±5%, %W	<b>R</b> 2	EB-1815	01121	0686-1815	
R53-	+ (xd, ww, 2.7 ±5%, 2W	2	Type BWH	.07.716	0811-1671	
R54	fxd, metal oxide, 22K, 2W	$O_{X,Y}$	Type C42S	16299	0764-0045	- H
R55	fxd, metal oxide, 47K, 2W	10.15	Type C425	16299	0764-0031	
R56 57	fxd, metal oxide, 33K, 2W		Type C42S	16299	0764-0046	· ľ
R58			EB-8235	01121	0686-8235	
R59	fxd, comp, 82K ±5%, %W		EB-2015	01121	0686-2015	
8 RGD 🦾 🕴	fxd, comp, 200 ±5% /2W	2	EB-2705	01121	0686-2705	
R61	fxd, comp <sup>1</sup> -27 ±5%, %W		Type C42S	16299.	0764-0045	
-R62	fxd, metal oxide, 22K, 2W		EB-2015	01121	0686-2015	
R63 -	fxd, comp, 200 ±5%, ½W		Type BWH	.07716	0811-1671	
RG4	fxd, wwj-2.7 ±5% 2W :	4	EB-2705	01121	0686-2705	•
R65	fxd, comp; 27 ±5%, %W	£	EB-1645 (	01121.	0686-1645	
R66 👋 👌	fxd, comp, 160K ±5%, %W		ED-1040		1. S. A. S.	•
R67-69	Not Assigned	- A. 3	<b>FD 47CE</b>	101121	0698-0001	
R70	fxd, comp. 4.7 ±5%, 42W		EB-47G5			1
VR1,2	Not Assigned	: ::- ·		28480	1902-0184	1
VR3	Diode, zener 16.2V 400mW		1N966	04713	1902-0182	
VR4	Diode, zener 20.5V 400mW	1	SZ10939-272	04713	1902-0597	
VR5	Diode, zener 56.2V 1W	2	SZ11213-356	04713	1902-0660	- <b>1</b>
VR6,7	Diode, zener 61.9V	- 2	SZ11213-368	04713	1902-0597	
VR8	Diode, zener 56.2V 1W		SZ11213-356	047.13	1995 0991	
<u>Alexisten (m. 1996)</u> Alexisten (m. 1996) Alexisten (m. 1996)	Power Module lindudes slide, switch			28480	5060-1189	
A4	and fuse)					
	Fuse, 2A 250V Sto Blo		MDX-2A	71400	2110-0303	4
6 F1 - 14 F2					en e	1.3 2.5
A5	Front Panel - Electrical					
DS1					0140 0027	
6826A	IndicatorsLamp (LINE)	2021		28480	31 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
6827A	Indicator Lamp (LINE)			28480		1.34
DS2	Indicator, Light Emitting	91 (t		28480	1990-0325	
D02	Diode (CURRENT-MODE)				1828년 22	1
김 작품 공공을 수	4. 图1. 通知的 网络马马马马马马马马马马马马马马马马马马马马马马马马马马马马马马马马马马马马					
M1	Voltmeter, Dual Range DC or AC	1		28480	1120-1370	1.
6826A	(±6, ±60Vdc or 4, 40V rms)					Т. Ц
A0074	Voltmeter, Dual Range DC or AC	1		28480	1120-1372	213 201
6 18827A	(±12, ±120Vdc or 8, 80V rps)	2 <b>1</b> 2				: • • • •
		' <b> </b> ' i			6 P.C. N	
M2.		े <b>।</b>		28480	1120-137	3
6826A	Ammeter, Dual Range DC or AC					1
	(±0.12, ±1.2A or 0.08, 0.8A rms)	<b>.</b>		28480	1120 136	9
6827A	Ammeter, Dual Range DC of AG	- I -		副語動的	研究したと	
en tukar	(±0.06, ±0.6A or 0.04, 0.4A rms)		방법에 이번 것 같아. 소리 관계에 가격 수 있는 것이 같아.			1

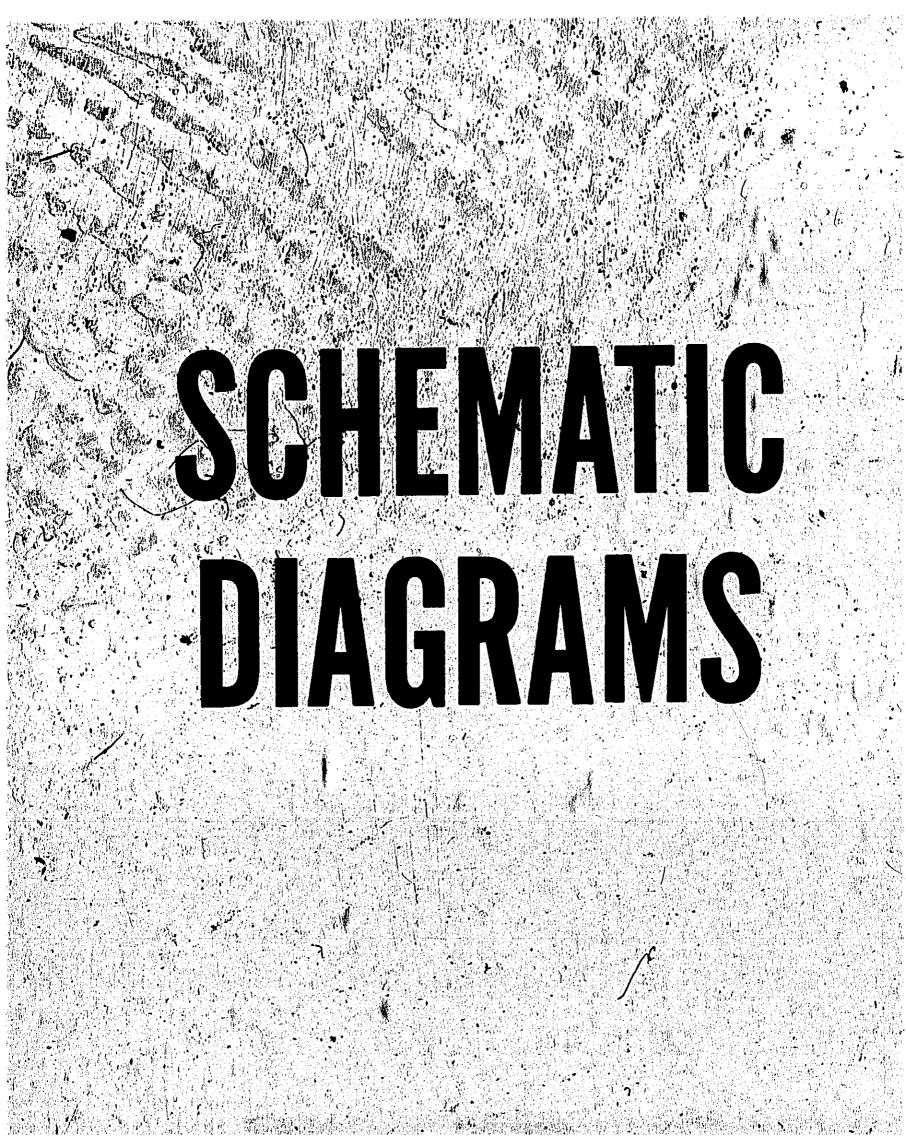
REF. DESIG.	DESCRIPTIÓN	тα	MFR. PART NO	MFR. CODE	HP PART NO,	R
A5B1 R2 S1 S2	var, ww. dual ganged 15K-15K (CURRENT Control) var, ww. 25K (VOLTAGE Control) Switch, Toggle SPDT 5A (LINE Switch) Switch, Rotary, 3 Sections (RANGE/MODE Selection Switch)			28480 28480 28480 28480	2100-3271 2100 3272 3101-1605 3100-1942	
A6 A1-4 O5,6 6826A 6827A4	Heat Sink Assembly – Electrical Power NPN Si Not Used Power NPN Si.	4	60128 C	86684 \$6684	1854-0421 1854-0421	4 2
R1-3 6826A 6827A W2 TB1-3 W1	Not Used fxd, ww2.7 ±5%, 2W Ribbon Cable Assembly Terminal Block Ribbon Cable Assembly	1 3 1	Туре ВШН	, 07716 28480 28480 28480 28480	5060-9662	
C1,2 C3 6826A .6827A T1 6826A 6827A	Chassis – Electrical fxd, mylar 1µF 220Vac fxd, cer., .1µF 500V Nor Used Transformer, Power Transformer, Power	2	439P1059220 41C92B5-CDH	56289 28480	0160-3679 0160-0269 06826-80091 06827-80091	
	A1-Interconnect and Power Supply Board — Mechanical Heat Dissipator (Q1, Q3)	-12	NF:207	05820	1205-0033	
	A3 Power Amplifier Board Mechanical Heat Dissipator 6826A (Q6, Q9:Q11) 6827Å (Q13:Q20) Heat Dissipator 6826A (Q14-Q19) ;	<b>4</b> 8	NF-207 NF-207 2227-B	0 <b>982</b> 0 05820 13103	1205-0033 1205-0033 1205-0206	
	Heat Sink Assembly – Mechanical 4 Bushing Insulator 6826A (Q1-Q4) 6827A (Q1-Q6) Insulator, Micer 6826A (Q1-Q4) 6827A (Q1-Q6)	4		28480 28480		
	Chassis Rear Heat Sink			28480 28480		

6-1

REF. DESIG	DESCRIPTION	то	MFR. PART NO.	MFR. CODE	HP PART NO.	F
	Heat Sink Assembly (Continued)					
	Barrier Strip Board Assembly	[ ]		28480	5060 9663	
	Heat Sink Board Assembly			1.		<u> </u>
	682GA	1.1		28480	06826 60023	
	6827A	1		28480	06827-60023	ŀ
• <b>•</b> •		- 61				_
	Front Panel - Mechanical			· · · ·		-
	Bezel, Meteri	2		28480	4040,0483	
1. Sec. 1	Control Panel		的复数形式 网络马克斯马尔			
	6826A • / 🥠 🔭				06826 60001	E
	6827A 7				06827 60001	
t de la service de la servi	Output Panel	1		{ 28480		1
pu' (	Foot Assembly.	1		28480		
	Insulator, Binding Post Black)	2	O MARIA DA CARA	28480		2
e et l'égé fair d'	Insulator, Binding Post (Red)	8		28480	0340-0734	
	Knob (VOLTAGE Control, CURRENT	$\mathbb{N}^{+}$				١.
• • •	Control, VOLTAGE METER	15				ľ
	switch, CURRENT METER switch		an and the Million States			ľ
- <b>- - - -</b>	Knob (MODE select)	1		28480	0370-1100	
		81	4	28480	0370-1125	
n	Knob (RAN E select)			5		
•	Collar, LED (CURRENT MODE indi	4		28480	1400-0825	
	cator)-	4		28480		
	Lamp Holder, Clear			28480		l
	Lamp Holder, Black			28480		
	Binding Post (Red)	4		28480	1 A	4
	Binding Post/(Black) 'Tilt Stand	<b>%</b> 1		28480	474 - I I I I I I I I I I I I I	
	and a second					2 2 2 2
승규는 영국 🌔 영국	Chassis – Mechanical			28480	5000-3090	
	Cover, Ton			28480		
	-Cover, Bottom			28480		
	Cover, See	T 2	性的问题,自然的问题是自己的情况。 我们就是一次是一次,我们还是是是不是不是不是不是不是	28480	1. A start of the start of t	1
	Chassis			28480	1	
	Rear Panel, Bottom			28480	Fight and the barrier of the second secon	1
	Foot Assembly	1		28480		š
는 물질 것은 아이지? 1997년 1월 20일 - 1997년 1월 1997년 1월 20일 - 1997년 1	Jumper, Barrier Strip	9				
	Standoff 8-32 × 875	<b>4</b> 2		28480		
	Cover Barrier Strip	<b>] D</b>		28480	and the second states of the	- L'
	6 x 11 Frame Assembly	-2		28480	5060-0703	
		<u> </u>			Normal States and States	7
	Miscellaneous			71400	2110-0007	
	Fuse 1A 250V Slo-Blo	18	MDX-1A		[1] M. M. K. K. K. M.	3 - T
	Power Card	1 1	目标的新闻的复数分词	28480		- E.
	Packing Carton	<b>이 1</b> 종		28480		- 1 E
	Floater Pad, Carton			28480	9220-1409	ľ
		214 104 2142 10	■ このの目的になるのでありの思い。			
	Option 007:			28480	2100,1867	
	10-Turn VOLTAGE Control				A Star	
	var. ww 20K ±5%, 2W, linear, 10-turn	•	161222 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 - 1612 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 1 16122 - 1612 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 1612 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 1612 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 16122 - 1612 1712 - 1612	1.1	<b>电话和</b> 在我们	j li
para ng akaragi	knob A	21 C B	1. 이슈킹 : 그렇는 데 요즘 말 하나 같아?	5 K - S - C - C - C - C - C - C - C - C - C	「「「「「「」」	1

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Table 6-4. Replaceable Parts



#### SECTION VII CIRCUIT DIAGRAMS

## 7-1 INTRODUCTION

7.2 This section contains the circuit diagrams necessary for the operation and maintenance of BPS/A Models 6826A and 6827A.

### 7-3 SIMPLIFIED SCHEMATIC DIAGRAM

7.4. This diagram, Figure 7.1, shows the relationship between the instrument assemblies and ties the schematic diagram sheets together.

#### 7-5 COMPONENT LOCATION ILLUSTRATIONS

7.6 The component location diagrams show the physical, location of parts mounted on each assembly. They are included on the schematic diagrams where they apply of on the rear of the previous schematic. Thus, the schematic diagram is unfolded to the right and component location diagram is unfolded to the left,

## 7-7 SCHEMATIC DIAGRAMS

7-8 Separate schematic diagrams are provided for Models 6826A and 6827A as follows:

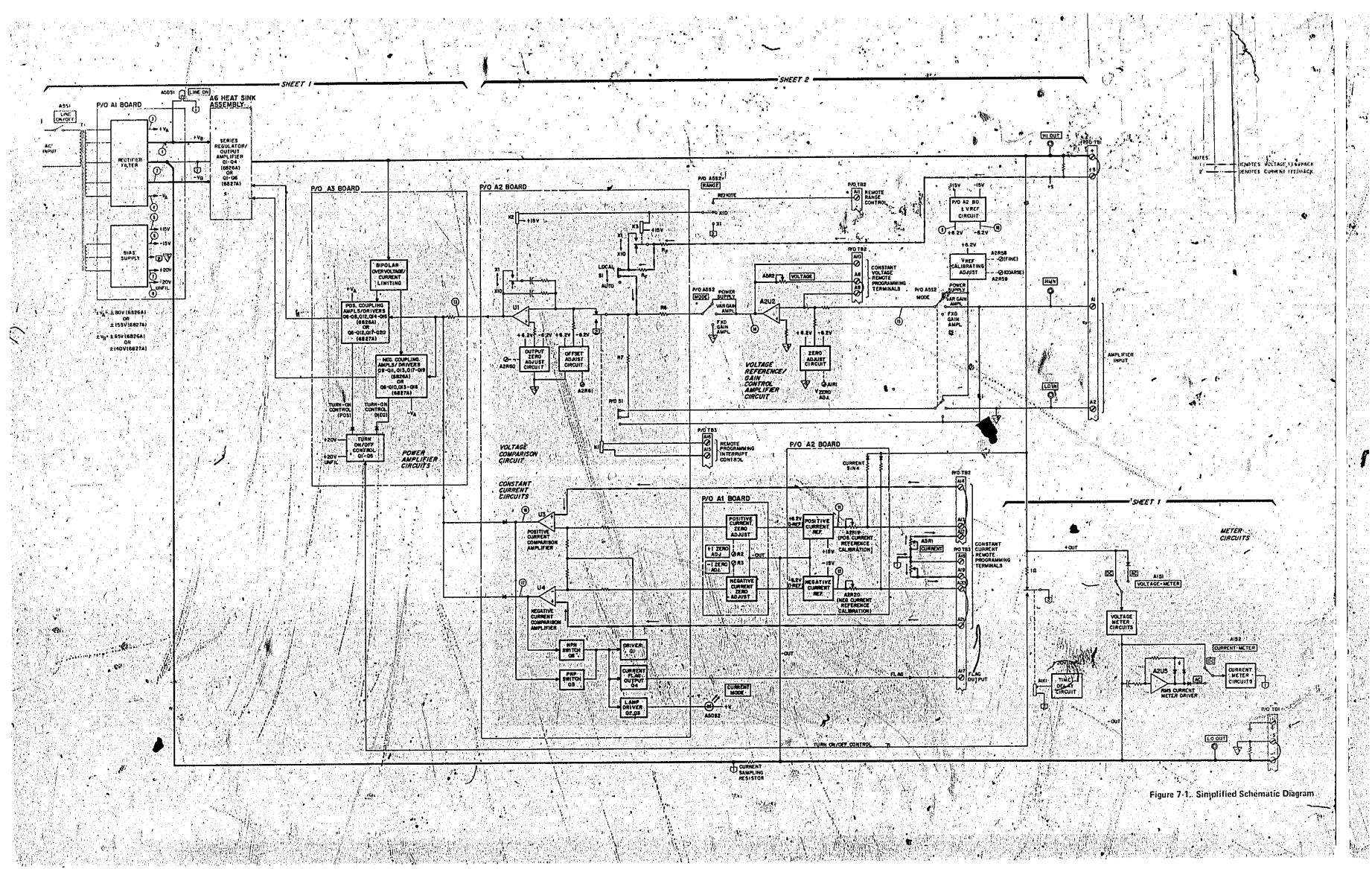
Figure 7-2 (Sheet /), Model 6826A Output Power Amplifier Circuits, Schymatic Diagram

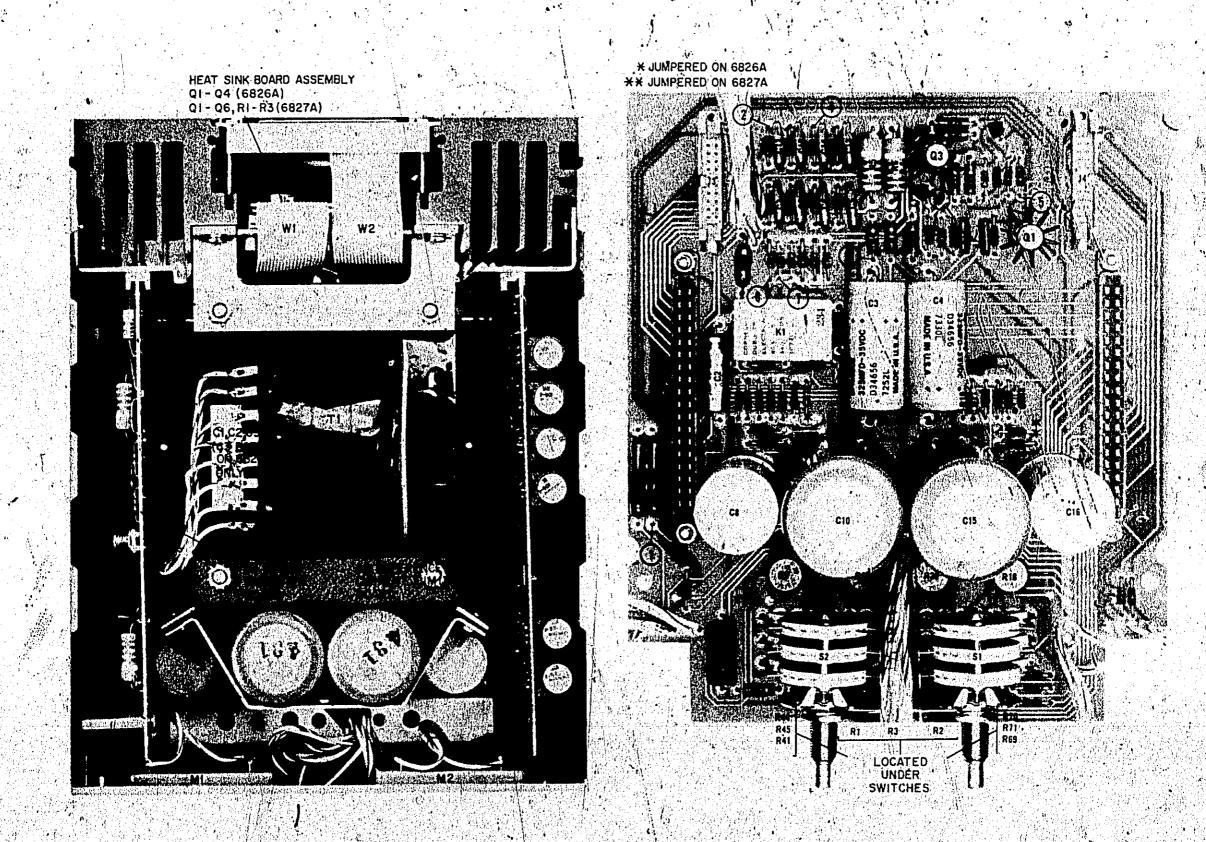
Figure 7-2 (Siley, 2). Model 6826A Voltage and Current Control Circuits Schematic Diagram

Figure 7-3 (S/eer, 1), Model 6827A Output Power Amplifier Circuits, Schematic Diagram

Figure 7-3 (Sheet 2), Model 6827A Voltage and Current Control Circuits, Schematic Diagram

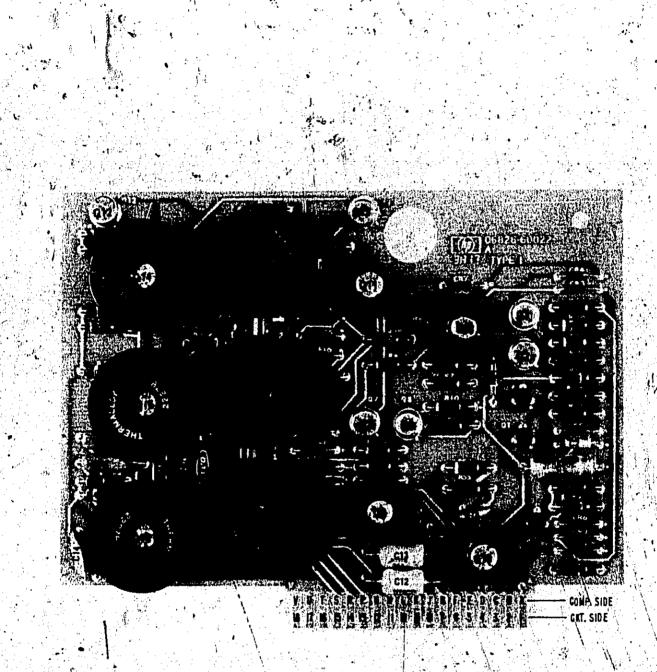
7.9 Test points (encircled numbers) appear on the schematics. These points coincide with the test points on the component location diagrams and are referred to in the text.





Top View, Component Location

A1 Power Supply and Interconnect Board, Component Location



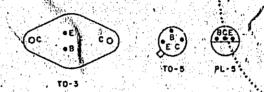
A3 Amplifier Board (6826A only), Component Location

NOTES: 1. ALL RESISTORS ARE IN OHNS, 1/2W, 5% UNLESS OTHERWISE INDICATED.

2. ALL I/BW AND 1/4W RESISTORS ARE IN UNLESS OTHERWISE INDICATED

S. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE INDICATED

A CON DENOTES FRONT PANEL MARKING.



(ALL BOTTOM VIEWS)

8, PIN LOCATIONS FOR INTEGRATED CIRCUITS A2UI A2US ARE AS FOLLOWS: "

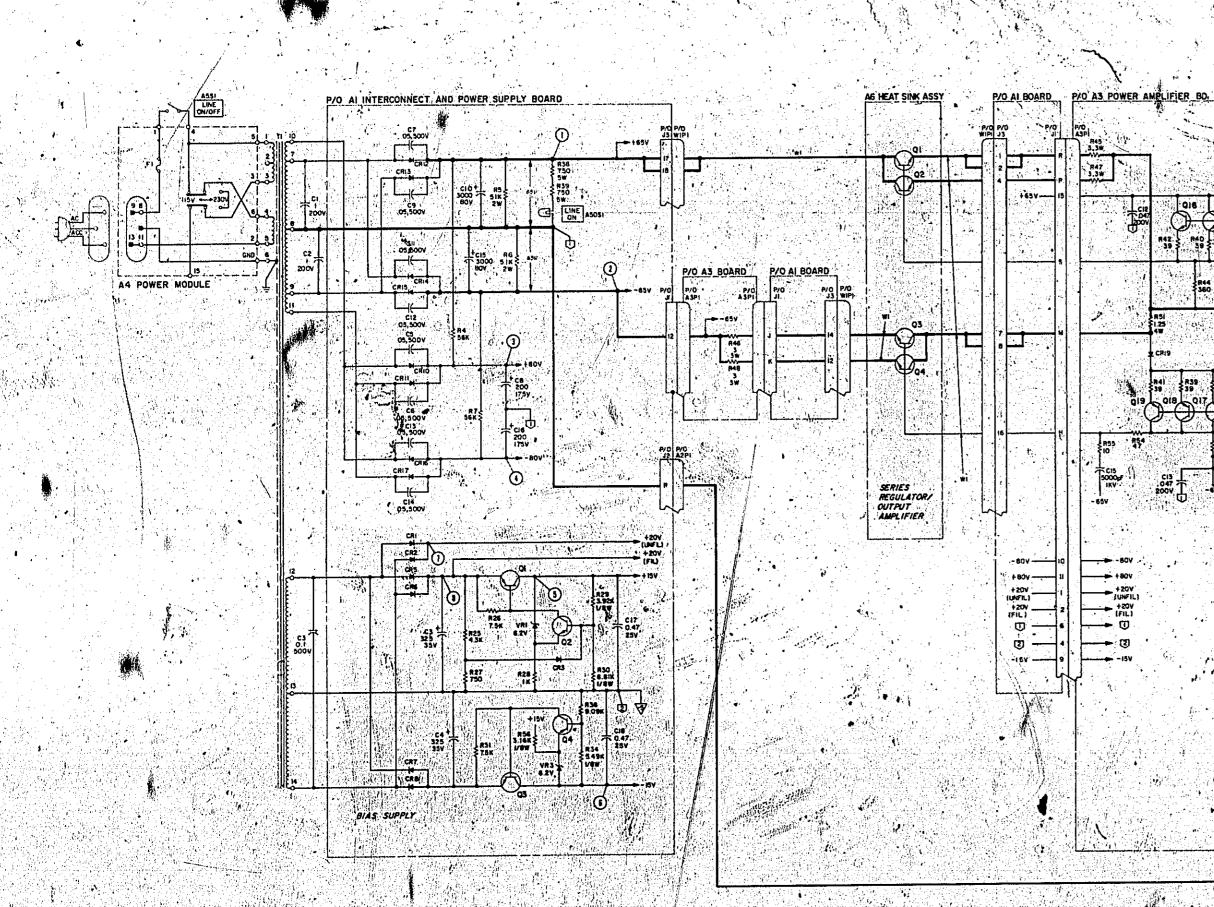


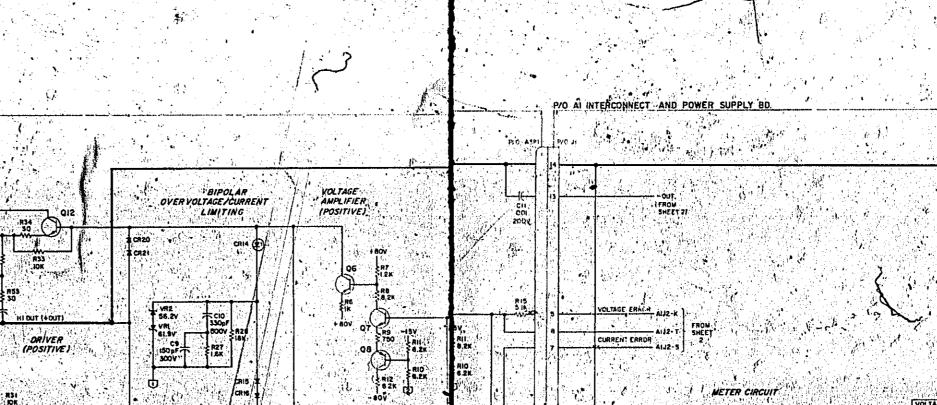
(BOTTOM VIEW)

9. THE LOCAL/AUTO SWITCH, LOCATED INSIDE THE UNIT ON BOARD 42, IS ACCESSIBLE BY REMOVING THE RIGHT SIDE COVER OR TOP COVER, FOR NORMAL OPERATION, THE SWITCH MUST BE LEFT IN THE LOCAL POSITION (PUSHED TO THE RIGHT OR TO REAR OF UNIT) THE AUTO POSITION (PUSHED TO LEFT OR FRONT OF UNIT) IS USED ONLY DURING AUTO-SERIES OR AUTO-PARALLEL OPERATION OF TWO OR MORE UNITS.

655

IO JUMPER AZWI IS REMOVED FOR OPEN COLLECTOR OPERATION. NOTE THAT THIS CIRCUIT





25 R31 60 IDR R32 30

ORIVER (NEGATIVE)

#30 200

R29 390

TURN-ON CONTROL (POS) ----

824 5.6K

A23

#2 15K

TURN ON/OFF

866 160K

13 A (1)

CRI # # CR2 120V +20V (UNFIL) (FIL)

R25

TURN ON CONTROL (NES)-

+20V (FIL) R122 03 1.5M

HIS EI 2K

1 AM \$ 0.2K

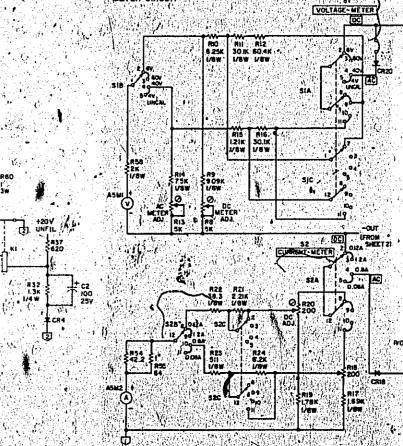
2 R(7 2 750

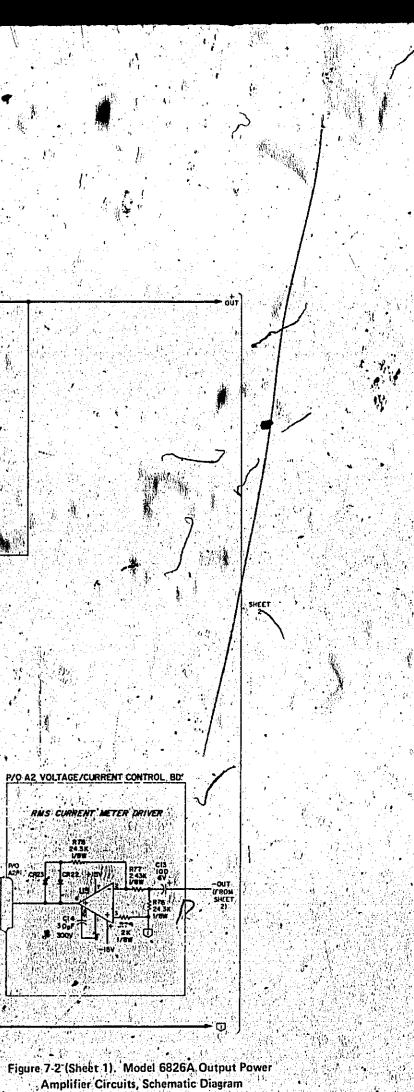
**₹**₽18 **₹**ΙΚ

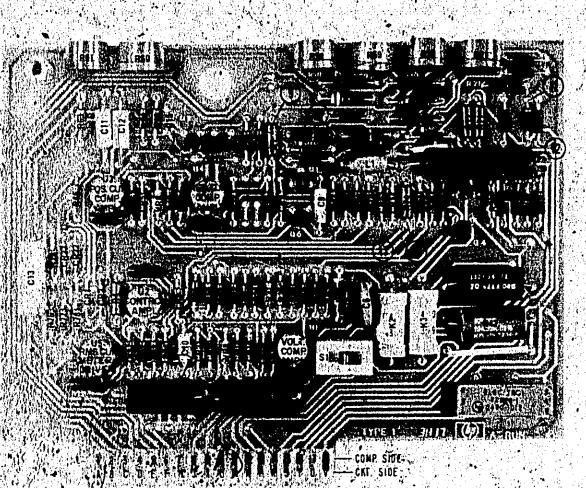
VOLTAGE

142; 39

\$839 \$39

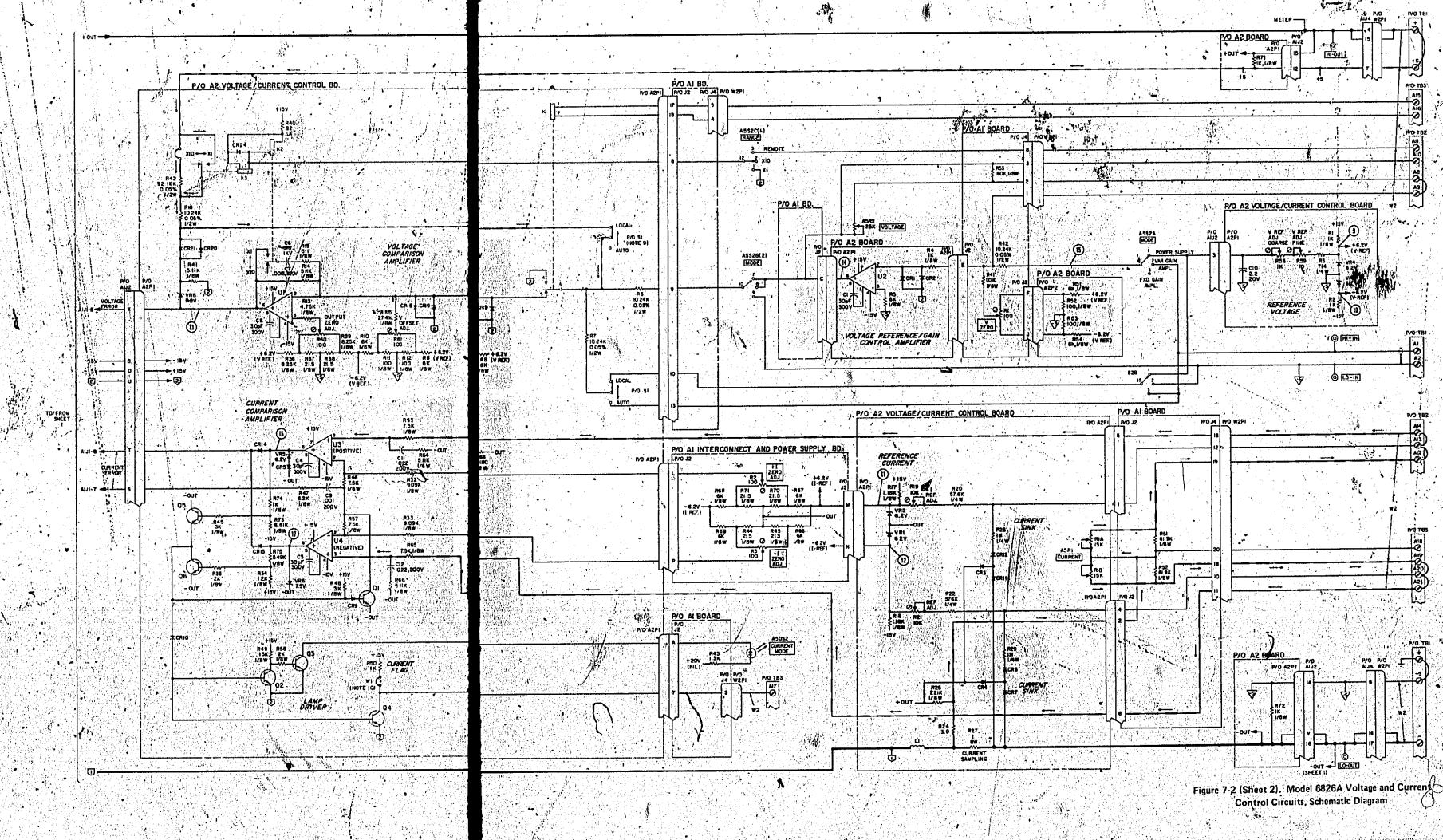






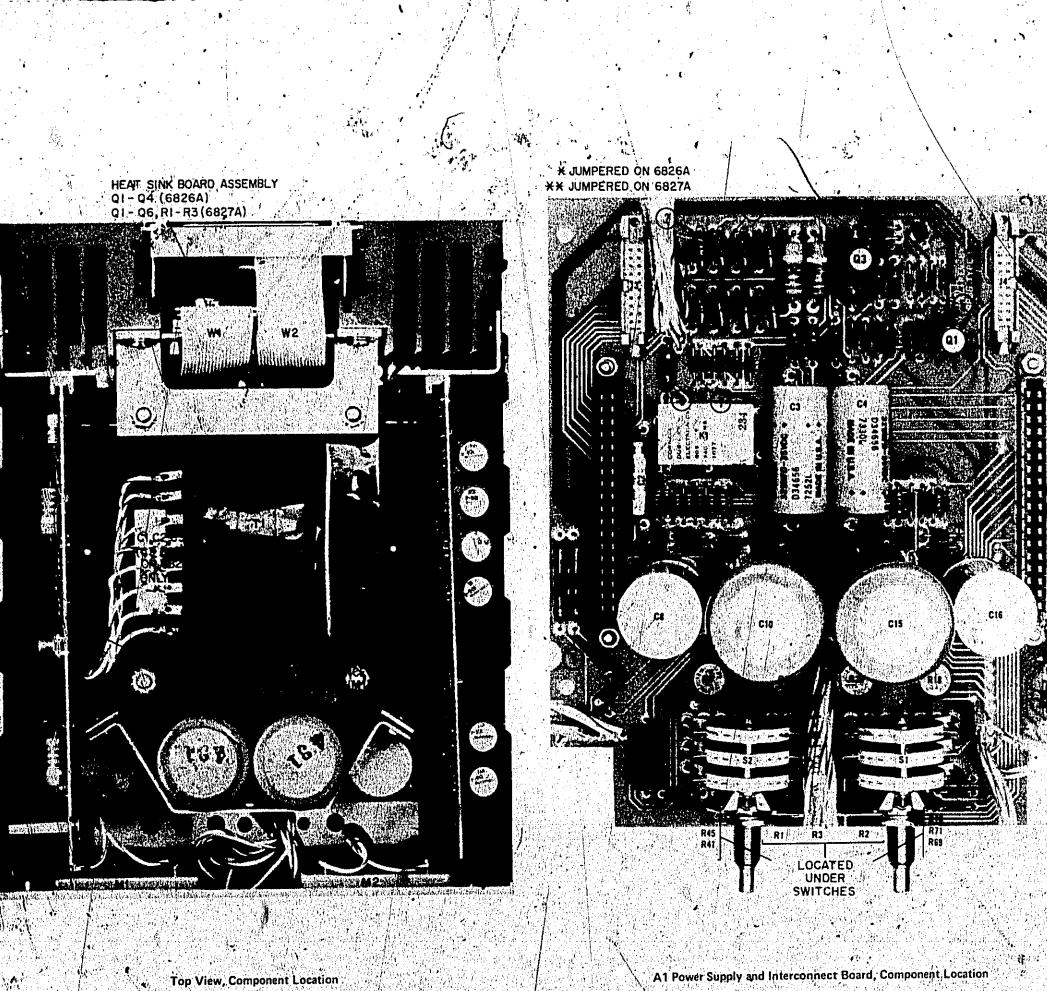
A JUMPLRED ON 68264

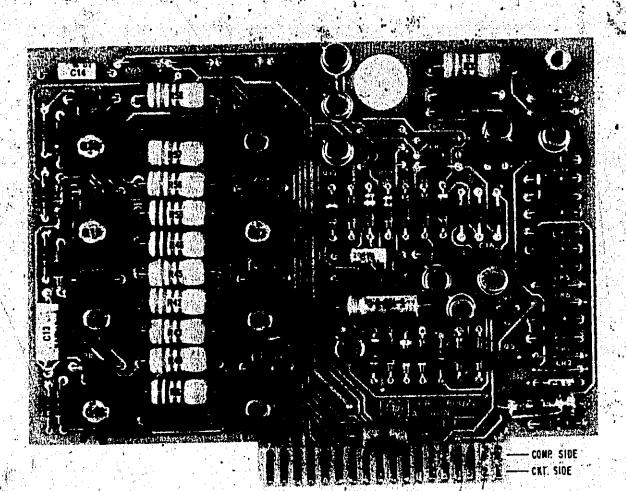
A2 Voltage and Current Control Board, Component Location



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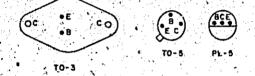


A3 Amplifier Board (6827A only), Component Location

## NOTES: 1. ALL RESISTORS ARE IN OHMS, 1/24, 5% UNLESS OTHERWISE INDICATED.

- 2. ALL 1/8W AND 1/4W RESISTORS ARE 1%, UNLESS OTHERWISE INDICATED
- 3. ALL CAPACITORS ARE IN MICROFARADS, UNLESS OTHERWISE INDICATED
- 4. C DENOTES FRONT PANEL MARKING.
- 5. ----- DENOTES VOLTAGE FEEDBACK
- 6. DENOTES CURRENT FEEDBACK

Z PIN LOCATIONS FOR TRANSISTORS ARE AS FOLLOWS



## (ALL BOTTOM VIEWS)

8. PIN LOCATIONS FOR INTEGRATED CIRCUITS A201-A205 ARE AS FOLLOWS:

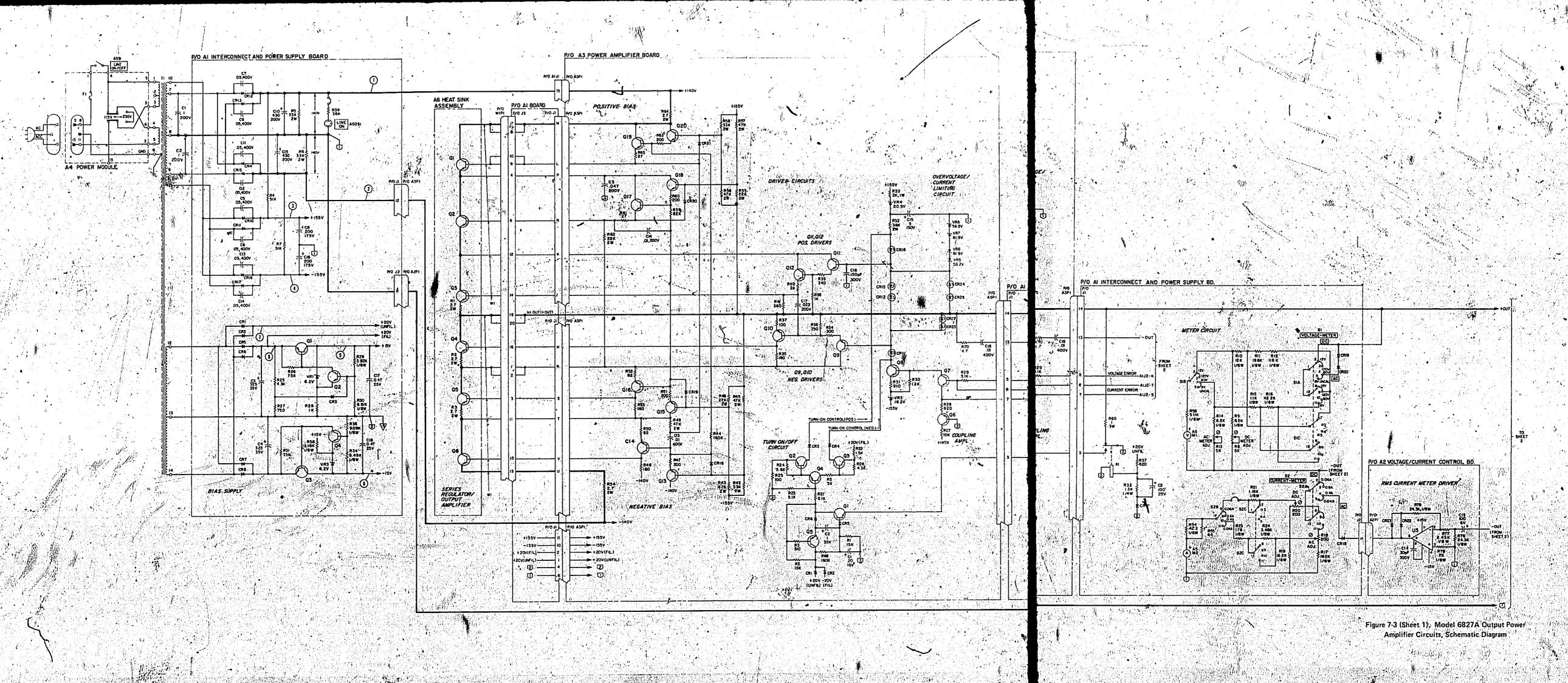


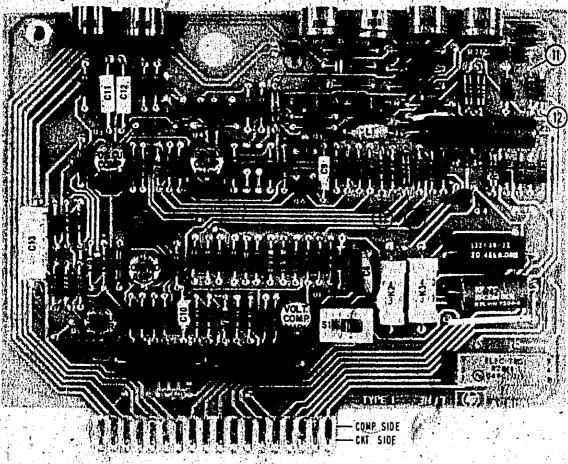
## (BOTTOM VIEW)

9. THE LOCAL/AUTO SWITCH, LOCATED INSIDE THE UNIT ON BOARD A2, IS ACCESSIBLE BY REMOVING THE RIGHT SIDE COVER OR TOP COVER, FOR NORMAL OF RATION, THE SWITCH MUST BE LEFT IN THE LOCAL POSITION (PUSHED TO THE RIGHT OR TO REAR OF UNIT). THE AUTO POSITION (PUSHED TO LEFT OR FRONT OF UNIT) IS USED ONLY DURING AUTO-SERIES OR AUTO-PARALLEL OPERATION OF TWO OR MORE UNITS.

10. JUMPER AZWI IS REMOVED FOR OPERATION OPERATION. NOTE THAT THIS CIRCUIT IS REFERENCED TO (2) COMMON WHICH IS CONNECTED TO THE -S TERMINAL.

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\* JUMPERED, ON 6826A

A2 Voltage and Current Control Board, Component Location

